ISR201

Ruggedized Embedded System

with NXP ARM® Cortex®
A9 i.MX6 Dual SoC

User's Manual

Version 1.0
(Aug. 2018)

iBASE

Copyright

Disclaimer

IBASE reserves the right to make changes and improvements to the products described in this document without prior notice. Every effort has been made to ensure the information in the document is correct; however, IBASE does not guarantee this document is error-free. IBASE assumes no liability for incidental or consequential damages arising from misuse or inability to use the product or the information contained herein, and for any infringements of rights of third parties, which may result from its use.

Trademarks

All the trademarks, registrations and brands mentioned herein are used for identification purposes only and may be trademarks and/or registered trademarks of their respective owners.

Compliance

CE

The product described in this manual complies with all applicable European Union (CE) directives if it has a CE marking. For systems to remain CE compliant, only CE-compliant parts may be used. Maintaining CE compliance also requires proper cable and cabling techniques.

FC

This product has been tested and found to comply with the limits for a Class B device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with manufacturer's instructions, may cause harmful interference to radio communications.

WEEE

This product must not be disposed of as normal household waste, in accordance with the EU directive of for waste electrical and electronic equipment (WEEE - 2012/19/EU). Instead, it should be disposed of by returning it to a municipal recycling collection point. Check local regulations for disposal of electronic products.

Green IBASE

This product complies with the current RoHS directives restricting the use of the following substances in concentrations not to exceed 0.1% by weight (1000 ppm) except for cadmium, limited to 0.01% by weight (100 ppm).

- Lead (Pb)
- Mercury (Hg)
- Cadmium (Cd)
- Hexavalent chromium (Cr6+)
- Polybrominated biphenyls (PBB)
- Polybrominated diphenyl ether (PBDE)

iBASE

Important Safety Information

Carefully read the following safety information before using this device.

Setting up your system:

- Put the device horizontally on a stable and solid surface.
- Do not use this product near water or any heated source.
- Leave plenty of space around the device and do not block the ventilation openings. Never drop or insert any objects of any kind into the openings.
- Use this product in environments with ambient temperatures between 0˚C and 60˚C.

Care during use:

- Do not place heavy objects on the top of the device.
- Make sure to connect the correct voltage to the device. Failure to supply the correct voltage could damage the unit.
- Do not walk on the power cord or allow anything to rest on it.
- If you use an extension cord, make sure the total ampere rating of all devices plugged into the extension cord does not cord's ampere rating.
- Do not spill water or any other liquids on your device.
- Always unplug the power cord from the wall outlet before cleaning the device.
- Only use neutral cleaning agents to clean the device.
- Vacuum dust and particles from the vents by using a computer vacuum cleaner.

Product Disassembly

Do not try to repair, disassemble, or make modifications to the device. Doing so will void the warranty and may result in damage to the product or personal injury.

CAUTION

Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries by observing local regulations.

Warranty Policy

- **IBASE standard products:**

  24-month (2-year) warranty from the date of shipment. If the date of shipment cannot be ascertained, the product serial numbers can be used to determine the approximate shipping date.

- **3rd-party parts:**

  12-month (1-year) warranty from delivery for 3rd-party parts that are not manufactured by IBASE, such as CPU, CPU cooler, memory, storage devices, power adaptor, display panel and touch screen.

* PRODUCTS, HOWEVER, THAT FAIL DUE TO MISUSE, ACCIDENT, IMPROPER INSTALLATION OR UNAUTHORIZED REPAIR SHALL BE TREATED AS OUT OF WARRANTY AND CUSTOMERS SHALL BE BILLED FOR REPAIR AND SHIPPING CHARGES.

Technical Support & Services

1. Visit the IBASE website at www.ibase.com.tw to find the latest information about the product.
2. If you encounter any technical problems and require assistance from your distributor or sales representative, please prepare and send the following information:
   - Product model name
   - Product serial number
   - Detailed description of the problem
   - Error messages in text or screenshots if any
   - The arrangement of the peripherals
   - Software used (such as OS and application software)
3. If repair service is required, please download the RMA form at http://www.ibase.com.tw/english/Supports/RMAService/.
   Fill out the form and contact your distributor or sales representative.

iBASE

Table of Contents

Chapter 1
General Information

The information provided in this chapter includes:

- Features
- Packing List
- Optional Accessories
- Specifications
- Overview
- Dimensions

1.1 Introduction

ISR201 is an ARM®-based embedded system with NXP Cortex® i.MX6 A9 processor. The device offers 2D, 3D graphics and multimedia accelerations while it also features numerous peripherals that are well suited for industrial applications, including RS-232/422/485, GPIO, USB, USB OTG, LAN, HDMI for a full HD display, M.2 E2230 for wireless connectivity and mini-PCIe for expansion.

1.2 Features

- NXP ARM® Cortex® A9 i.MX6 Dual 1 GHz processor
- Video output through HDMI
- 1 GB DDR3, 4 GB eMMC and SD socket
- Rich I/O interface with RS-232/422/485 GPIO, USB, USB-OTG, and Ethernet
- M.2 E2230 & mini-PCIe (USB only) with the SIM socket for wireless connectivity
- 2 x 2 wire UART headers
- OpenGL ES 2.0 for 3D BitBlt for 2D and OPENVG1.1 hardware accelerators

1.3 Packing List

Your product package should include the items listed below. If any of the item below is missing, contact the distributor or the dealer from whom you have purchased the product. Drivers and this user manual are downloadable from our website.

- ISR201

1.4 Optional Accessories

IBASE provides optional accessories as listed below. Please contact us or your dealer if you would like to order any item.

- Power Adapter
- WiFi & BT Combo (M.2 card)
- USB OTG Cable (USB-81)
- VESA Mounting Kit (75 x 75 / 100 x 100)
- DIN Rail Mounting Kit

1.5 Specifications

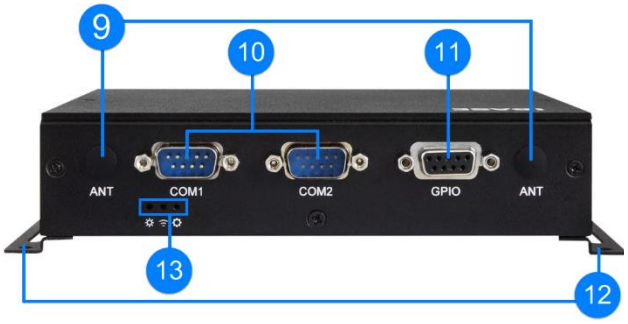
Product Name	ISR201
System Motherboard	IBR117 3.5” SBC
System	
Operating System	• Yocto (Kernel 4.1) • Android 6.0
CPU Type	NXP Cortex™ A9 i.MX6 Dual Core 1 GHz SoC
CPU Speed	1 GHz
Memory	• **System memory:** 1 GB DDR3 • **Data Memory:** 4 GB eMMC
Video Codec	• **Encoder:** 1080p, 30 fps (MPEG-4 SP, H.264 BP, H.263, MJPEG BP) • **Decoder:** 1080p, 30 fps (MPEG-4 ASP, H.264 HP, MPEG-2 MP, MJPEG BP)
Touch	USB headers for CPT
RTC	AnalogTEK AT8565S
Wireless	WiFi / BT / 3G / LTE module (Optional)
Power Supply	12V DC-In
Watchdog Timer	Yes (256 segments, 0, 1, 2…128 secs)
Dimensions (W x H x D)	172 x 36 x 162 mm (6.77” x 1.42” x 6.38”)
RoHS	Yes
Certification	CE, FCC Class B
I/O Ports	
DC Jack	1 x 12V DC jack
Display	1 x HDMI 1.4 (up to 1080P30 at 60 Hz)
LAN	1 x RJ45 GbE LAN
USB	• 2 x USB 2.0 Type A • 1 x USB OTG via mini-USB Type B
Serial	• 1 x COM RS-232/422/485 port (DB9 male connector) • 2 x COM RS-232 ports (DB9 male connector)
Digital IO	8-In/Out
Expansion Slots	• 1 x M.2 E-key (2230) with USB, SDIO and UART • 1 x Mini-PCIe with USB only (interrelated with the SIM socket) • 2 x CAN bus (6-pin header)

Environment	
Operating Temperature	0 ~ 60 °C (32 ~ 140 °F)
Relative Humidity	10 ~ 90 %, non-condensing

All specifications are subject to change without prior notice.

1.6 Overview

I/O View



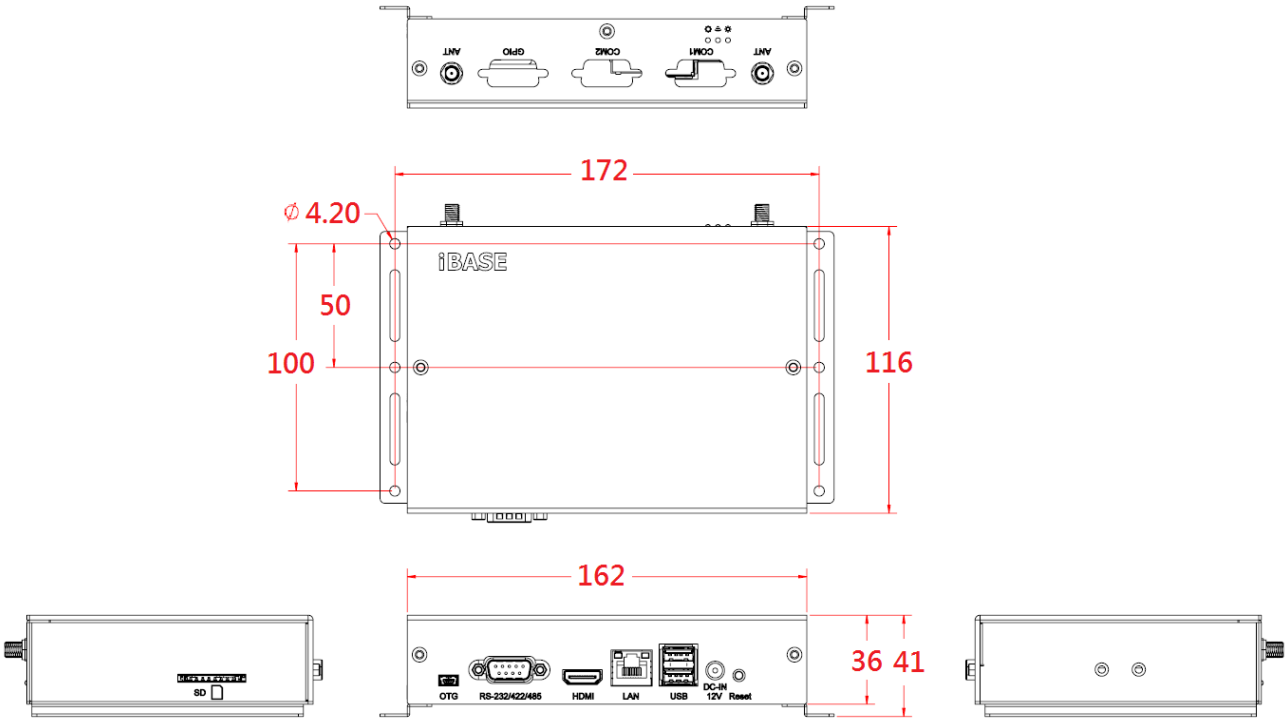
Top View

No.	Name	No.	Name
1	USB OTG Port	8	DIN Rail Mounting Holes
2	COM RS-232/422/485 Port	9	Antenna Holes
3	HDMI Port	10	COM1 & COM2 RS-232 Ports
4	GbE LAN Port	11	GPIO Port
5	USB 2.0 Ports	12	Wall Mount Kit
6	DC Jack	13	LED Indicators (from left to right: Power, Wireless, Programmable Setting)
7	Reset Button	14	SD Card Slot

DIN Rail Mounting View (Optional)

1.7 Dimensions

Unit: mm



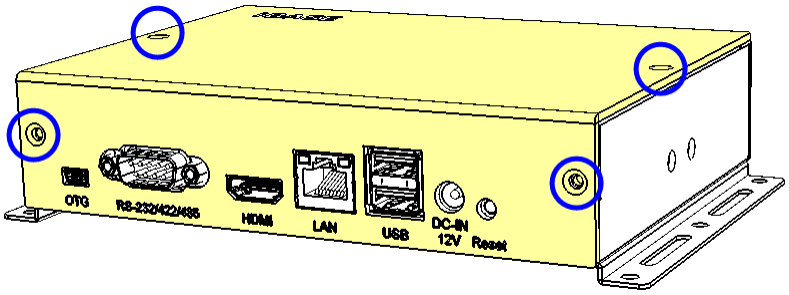
Chapter 2
Hardware Configuration

This section contains general information about:

- Installations
- Jumper and connectors

2.1 Installations

Before installing M.2, wireless, SIM card or module into the device, remove the 4 screws as shown in the picture below to take away the device cover.

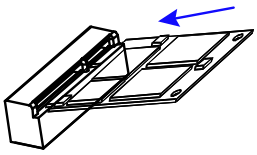


2.1.1 Mini-PCIe & M.2 Cards Installation

To install the mini-PCIe & NGFF M.2 card, remove the device cover first as mentioned above, locate the slot inside the device, and perform the following steps.

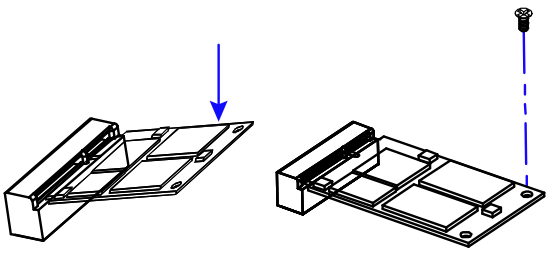
1. Align the keys of the mini-PCIe card with that of the mini-PCIe interface, and insert the card slantwise.

   (Insert the M.2 card in the same way.)



2. Push the mini-PCIe card downwards as shown in the picture below, and fix it onto the brass standoff with a screw.

   (Fix the M.2 card also with one screw.)



2.1.2 WiFi / 3G / 4G Antenna Installation

Insert the WiFi / 3G / 4G antenna extension cable through the antenna hole of the front I/O cover and fasten the antenna as shown below. Then apply adhesive around the hex nut behind the front I/O cover to prevent the extension cable from falling off if the cable becomes loose.

1. Fasten the hex nut and the washer. Then install the antenna.
2. Apply adhesive around here.

Info: The diameter of the nut is around 6.35 mm (0.25”-36UNC).

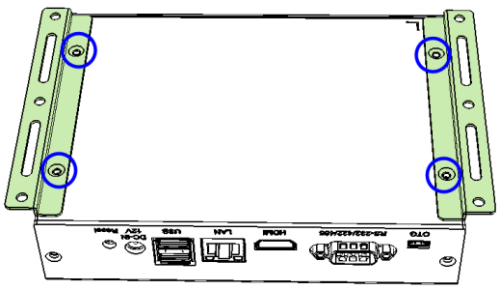
2.1.3 Mounting Installation

Requirements

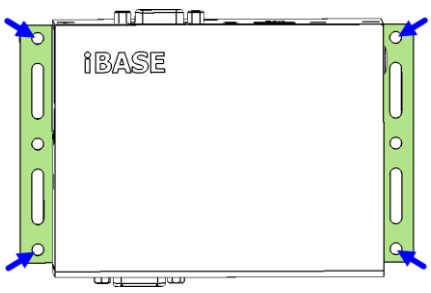
Before mounting the system, ensure that you have enough room for the power adaptor and signal cable routing, and have good ventilation for the power adaptor. The method of mounting must be able to support weight of the device plus the weight of the suspending cables attached to the system. Use the following methods for mounting your system:

Wall Mounting Installation

1. Turn your device upside down. Attach the wall-mount kit to the device and secure with the supplied 4 screws.



2. Prepare at least 4 screws (M3) to install the device on the wall.



2.1.4 COM RS-232/422/485 Port

Pin	Signal Name	Pin	Signal Name
1	DCD, Data carrier detect	6	DSR, Data set ready
2	RXD, Receive data	7	RTS, Request to send
3	TXD, Transmit data	8	CTS, Clear to send
4	DTR, Data terminal ready	9	NC
5	Ground		

Pin	Assignment		
	RS-232	RS-422	RS-485
1	DCD	TX-	DATA-
2	RX	TX+	DATA+
3	TX	RX+	NC
4	DTR	RX-	NC
5	Ground	Ground	Ground
6	DSR	NC	NC
7	RTS	NC	NC
8	CTS	NC	NC
9	NC	NC	NC

2.1.5 COM1 & COM2 Port

Pin	Signal Name	Pin	Signal Name
1	NC	6	NC
2	RXD, Receive data	7	NC
3	TXD, Transmit data	8	NC
4	NC	9	NC
5	Ground		

2.1.6 GPIO Port

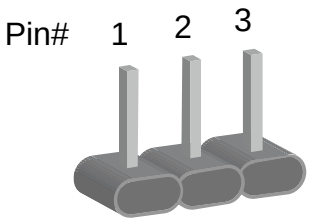
Pin	Signal Name	Pin	Signal Name
1	DIO1	6	DIO5
2	DIO2	7	DIO6
3	DIO3	8	DIO7
4	DIO4	9	DIO8
5	Ground		

2.2 Setting the Jumpers

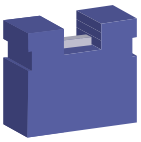
Configure your device by using jumpers to enable the features that you need based on your applications. Contact your supplier if you have doubts about the best configuration for your use.

2.2.1 How to Set Jumpers

Jumpers are short-length conductors consisting of several metal pins with a base mounted on the circuit board. Jumper caps are placed (or removed) on the pins to enable or disable functions or features. If a jumper has 3 pins, you can connect Pin 1 with Pin 2 or Pin 2 with Pin 3 by shorting the jumper.

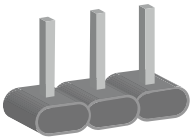
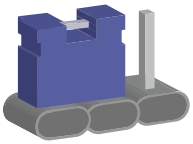
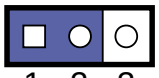
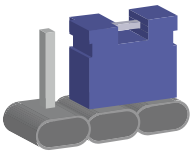
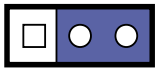


A 3-pin jumper



A jumper cap

Refer to the illustration below to set jumpers.

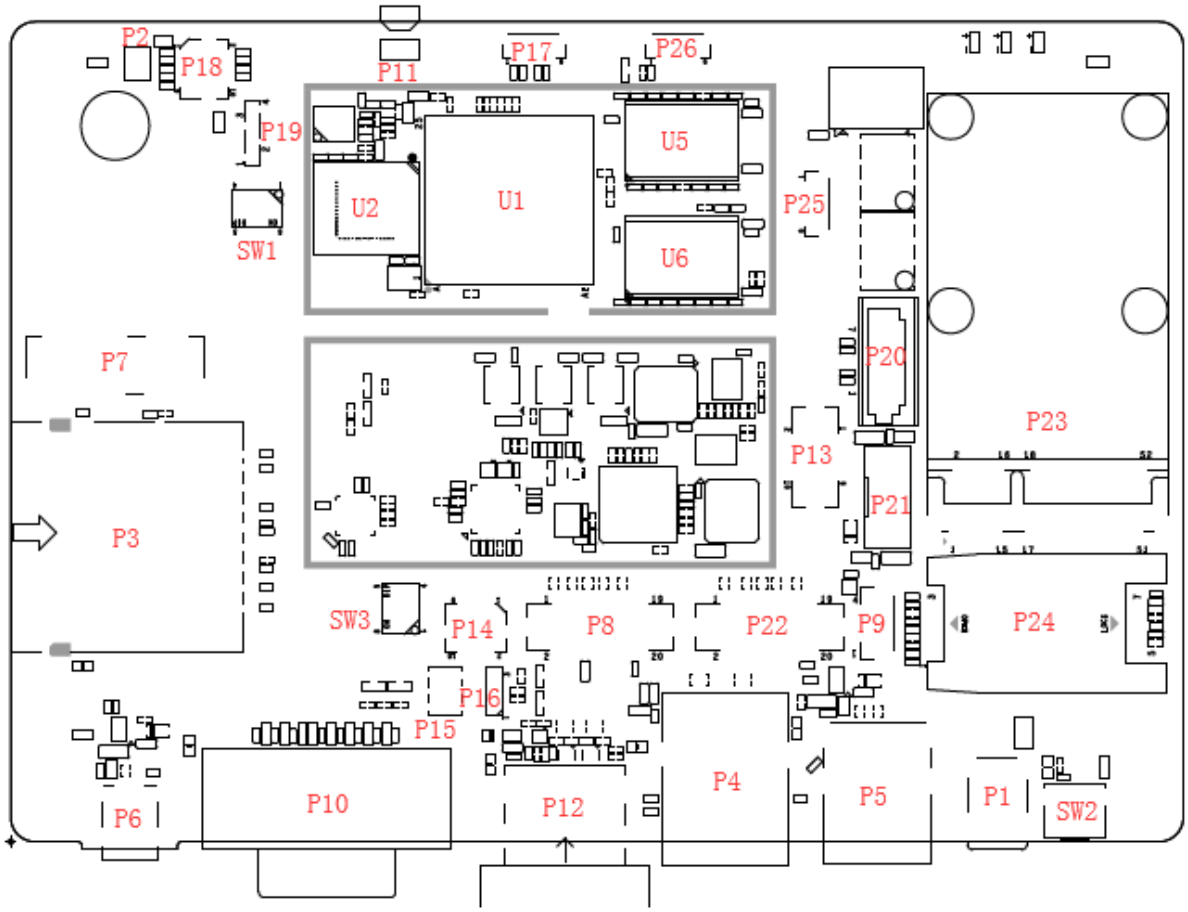
Pin closed	Oblique view	Illustration in the manual
Open		
1-2		
2-3		

When two pins of a jumper are encased in a jumper cap, this jumper is **closed**, i.e. turned **On**.

When a jumper cap is removed from two jumper pins, this jumper is **open**, i.e. turned **Off**.

2.3 Jumper & Connector Locations on Motherboard

Motherboard: IBR117



2.4 Jumper & Connectors Quick Reference

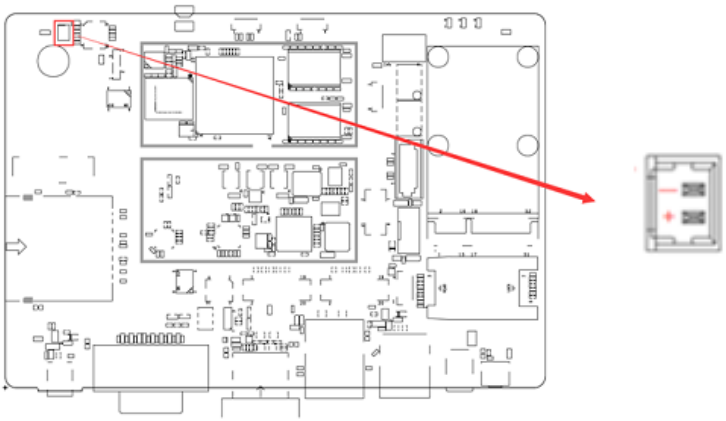
Connectors:

Function	Connector Name	Page
RTC Lithium Cell Connector	P2	15
COM RS-232/422/485 Selection	SW3	15
COM RS-232/422/485 Port	P10	16
USB Hub Connector	P13	17
Digital I/O (GPIO) Connector	P18	17
SATA Power Connector	P21	18
CAN Bus Connector	P25	18
I^2C Connector	P26	19
DC Power Input	P1	- -
SD Card Slot	P3	- -
HDMI Port	P12	- -
GbE LAN Port	P4	- -
Dual USB 2.0 Type-A Port	P5	- -
Mini-USB OTG Port	P6	- -
NGFF M.2 E2230 Slot	P7	- -
LVDS Display Connector	P8, P22	- -
UART Connector	P17	- -
LVDS Backlight Control Connector	P9	- -
Audio Line-In & Line-Out Connector	P14	- -
Factory Use Only	SW1, P11, P19	- -

Jumpers:

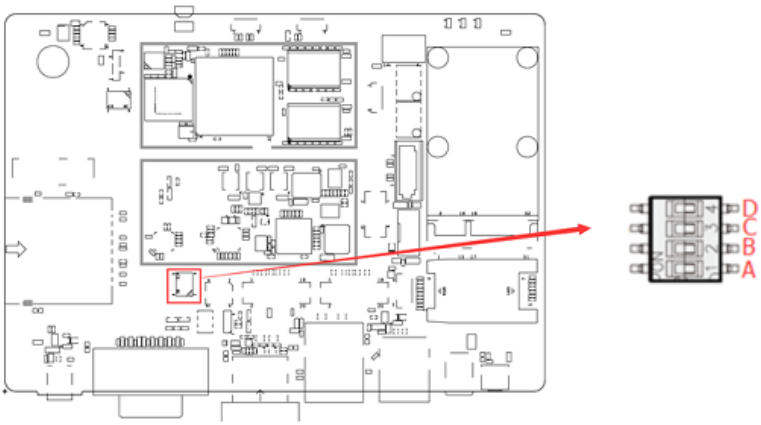
Function	Connector Name	Page
LVDS Power Setting	P16	- -
LVDS Backlight Power Setting	P15	- -

2.4.1 RTC Lithium Cell Connector (P2)



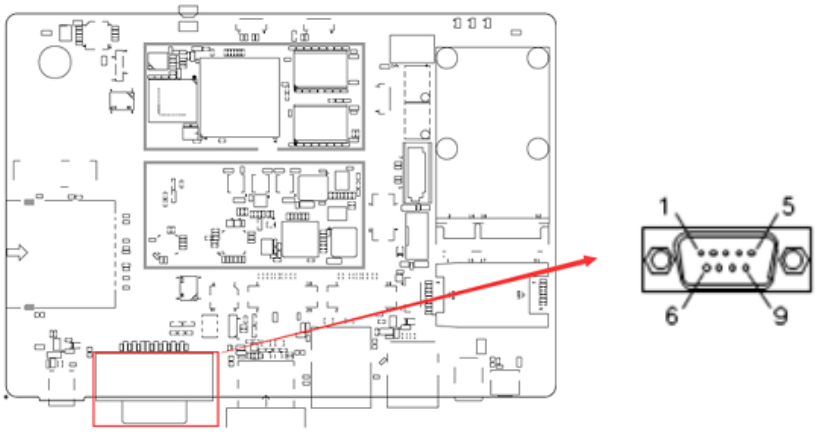
Pin	Signal Name	Pin	Signal Name
1	RTC_VCC	2	Ground

2.4.2 RS-232/422/485 Selection (SW3)



Panel Type	A	B	C	D
RS-422 Full Duplex	NC	ON	ON	ON
Pure RS232 (3T/5R)	NC	OFF	ON	ON
RS-485 Half Duplex (TX Low-Active)	NC	ON	OFF	ON
RS-485 Half Duplex (TX High-Active)	NC	OFF	OFF	ON
RS-422 Full Duplex	NC	ON	ON	OFF
Pure RS232 (1T/1R)	NC	OFF	ON	OFF
RS-485 Half Duplex	NC	ON	OFF	OFF
Shutdown (Default)	NC	OFF	OFF	OFF

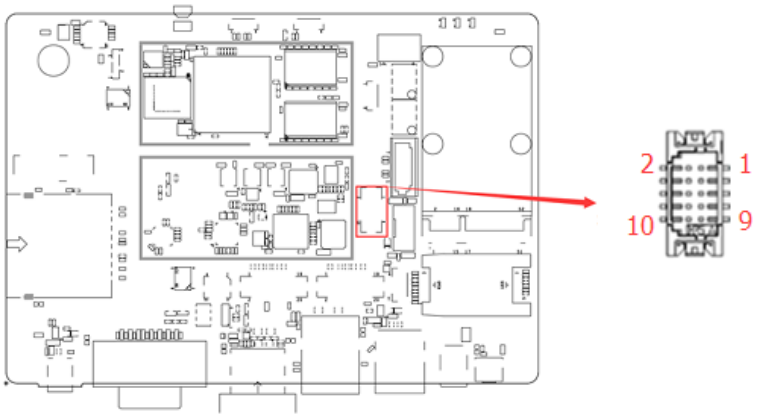
2.4.3 COM RS-232/422/485 Port (P10)



Pin	Signal Name	Pin	Signal Name
1	DCD, Data carrier detect	6	DSR, Data set ready
2	RXD, Receive data	7	RTS, Request to send
3	TXD, Transmit data	8	CTS, Clear to send
4	DTR, Data terminal ready	9	NC
5	Ground		

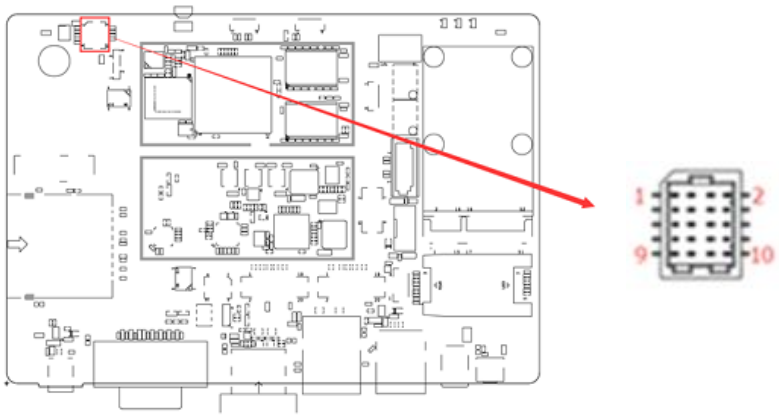
Pin	Assignment		
	RS-232	RS-422	RS-485
1	DCD	TX-	DATA-
2	RX	TX+	DATA+
3	TX	RX+	NC
4	DTR	RX-	NC
5	Ground	Ground	Ground
6	DSR	NC	NC
7	RTS	NC	NC
8	CTS	NC	NC
9	NC	NC	NC

2.4.4 USB Hub Connector (P13)



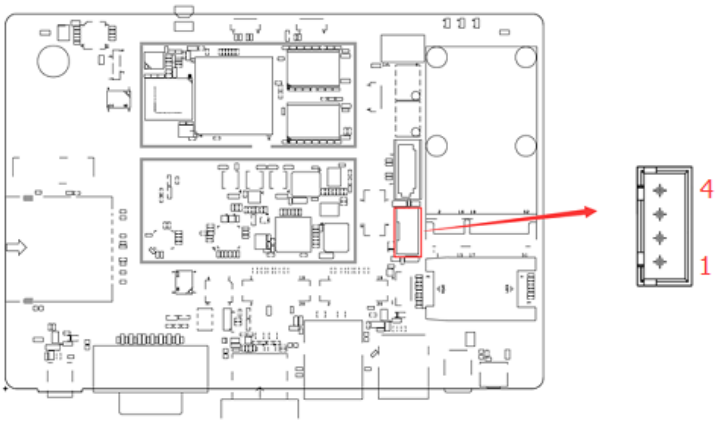
Pin	Signal Name	Pin	Signal Name
1	Ground	2	NC
3	USB1_DP	4	USB2_POWER
5	USB1_DM	6	USB2_DM
7	USB1_POWER	8	USB2_DP
9	NC	10	Ground

2.4.5 Digital (GPIO) Connector (P18)



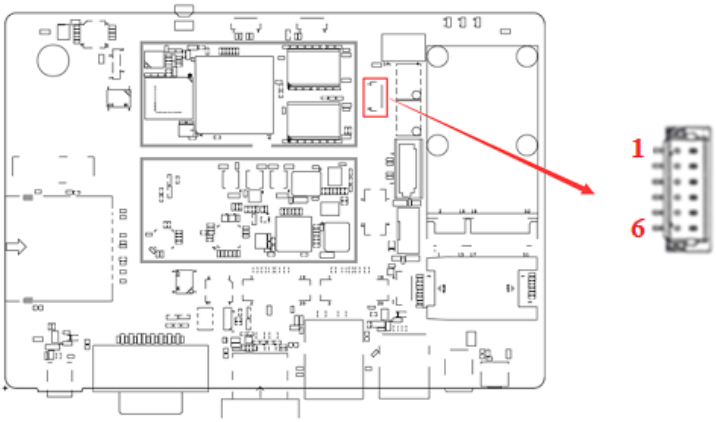
Pin	Signal Name	Pin	Signal Name
1	3.3V	2	DIO5
3	DIO1	4	DIO6
5	DIO2	6	DIO7
7	DIO3	8	DIO8
9	DIO4	10	Ground

2.4.6 SATA Power Connector (P21)



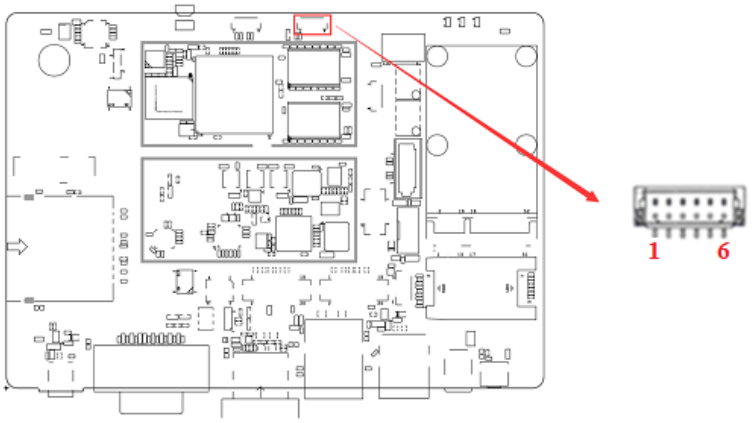
Pin	Signal Name	Pin	Signal Name
1	5V	3	Ground
2	Ground	4	12V

2.4.7 CAN Bus Connector (P25)



Pin	Signal Name	Pin	Signal Name
1	CAN1_TXD	4	CAN2_TXD
2	CAN1_RXD	5	CAN2_RXD
3	Ground	6	Ground

2.4.8 I²C Connector (P26)



Pin	Signal Name	Pin	Signal Name
1	VCC	4	I2C3_SCL
2	TP_INT_B	5	I2C3_SDA
3	TP_RST_B	6	GND

Chapter 3
Software Setup

This chapter introduces the following setup on the device:

(for advanced users only)

- Make a recovery SD card
- Upgrade firmware through the recovery SD card

3.1 Make a Recovery SD Card

Note: This is for advanced users who has IBASE standard image file only.

Basically, ISR201 is preloaded with O.S (Android / Linux) into eMMC by default. Connect the TFT-LCD with ISR201 (or HDMI), and 12V power directly.

This chapter guides you to make a recovery boot-up microSD card. IBASE provides HDMI display for you to prepare the software application pre-development easily under Linux platform.

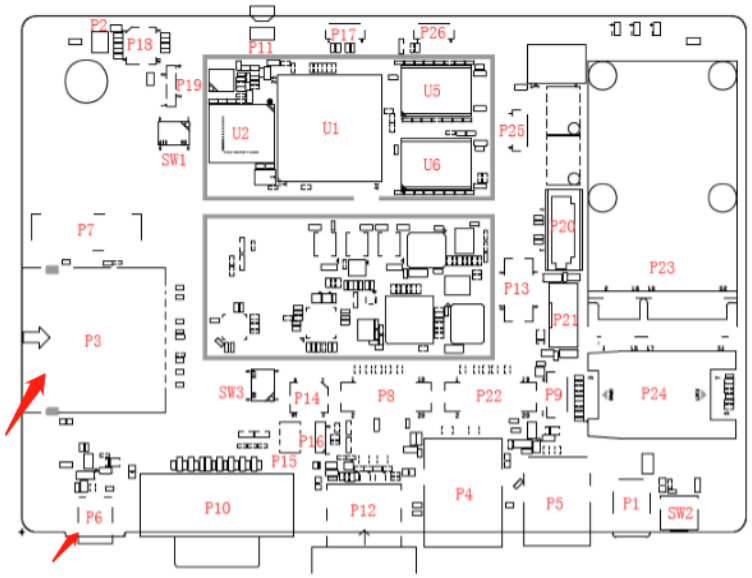
3.1.1 Preparing the Recovery SD card to Install Linux / Android image into eMMC

Note: All data in the eMMC will be erased.

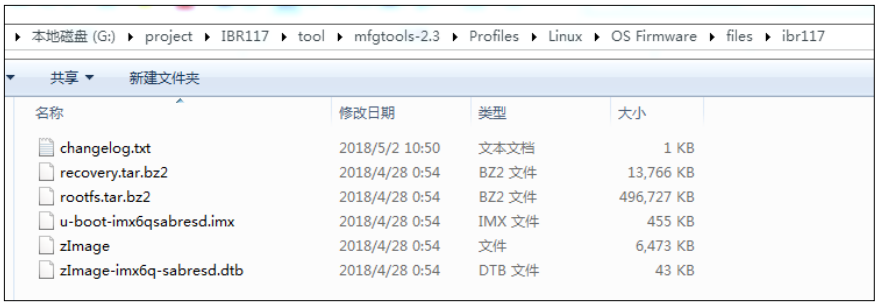
1. System requirements:

   Operating System: Windows 7 or later
   Tool: mfgtools-2.3
   SD card: 4GB or greater in size

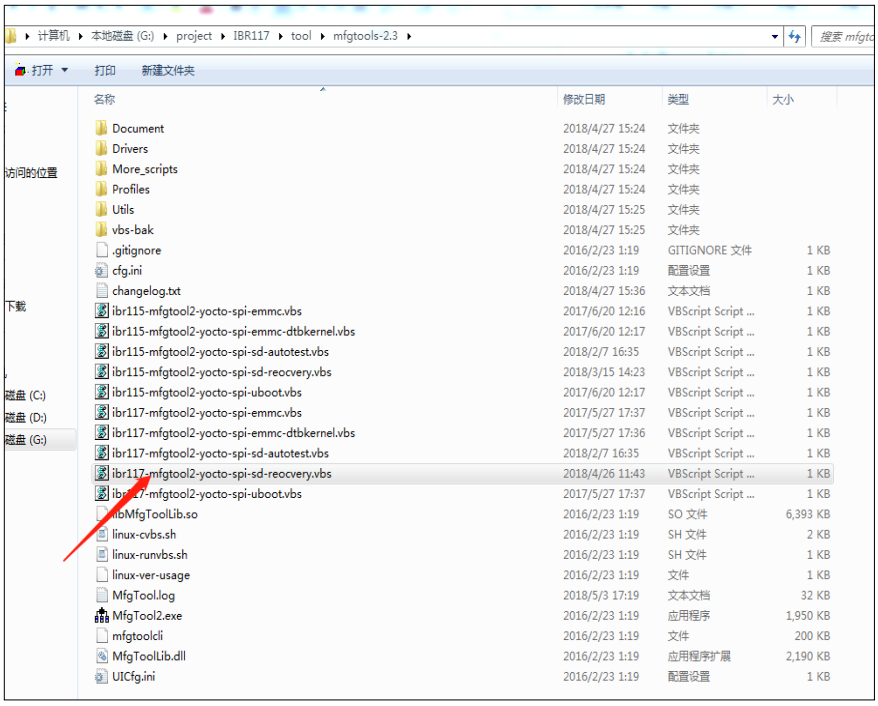
2. Insert your SD card to this board (i.e. the P3 connector), connect the board to PC through the mini-USB port (i.e. the P6 connector), and change the boot mode to burning state.



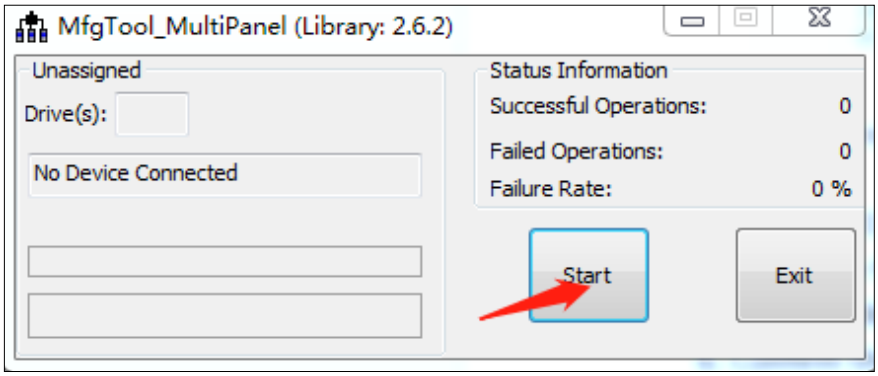
3. Copy the image to the directory **mfgtools-2.3\Profiles\Linux\OS Firmware\files\isr201**.



4. Select the script of **mfgtools sdcard recovery**.

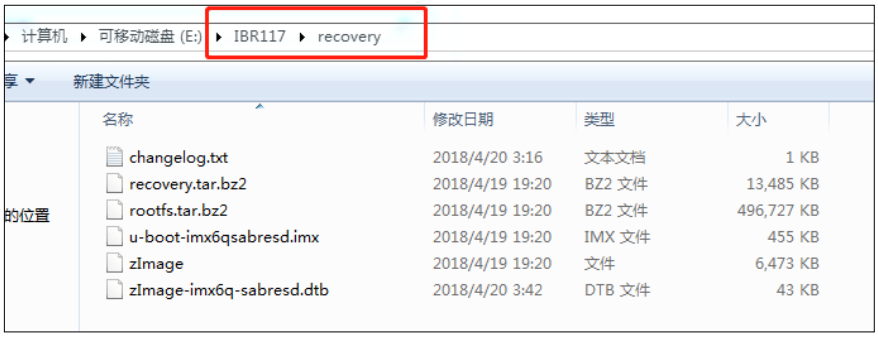


5. Start burning image to SD card. When the image buring finishes, the recovery SD card is ready.



3.1.2 Upgrade Firmware through the Recovery SD Card

1. Copy the image files (what you want to upgrade) to U disk **/ISR201/recovery/**.



2. Insert the recovery SD card and U disk to the board for upgrade.
3. Power up the board and the recovery program starts.
4. When the recovery finishes, power down the board, pull out the recovery SD card and U disk.

Chapter 4
BSP Source Guide

This chapter is dedicated for advanced software engineers only to build BSP source. The topics covered in this chapter are as follows:

- Preparation
- Installing Toolchain
- Building U-Boot
- Building Kernel
- Building RAMdisk Image (Optional)
- Installing Linux to SD Card
- Booting with your SD Card

4.1 Building BSP Source

4.1.1 Preparation

The suggested Host platform is Ubuntu 12.04 and 14.04 in 32-bit and 64-bit versions.

1. Install necessary packages before building:

```
sudo apt-get install gawk wget Git-core diffstat unzip texinfo
sudo apt-get install gcc-multilib build-essential chrpath socat
sudo add-apt-repository ppa:git-core/ppa
sudo apt-get update
sudo apt-get install git
sudo apt-get install texinfo
```

2. Decompress the ISR201 source file **ISR201.tar.xz** into "/home/" folder.

4.1.2 Installing Toolchain

Decompress Toolchain **poky.tar** into directory "/opt".

4.1.3 Building U-Boot

```
cd /home/ISR201
cd  ISR201-x11/tmp/work/imx6dlsabresd-poky-linux-gnueabi/u-boot-imx/2016.03-r0/git
source /opt/poky/1.8/environment-setup-cortexa9hf-vfp-neon-poky-linux-gnueabi
make mx6dlsabresd_defconfig
make
```

4.1.4 Building Kernel

```
cd /home/ISR201
cd ISR101-x11/tmp/work/imx6dlsabresd-poky-linux-gnueabi/linux-imx/4.1.15-r0/git
source /opt/poky/1.8/environment-setup-cortexa9hf-vfp-neon-poky-linux-gnueabi
make imx_v7_defconfig
make zImage LOADADDR=0x10008000
make imx6dl-sabresd.dtb
```

4.1.5 Building RAMdisk Image (Optional)

1. Enter ISR201.

```
cd /home/ISR201
```

2. Running the script below.

```
./build.sh ISR201
```

3. See the image in the "release" directory.

```
ls release/ISR201-20170519-030011/
rootfs.tar.bz2  u-boot-imx6dlsabresd.imx  zImage  zImage-imx6dl-
sabresd.dtb
```

4.1.6 Install Linux to SD Card

1. Refer to the section *3.1 Make a Recovery SD Card* in chapter 3.
2. Run the make card script below.

```
./make_sd.sh /dev/sdd 20150814-111827
```

3. The appearance of the sentence "SD card for booting create done" indicates the installation is successfully done.
4. Pull out the SD card

4.1.7 Booting with Your SD Card

Insert the SD card to your board and insert special COM port dongle to boot from SD. Connect a debug cable to debug port with serial port 115200/N/8/1 setting on your PC’s serial port program, such as hyperterminal/teraterm. Connect a display panel. Power on and you will see U-Boot prompt.

Appendix

This section provides the information of reference code.

A. How to Use GPIO in Linux

```
# Take gpio1_0    as example, export value:1*32+0=32

# GPIO example 1: Output
echo 32 > /sys/class/gpio/export
echo out > /sys/class/gpio/gpio40/direction
echo 0 > /sys/class/gpio/gpio40/value
echo 1 > /sys/class/gpio/gpio40/value

# GPIO example 2: Input
echo 32 > /sys/class/gpio/export
echo in > /sys/class/gpio/gpio40/direction
cat /sys/class/gpio/gpio40/value
```

B. How to Use Watchdog in Linux

```
// create fd
int fd;
//open watchdog device
fd = open("/dev/watchdog", O_WRONLY);
//get watchdog support
ioctl(fd, WDIOC_GETSUPPORT, &ident);
//get watchdog status
ioctl(fd, WDIOC_GETSTATUS, &status);
//get watchdog timeout
ioctl(fd, WDIOC_GETTIMEOUT, &timeout_val);
//set watchdog timeout
ioctl(fd, WDIOC_SETTIMEOUT, &timeout_val);
//feed dog
ioctl(fd, WDIOC_KEEPALIVE, &dummy);
```

C. eMMC Test

Note: This operation may damage the data stored in eMMC flash. Before starting the test, make sure there is no critical data in the eMMC flash being used.

- **Read, write, and check**

```
MOUNT_POINT_STR="/var"

#create data file
dd if=/dev/urandom of=/tmp/data1 bs=1024k count=10
#write data to emmc
dd if=/tmp/data1 of=$MOUNT_POINT_STR/data2 bs=1024k count=10
#read data2, and compare    with data1
cmp $MOUNT_POINT_STR/data2 /tmp/data1
```

- **eMMC speed test**

```
MOUNT_POINT_STR="/var"

#get    emmc write speed"
time dd if=/dev/urandom of=$MOUNT_POINT_STR/test bs=1024k count=10
# clean caches
echo 3 > /proc/sys/vm/drop_caches
#get    emmc read speed"
time dd if=$MOUNT_POINT_STR/test of=/dev/null bs=1024k count=10
```

D. USB (flash disk) Test

Insert the USB flash disk then assure it is in ISR201 device list.

Note: This operation may damage the data stored in the USB flash disk. Before starting the test, make sure there is no critical data in the eMMC flash being used.

- **Read, write, and check**

```
USB_DIR="/run/media/mmcblk1p1"
#create data file
dd if=/dev/urandom of=/var/data1 bs=1024k count=100
#write data to usb flash disk
dd if=/var/data1 of=$USB_DIR/data2 bs=1024k count=100
#read data2, and compare    with data1
cmp $USB_DIR/data2 /var/data1
```

- **USB speed test**

```
USB_DIR="/run/media/mmcblk1p1"
# usb write speed
dd if=/dev/zero of=$BASIC_DIR/$i/test bs=1M count=1000 oflag=nocache

# usb read speed
dd if=$BASIC_DIR/$i/test of=/dev/null bs=1M oflag=nocache
```

E. SD Card Test

When ISR201 is booted from eMMC, SD card is “/dev/mmcblk1” and able to see by “ls /dev/mmcblk1*” command:

```
/dev/mmcblk1    /dev/mmcblk1p2 /dev/mmcblk1p4 /dev/mmcblk1p5 /dev/mmcblk1p6
```

When booting from SD card, replace test pattern “/dev/mmcblk1” to “/dev/mmcblk0”.

Note: This operation may damage the data stored the SD card. Before starting the test, make sure there is no critical data in the eMMC flash being used.

- **Read, write, and check**

```
SD_DIR="/run/media/mmcblk1"
#create data file
dd if=/dev/urandom of=/var/data1 bs=1024k count=100
#write data to SD card
dd if=/var/data1 of=$ SD_DIR/data2 bs=1024k count=100
#read data2, and compare    with data1
cmp $SD_DIR/data2 /var/data1
```

- **SD card speed test**

```
SD_DIR="/run/media/mmcblk1"

# SD write speed
dd if=/dev/zero of=$SD_DIR/test bs=1M count=1000 oflag=nocache

# SD read speed
dd if=$SD_DIR/test of=/dev/null bs=1M oflag=nocache
```

F. RS-232 Test

```
//open ttymxc1
fd    = open(/dev/ttymxc1,O_RDWR );

//set speed
tcgetattr(fd, &opt);
cfsetispeed(&opt, speed);
cfsetospeed(&opt, speed);
tcsetattr(fd, TCSANOW, &opt)

//get_speed
tcgetattr(fd, &opt);
speed = cfgetispeed(&opt);

//set_parity
// options.c_cflag
options.c_cflag &= ~CSIZE;
options.c_cflag &= ~CSIZE;
options.c_lflag    &= ~(ICANON | ECHO | ECHOE | ISIG);    /*Input*/
options.c_oflag    &= ~OPOST;     /*Output*/
//options.c_cc
options.c_cc[VTIME] = 150;
options.c_cc[VMIN] = 0;
#set parity
tcsetattr(fd, TCSANOW, &options)

//write ttymxc1
write(fd, write_buf, sizeof(write_buf));

//read ttymxc1
read(fd, read_buf, sizeof(read_buf)))
```

G. RS-485 Test

```
//open ttymxc1
fd    = open(/dev/ttymxc1,O_RDWR );

//set speed
tcgetattr(fd, &opt);
cfsetispeed(&opt, speed);
cfsetospeed(&opt, speed);
tcsetattr(fd, TCSANOW, &opt

//get_speed
tcgetattr(fd, &opt);
speed = cfgetispeed(&opt);

//set_parity
// options.c_cflag
options.c_cflag &= ~CSIZE;
options.c_cflag &= ~CSIZE;
options.c_lflag    &= ~(ICANON | ECHO | ECHOE | ISIG);    /*Input*/
options.c_oflag    &= ~OPOST;     /*Output*/
//options.c_cc
options.c_cc[VTIME] = 150;
options.c_cc[VMIN] = 0;
#set parity
tcsetattr(fd, TCSANOW, &options)

//write ttymxc1
write(fd, write_buf, sizeof(write_buf));

//read ttymxc1
read(fd, read_buf, sizeof(read_buf)))
```

H. Ethernet Test

- **Ethernet FTP test**

```
#intall ftp
sudo chmod 777 /home/root/testscript/ethernet/lftp_library/lftp
sudo cp /home/root/testscript/ethernet/lftp_library/lftp /usr/bin/
sudo mkdir /usr/lib/lftp
sudo mkdir /usr/lib/lftp/4.6.3a
sudo cp /home/root/testscript/ethernet/lftp_library/*.so /usr/lib/lftp/4.6.3a
sudo cp /home/root/testscript/ethernet/lftp_library/* /lib/

#lftp server 192.168.1.123
lftp 192.168.1.123 <<EOF
#upload data to server    192.168.1.123
put data1
#download data from server    192.168.1.123
get data1
#exit lftp
bye
```

- **Ethernet Ping test**

```
#ping server 192.168.1.123
ping -c 20 192.168.1.123 >/tmp/ethernet_ping.txt
```

- **Ethernet TCP test**

```
#server 192.168.1.123 run command    “iperf3 -s”
#communicate with server 192.168.1.123 in tcp mode by iperf3
iperf3 -c 192.168.1.123 -i 1 -t 20 -w 32M -P 4
```

- **Ethernet UDP test**

```
#server 192.168.1.123 run command    “iperf3 -s”
#communicate with server 192.168.1.123 in udp mode by iperf3
iperf3 -c $SERVER_IP -u -i 1 -b 200M
```

I. HDMI Test

- **HDMI display test**

```
//Open the file for reading and writing
framebuffer_fd = open("/dev/fb2", O_RDWR);

// Get fixed screen information
ioctl(framebuffer_fd, FBIOGET_FSCREENINFO, &finfo)

// Get variable screen information
ioctl(framebuffer_fd, FBIOGET_VSCREENINFO, &vinfo)

// Figure out the size of the screen in bytes
screensize = vinfo.xres * vinfo.yres * vinfo.bits_per_pixel / 8;

// Map the device to memory
fbp = (char *)mmap(0, screensize, PROT_READ | PROT_WRITE, MAP_SHARED, framebuffer_fd, 0);

// Figure out where in memory to put the pixel
memset(fbp, 0x00,screensize);

//draw point by fbp
long int location = 0;
location = (x+g_xoffset) * (g_bits_per_pixel/8) +
  (y+g_yoffset) * g_line_length;
*(fbp + location + 0) = color_b;
*(fbp + location + 1) = color_g;
*(fbp + location + 2) = color_r;

//close framebuffer fd
close(framebuffer_fd);
```

- **HDMI audio test**

```
#enable hdmi audio
echo 0 > /sys/class/graphics/fb2/blank
#play wav file by    hdmi audio
aplay /home/root/testscript/hdmi/1K.wav -D plughw:0,0
```

J. SATA (hard disk) Test

Insert the hard disk and then assure it is in ISR201 device list.

Note: This operation may damage the data stored in the hard disk. Before starting the test, make sure there is no critical data in the hard disk being used.

- **Reading & writing test**

```
HD_DIR="/run/media/mmcblk1p1"
#create data file
dd if=/dev/urandom of=/var/data1 bs=1024k count=100
#write data to hard disk
dd if=/var/data1 of=$ HD_DIR/data2 bs=1024k count=100
#read data2, and compare    with data1
cmp $HD_DIR/data2 /var/data1
```

- **Hard disk spped test**

```
HD_DIR="/run/media/mmcblk1p1"
# hard disk write speed
dd if=/dev/zero of=$HD_DIR/$i/test bs=1M count=1000 oflag=nocache

# hard disk read speed
dd if=$HD_DIR/$i/test of=/dev/null bs=1M oflag=nocache
```

K. CAN Test

- **Checking CAN GPIO**

```
#config can pin to gpio
/home/root/testscript/can/memtool -32    0x20e0208 = 5
echo "106"    > /sys/class/gpio/export
#config gpio to out mode
echo "out" > /sys/class/gpio/gpio106/direction
#set gpio to 1
echo    1       > /sys/class/gpio/gpio106/value
#set gpio to 1
echo    0       > /sys/class/gpio/gpio106/value
#config gpio to in mode
echo "in" > /sys/class/gpio/gpio106/direction
#get gpio value
ret0_0=`cat /sys/class/gpio/gpio106/value`
```

- **Testing CAN**

```
#config can parrameter
ip link set can0 type can bitrate 125000 triple-sampling on
#enable can
ifconfig can0 up
#disable
ifconfig can0 down
#send data
cangen can0 &
#receive data
candump -n 10 can1 -T 5000
```

L. 3G Test

- **Checking 3G state**

```
#Check UC20 module state and sim state
cat /dev/ttyUSB2    &
```

- **Testing 3G**

```
# the command will connect 3g to network
# make sure that the simcard is inserted right, and ANT connected
pppd call wcdma

echo "ping www.baidu.com to make sure the network ok"
ping www.baidu.com
```