

# AR3399RK

Quick Start Guide

Ver 0.1

This Quick Start Guide is for BCM AR3399RK ARM motherboards based on Rockchip RK3399 Cortex A72+A53 platform.



# Contents

<b>1. Overview.....</b>	<b>3</b>
<b>2. Mainboard Illustration.....</b>	<b>4</b>
<b>3. Getting Started.....</b>	<b>5</b>
<b>4. Jumper and Connector pin out.....</b>	<b>7</b>

## 1. Overview

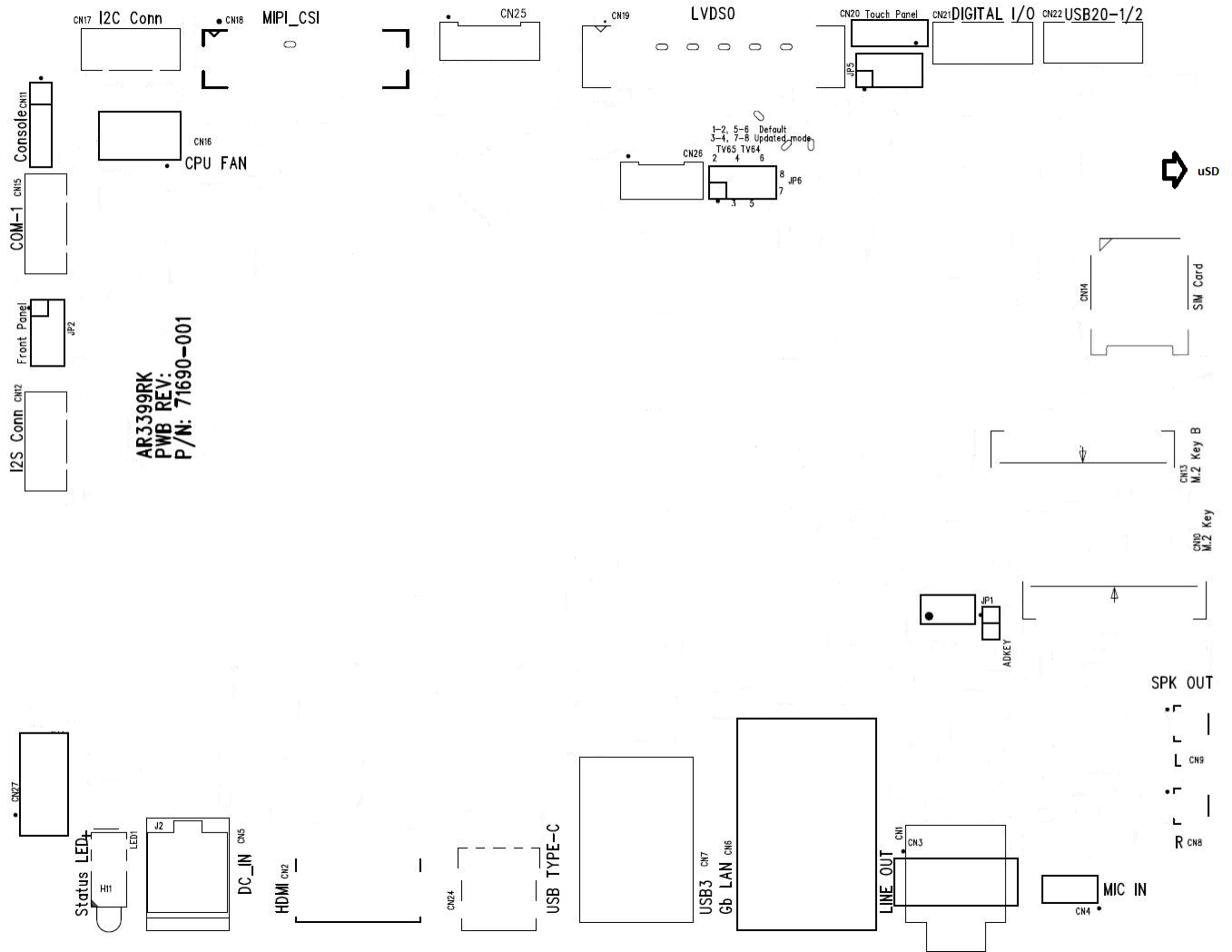
# AR3399RK

## Rockchip® RK3399 Dual Core Cortex-A72 + Quad Core Cortex-A53 (6-Core) SoC ARM Motherboard

- Rockchip® RK3399 Dual Core Cortex-A72 + Quad Core Cortex-A53 (6-Core) SoC
- 2GB DDR3L Onboard (Expandable to 4GB Max)
- ARM® Mali-T860MP4 Integrated Graphics
- HDMI, LVDS, MIPI-CSI
- 1 x uSD Card Socket
- 16GB eMMC Onboard
- 2 x USB 3.0, 2 x USB 2.0, 1 x USB 3.0 Gen 2 Type C OTG Connector
- 1 x 2230 M.2 E Key and 1 x 2242 M.2 B Key
- I2C, I2S, GPIO
- 12V DC-In
- 4.33" x 5.71" (110mm x 145mm)

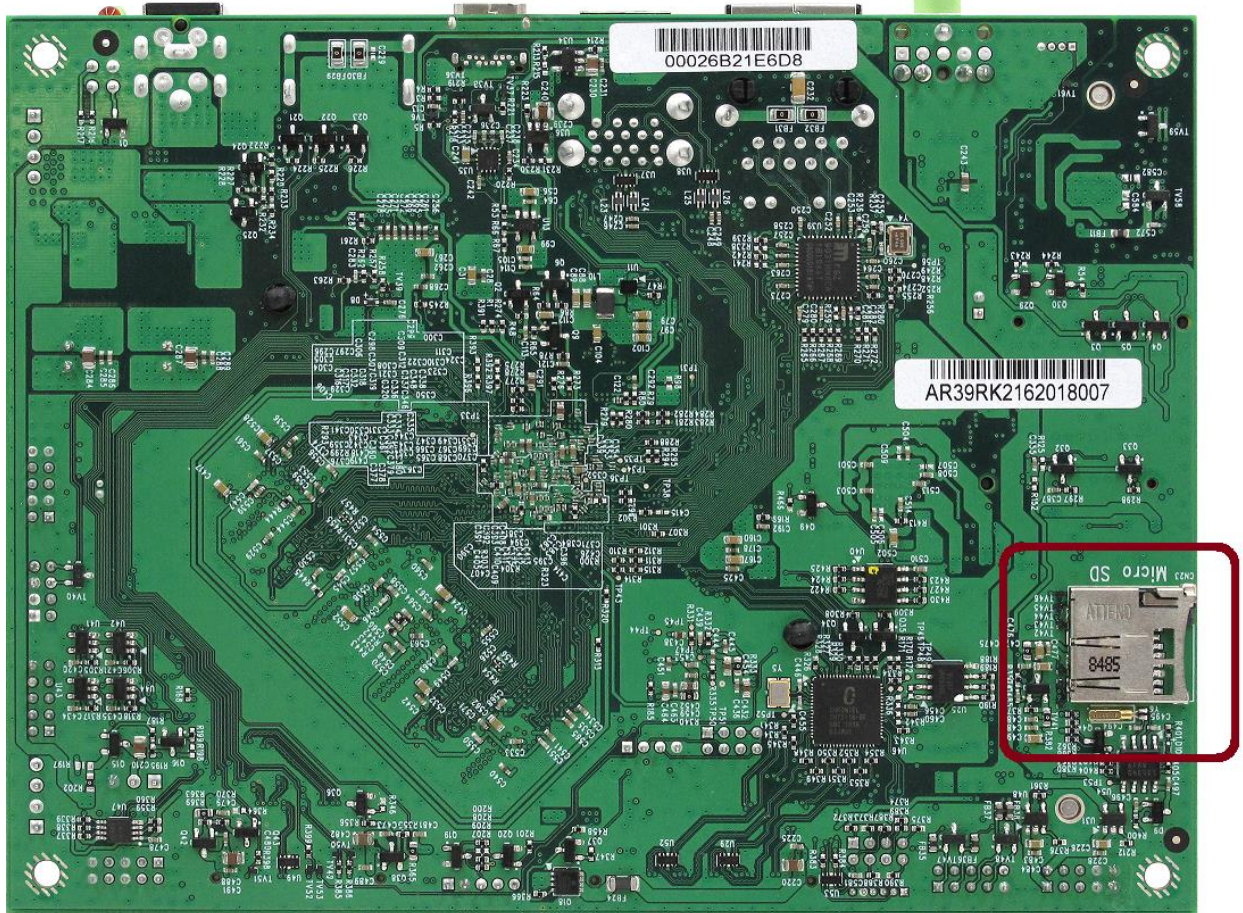
## 2. Board IO/Jumper illustration

Board Top view :

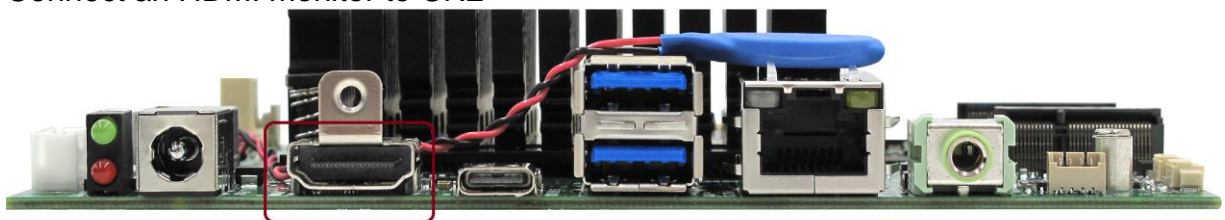


### 3. Getting started

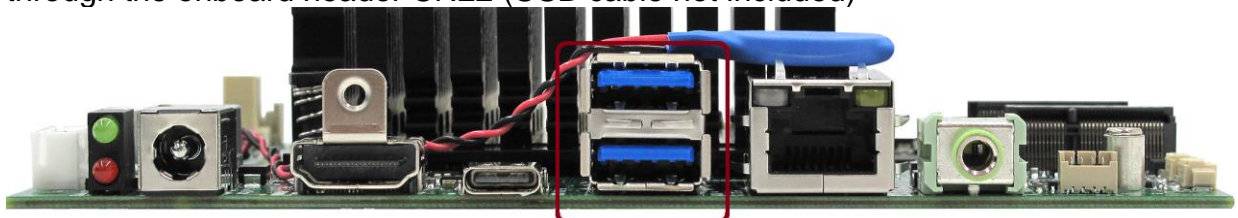
1. Insert a bootable uSD card into slot CN23 if booting from uSD instead of eMMC.  
*Note: do not force the card into the slot; it should go in with moderate pressure*



2. Connect an HDMI monitor to CN2



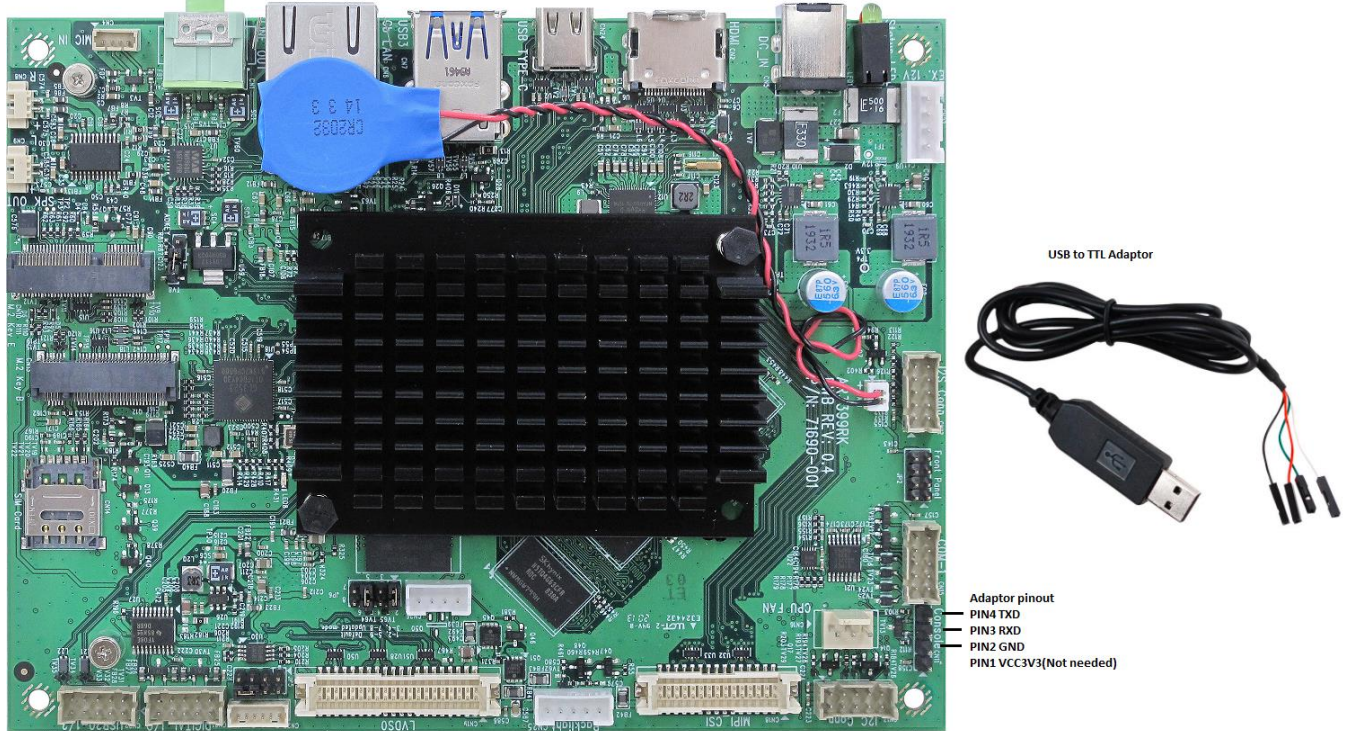
3. Connect a USB mouse/keyboard either to the rear I/O USB connectors (CN7), or through the onboard header CN22 (USB cable not included)



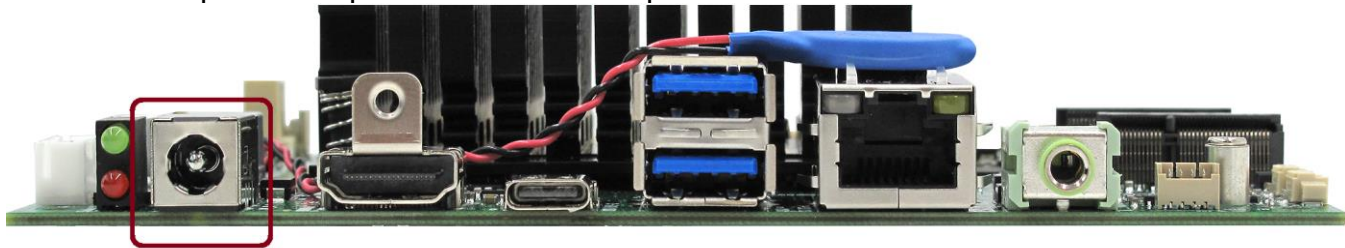


4. if you need console access, connect a serial (TTL) to USB adapter from CN11 to your workstation.




4.1 Set your terminal settings to the following: 115200 bps, 8bits, no parity, 1 stop bit, and no flow control

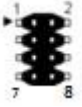



5. Connect 12V power adapter to CN5 to boot up



## 4. Jumper Settings and Pin Definition

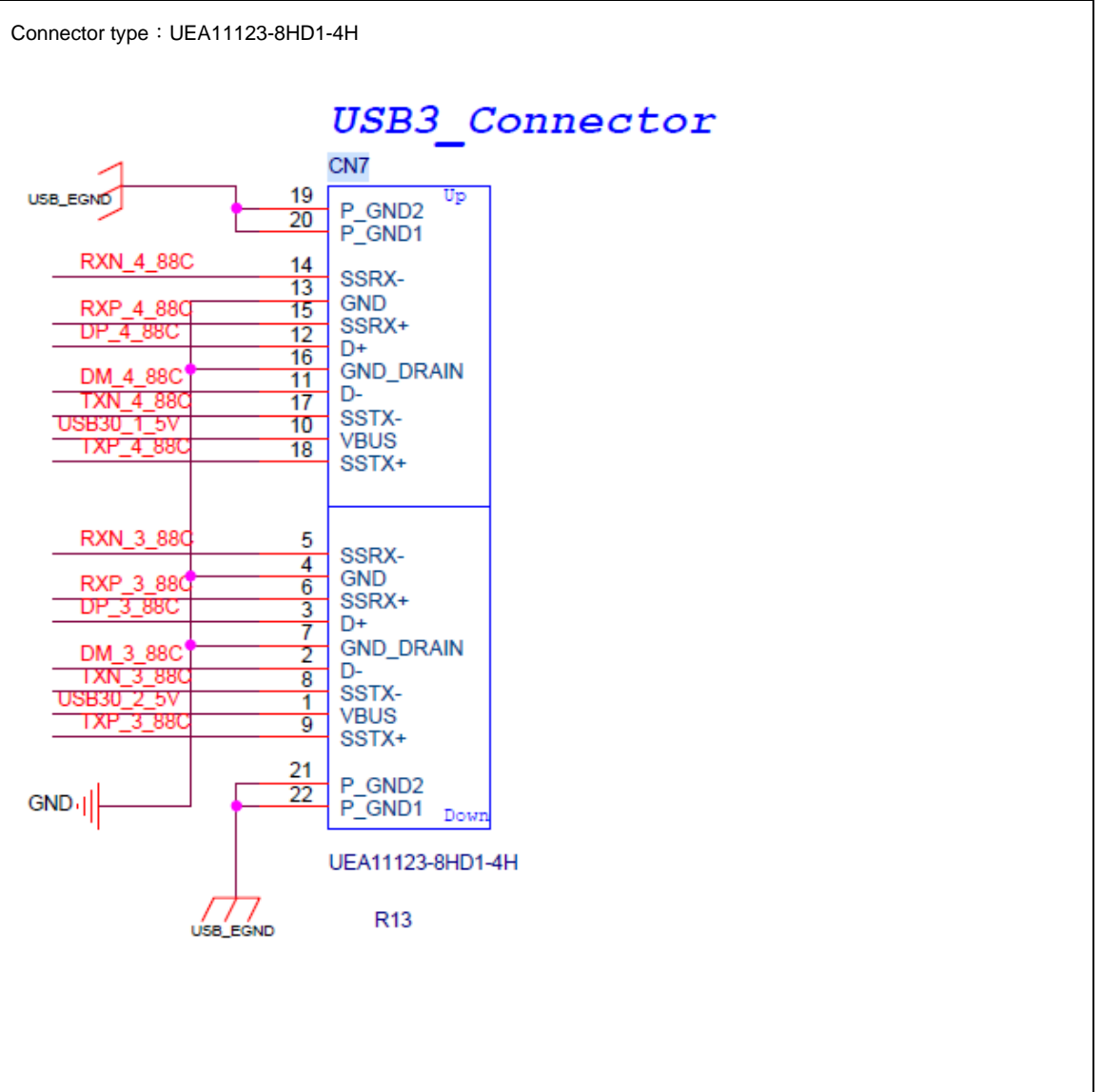
1.	JP1	Boot Mode	2.	JP2	Front Panel																																																								
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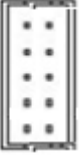
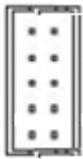
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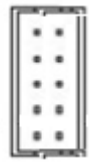
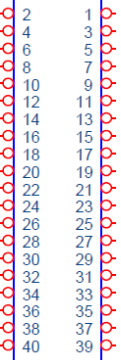
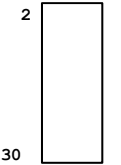





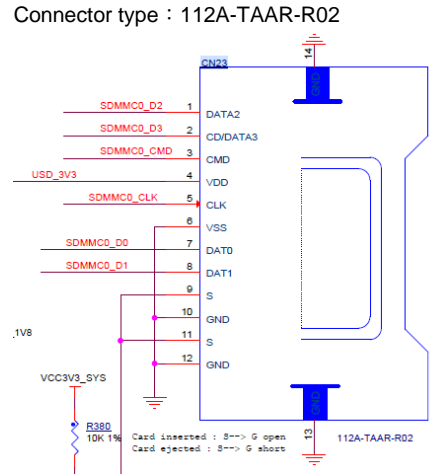
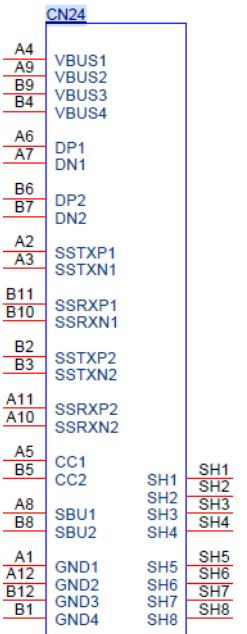

8.	CN5	<p><b>DC JACK</b></p> <p>Connector type : DC-JACK 6.5/2.5mm</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>12V DC-IN</td> </tr> <tr> <td>2</td> <td>GND</td> </tr> <tr> <td>3</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	1	12V DC-IN	2	GND	3	GND	9.	CN6	<p><b>LAN</b></p> <p>Connector type : RTA-164AAK1A</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GE_MDI0+</td> <td>9</td> <td>GE_MDI3+</td> </tr> <tr> <td>2</td> <td>GE_MDI0-</td> <td>10</td> <td>GE_MDI3-</td> </tr> <tr> <td>3</td> <td>GE_MDI1+</td> <td>11</td> <td>LED2</td> </tr> <tr> <td>4</td> <td>GE_MDI1-</td> <td>12</td> <td>D3V3</td> </tr> <tr> <td>5</td> <td>GND</td> <td>13</td> <td>D3V3</td> </tr> <tr> <td>6</td> <td>GND</td> <td>14</td> <td>LED1</td> </tr> <tr> <td>7</td> <td>GE_MDI2+</td> <td>15</td> <td>GND</td> </tr> <tr> <td>8</td> <td>GE_MDI2-</td> <td>16</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	Pin	Definition	1	GE_MDI0+	9	GE_MDI3+	2	GE_MDI0-	10	GE_MDI3-	3	GE_MDI1+	11	LED2	4	GE_MDI1-	12	D3V3	5	GND	13	D3V3	6	GND	14	LED1	7	GE_MDI2+	15	GND	8	GE_MDI2-	16	GND
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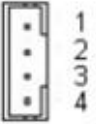
**10. CN7 : USB3.0x2 (Type A)**



<p>11. CN8</p> 	<p><b>SPK OUT R</b></p> <p>Connector type : Header 1X2 1.25</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>SPK_R_OUT+</td> </tr> <tr> <td>2</td> <td>SPK_R_OUT-</td> </tr> </tbody> </table>	Pin	Definition	1	SPK_R_OUT+	2	SPK_R_OUT-	<p>12. CN9</p> 	<p><b>SPK OUT L</b></p> <p>Connector type : Header 1X2 1.25</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>SPK_L_OUT+</td> </tr> <tr> <td>2</td> <td>SPK_L_OUT-</td> </tr> </tbody> </table>	Pin	Definition	1	SPK_L_OUT+	2	SPK_L_OUT-																												
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3	UART2_TXD																																										
4	UART2_RXD																																										
<p>15. CN12</p> 	<p><b>I2S</b></p> <p>Connector type : HDR 2x5 2.0mm DIP</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>I2S1_SCLK</td> <td>6</td> <td>I2S1_SDO0</td> </tr> <tr> <td>2</td> <td>VCC1V8</td> <td>7</td> <td>I2S_LRCK_TX</td> </tr> <tr> <td>3</td> <td>GND</td> <td>8</td> <td>I2S1_SDI0</td> </tr> <tr> <td>4</td> <td>GND</td> <td>9</td> <td>GND</td> </tr> <tr> <td>5</td> <td>I2S_LRCK_RX</td> <td>10</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	Pin	Definition	1	I2S1_SCLK	6	I2S1_SDO0	2	VCC1V8	7	I2S_LRCK_TX	3	GND	8	I2S1_SDI0	4	GND	9	GND	5	I2S_LRCK_RX	10	GND	<p>16. CN13</p>	<p><b>M.2 Key B Slot</b></p> <p>Standard M.2 Key B pinout</p>																
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<p>17. CN14</p>	<p><b>NANO SIM CARD</b></p> <p>Connector type : NANO SIM Socket</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>C1</td> <td>CCVCC</td> </tr> <tr> <td>C2</td> <td>CCRST</td> </tr> <tr> <td>C3</td> <td>CCCLK</td> </tr> <tr> <td>C5</td> <td>GND</td> </tr> <tr> <td>C6</td> <td>NC</td> </tr> <tr> <td>C7</td> <td>CCIO</td> </tr> <tr> <td>SW</td> <td>SIMDET_1</td> </tr> </tbody> </table>	Pin	Definition	C1	CCVCC	C2	CCRST	C3	CCCLK	C5	GND	C6	NC	C7	CCIO	SW	SIMDET_1	<p>18. CN15</p> 	<p><b>COM-1 UART0</b></p> <p>Connector type : HDR 2x5 2.0mm DIP</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>RTS_RS232</td> <td>6</td> <td>RX_TTL</td> </tr> <tr> <td>2</td> <td>RX_RS232</td> <td>7</td> <td>CTS_TTL</td> </tr> <tr> <td>3</td> <td>CTS_RS232</td> <td>8</td> <td>TX_TTL</td> </tr> <tr> <td>4</td> <td>TX_RS232</td> <td>9</td> <td>GND</td> </tr> <tr> <td>5</td> <td>RTS_TTL</td> <td>10</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	Pin	Definition	1	RTS_RS232	6	RX_TTL	2	RX_RS232	7	CTS_TTL	3	CTS_RS232	8	TX_TTL	4	TX_RS232	9	GND	5	RTS_TTL	10	GND
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19.	<b>CN16</b> 	<b>CPU FAN</b> Connector type : HDR 1x3 2.54mm Wafer <table border="1" data-bbox="381 289 592 487"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>NC</td> </tr> <tr> <td>2</td> <td>VCC</td> </tr> <tr> <td>3</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	1	NC	2	VCC	3	GND	20.	<b>CN17</b> 	<b>I2C</b> Connector type : HDR 2x5 2.0mm DIP <table border="1" data-bbox="1063 289 1507 583"> <thead> <tr> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>VCC3V3</td> <td>6</td> <td>GND</td> </tr> <tr> <td>2</td> <td>VCC5V</td> <td>7</td> <td>EXT_I2C_SCL</td> </tr> <tr> <td>3</td> <td>EXT_I2C_SDA</td> <td>8</td> <td>I2C_SCL_5V</td> </tr> <tr> <td>4</td> <td>I2C_SDA_5V</td> <td>9</td> <td>GND</td> </tr> <tr> <td>5</td> <td>GND</td> <td>10</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	Pin	Definition	1	VCC3V3	6	GND	2	VCC5V	7	EXT_I2C_SCL	3	EXT_I2C_SDA	8	I2C_SCL_5V	4	I2C_SDA_5V	9	GND	5	GND	10	GND																																																								
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21. <b>CN19 : LVDS0</b>																																																																																													
	<b>LVDS0</b> <b>CN19</b> 	Connector type : HIROSE DF13A-40DP-1.25 <table border="1" data-bbox="381 718 1445 1255"> <thead> <tr> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>LVDS_3.3V</td> <td>11</td> <td>LVDS_DB1-</td> <td>21</td> <td>LVDS_DA1+</td> <td>31</td> <td>GND</td> </tr> <tr> <td>2</td> <td>LVDS_5V</td> <td>12</td> <td>LVDS_DB0-</td> <td>22</td> <td>LVDS_DA0+</td> <td>32</td> <td>GND</td> </tr> <tr> <td>3</td> <td>LVDS_3.3V</td> <td>13</td> <td>GND</td> <td>23</td> <td>LVDS_DA1-</td> <td>33</td> <td>LVDSA_CLK+</td> </tr> <tr> <td>4</td> <td>LVDS_5V</td> <td>14</td> <td>GND</td> <td>24</td> <td>LVDS_DA0-</td> <td>34</td> <td>LVDSB_CLK+</td> </tr> <tr> <td>5</td> <td>LVDS0_PAL_SCL</td> <td>15</td> <td>LVDS_DB3+</td> <td>25</td> <td>GND</td> <td>35</td> <td>LVDSA_CLK-</td> </tr> <tr> <td>6</td> <td>LVDS0_PAL_SDA</td> <td>16</td> <td>LVDS_DB2+</td> <td>26</td> <td>GND</td> <td>36</td> <td>LVDSB_CLK-</td> </tr> <tr> <td>7</td> <td>GND</td> <td>17</td> <td>LVDS_DB3-</td> <td>27</td> <td>LVDS_DA3+</td> <td>37</td> <td>GND</td> </tr> <tr> <td>8</td> <td>HPD_DET_C</td> <td>18</td> <td>LVDS_DB2-</td> <td>28</td> <td>LVDS_DA2+</td> <td>38</td> <td>GND</td> </tr> <tr> <td>9</td> <td>LVDS_DB1+</td> <td>19</td> <td>GND</td> <td>29</td> <td>LVDS_DA3-</td> <td>39</td> <td>LVDS_12V</td> </tr> <tr> <td>10</td> <td>LVDS_DB0+</td> <td>20</td> <td>GND</td> <td>30</td> <td>LVDS_DA2-</td> <td>40</td> <td>LVDS_12V</td> </tr> </tbody> </table>				Pin	Definition	Pin	Definition	Pin	Definition	Pin	Definition	1	LVDS_3.3V	11	LVDS_DB1-	21	LVDS_DA1+	31	GND	2	LVDS_5V	12	LVDS_DB0-	22	LVDS_DA0+	32	GND	3	LVDS_3.3V	13	GND	23	LVDS_DA1-	33	LVDSA_CLK+	4	LVDS_5V	14	GND	24	LVDS_DA0-	34	LVDSB_CLK+	5	LVDS0_PAL_SCL	15	LVDS_DB3+	25	GND	35	LVDSA_CLK-	6	LVDS0_PAL_SDA	16	LVDS_DB2+	26	GND	36	LVDSB_CLK-	7	GND	17	LVDS_DB3-	27	LVDS_DA3+	37	GND	8	HPD_DET_C	18	LVDS_DB2-	28	LVDS_DA2+	38	GND	9	LVDS_DB1+	19	GND	29	LVDS_DA3-	39	LVDS_12V	10	LVDS_DB0+	20	GND	30	LVDS_DA2-	40	LVDS_12V
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8	HPD_DET_C	18	LVDS_DB2-	28	LVDS_DA2+	38	GND																																																																																						
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22. <b>CN18 : MIPI CSI</b>																																																																																													
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23.	<b>CN20 : I2C Touch Panel</b>  <p>Connector type : HDR 1x6 1.25mm</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>LVDS_TH_RST#</td> </tr> <tr> <td>2</td> <td>LVDS_TH_SCL</td> </tr> <tr> <td>3</td> <td>LVDS_TH_SDA</td> </tr> <tr> <td>4</td> <td>CAP_TCH_INT0</td> </tr> <tr> <td>5</td> <td>GND</td> </tr> <tr> <td>6</td> <td>VCC3V3</td> </tr> </tbody> </table>	Pin	Definition	1	LVDS_TH_RST#	2	LVDS_TH_SCL	3	LVDS_TH_SDA	4	CAP_TCH_INT0	5	GND	6	VCC3V3	24.	<b>CN21 : Digital IO</b>  <p>Connector type : Header 2X5 2.0mm</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>DIO0</td> <td>6</td> <td>DIO5</td> </tr> <tr> <td>2</td> <td>VCC3V3</td> <td>7</td> <td>DIO3</td> </tr> <tr> <td>3</td> <td>DIO1</td> <td>8</td> <td>DIO6</td> </tr> <tr> <td>4</td> <td>DIO4</td> <td>9</td> <td>GND</td> </tr> <tr> <td>5</td> <td>DIO2</td> <td>10</td> <td>DIO7</td> </tr> </tbody> </table>	Pin	Definition	Pin	Definition	1	DIO0	6	DIO5	2	VCC3V3	7	DIO3	3	DIO1	8	DIO6	4	DIO4	9	GND	5	DIO2	10	DIO7
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25.	<b>CN22 : USB2.0 1/2</b>  <p>Connector type : Header 2X5 2.0mm</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>VCC5V</td> <td>6</td> <td>USB1P+</td> </tr> <tr> <td>2</td> <td>VCC5V</td> <td>7</td> <td>GND</td> </tr> <tr> <td>3</td> <td>USB0P-</td> <td>8</td> <td>GND</td> </tr> <tr> <td>4</td> <td>USB1P-</td> <td>9</td> <td>GND</td> </tr> <tr> <td>5</td> <td>USB0P+</td> <td>10</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	Pin	Definition	1	VCC5V	6	USB1P+	2	VCC5V	7	GND	3	USB0P-	8	GND	4	USB1P-	9	GND	5	USB0P+	10	GND	26.	<b>CN23 : Micro SD Card Socket</b>  <p>Connector type : 112A-TAAR-R02</p> <p>SDMMC0_D2 1 DATA2  SDMMC0_D3 2 CD/DATA3  SDMMC0_CMD 3 CMD  USD_3V3 4 VDD  SDMMC0_CLK 5 CLK  6 VSS  SDMMC0_D0 7 DAT0  SDMMC0_D1 8 DAT1  9 S  10 GND  11 S  12 GND</p> <p>VCC3V3_SYS  R380 10K 1%  Card inserted : 0---&gt; G open  Card ejected : 0---&gt; G short</p>														
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4	USB1P-	9	GND																																						
5	USB0P+	10	GND																																						
27.	<b>CN24 : USB Type-C</b>  <p>USB Type-C (NWLUS-166A)</p> <p>A4 VBUS1  A9 VBUS2  B9 VBUS3  B4 VBUS4  A6 DP1  A7 DN1  B6 DP2  B7 DN2  A2 SSTXP1  A3 SSTXN1  B11 SSRXP1  B10 SSRXN1  B2 SSTXP2  B3 SSTXN2  A11 SSRXP2  A10 SSRXN2  A5 CC1 SH1  B5 CC2 SH2  A8 SBU1 SH3  B8 SBU2 SH4  A1 GND1 SH5  A12 GND2 SH6  B12 GND3 SH7  B1 GND4 SH8</p>	28.	<b>CN25 : Backlight power</b>  <p>Connector type : HDR 1x5 2.0mm</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>VCC12V_DCIN</td> </tr> <tr> <td>2</td> <td>GND</td> </tr> <tr> <td>3</td> <td>BL0EN</td> </tr> <tr> <td>4</td> <td>LVDS_PWM</td> </tr> <tr> <td>5</td> <td>VCC5V</td> </tr> </tbody> </table>	Pin	Definition	1	VCC12V_DCIN	2	GND	3	BL0EN	4	LVDS_PWM	5	VCC5V																										
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29.	CN26 : 9904 EEPROM Update Header	30.	CN27 : External 12V Output																				
	 <p>Connector type : HDR Wafer 1x4 2.0mm</p> <table border="1" data-bbox="383 289 618 533"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>NC</td> </tr> <tr> <td>2</td> <td>EE100_SPC</td> </tr> <tr> <td>3</td> <td>EE100_SPD</td> </tr> <tr> <td>4</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	1	NC	2	EE100_SPC	3	EE100_SPD	4	GND		 <p>Connector type : HDR Wafer 1x4 2.5mm</p> <table border="1" data-bbox="1075 289 1252 533"> <thead> <tr> <th>Pin</th> <th>Definition</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>DC_IN_C</td> </tr> <tr> <td>2</td> <td>DC_IN_C</td> </tr> <tr> <td>3</td> <td>GND</td> </tr> <tr> <td>4</td> <td>GND</td> </tr> </tbody> </table>	Pin	Definition	1	DC_IN_C	2	DC_IN_C	3	GND	4	GND
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