

EMX-ADLP

12th Gen Intel® Core™ SoC & Celeron Processor Mini ITX
Motherboard

User's Manual

1st Ed – 06 July 2023

FCC Statement



THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

- (1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.
- (2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRE OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

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Notice

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4. Carefully pack the defective product, a complete Repair and Replacement Order Card and a photocopy proof of purchase date (such as your sales receipt) in a shippable container. A product returned without proof of the purchase date is not eligible for warranty service.
5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

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1. Getting Started

1.1 Safety Precautions

Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

1.2 Packing List

Before you begin installing your single board, please make sure that the following materials have been shipped:

- 1 x EMX-ADLP motherboard
- 1 x SATA cable
- 1 x SATA power cable
- 1 x I/O Shield



If any of the above items is damaged or missing, contact your retailer.

1.3 Document Amendment History

Revision	Date	By	Comment
1 st	July 2023	Avalue	Initial Release

1.4 Manual Objectives

This manual describes in details Avalue Technology EMX-ADLP Single Board.

We have tried to include as much information as possible but we have not duplicated information that is provided in the standard IBM Technical References, unless it proved to be necessary to aid in the understanding of this board.

We strongly recommend that you study this manual carefully before attempting to set up EMX-ADLP or change the standard configurations. Whilst all the necessary information is available in this manual we would recommend that unless you are confident, you contact your supplier for guidance.

Please be aware that it is possible to create configurations within the CMOS RAM that make booting impossible. If this should happen, clear the CMOS settings, (see the description of the Jumper Settings for details).

If you have any suggestions or find any errors regarding this manual and want to inform us of these, please contact our Customer Service department with the relevant details.

1.5 System Specifications

EMX-ADLP	
Product Features	Onboard Alder Lake-P 12th Intel® Core™ SoC & Celeron® BGA Processor (TDP: 15~45W)
	Two 262-pin DDR5 4800MHz SO-DIMM socket, supports up to 64GB Max (non ECC only)
	3 x Intel® I226LM 2.5 Gigabit Ethernet 1 x Intel® I219LM Gigabit Ethernet PHY Realtek ALC888S-VD2-CG TI TPA3113D2PWP Stereo Class-D 6W x 2 Audio Amplifier
	2 x DP++ , 2 x DP, 1 x LVDS, 1 x eDP (Maximum: 6 for quadruple independent displays)
	2 x SATA III, 1 x SATA Power 4 x USB2.0 by pin header 2 x USB3.2 Gen2, 2 x USB 3.0 Gen2 at I/O 4 x RS-232, 2 x RS422/485 (ZT3243 for RS232, MAX13487 Transceiver & SP485 IC for RS422/485)
	1 x M.2 Type B 3042/3052 (with 1 x PCI-e x1 (default), USB2.0, USB 3.2 Gen2 with 1 x SIM card slot support WWAN+GNSS)
	1 x M.2 Key E 2230 support Wi-Fi module and CNVi (1 x PCI-e x1 & USB 2.0 Signal)
	1 x M.2 Key B 2242 (SATA Signal, shared with SATA2) slot support SSD storage
	1 x M.2 Key M 2280 (PCI-e x4) slot for storage NVMe SSD
	H-series: 1 x PCI-e x8 Gen4 with PCI-e x8 slot P/U-series: 1 x PCI-e x4 Gen4 with PCI-e x8 slot
	GPIO 16bit Onboard NuvoTon NPCT754AABYX support SPI TPM 2.0 Support Line-out & Mic-in & Front audio pin-header
	DC in +12V~24V
	Product Specification
CPU	Onboard Alder Lake-P 12th Intel® Core™ SoC i7/i5/i3/Celeron Processor (TDP: 15~45W) H-Series: 25~45W, P-Series: 20~28W, U-Series: 12~15W
BIOS	AMI uEFI BIOS, 256Mbit SPI Flash ROM
I/O Chip	EC-ITE: IT5782VG MAX3243 for RS232, MAX13487 Transceiver & SP485 IC for RS422/485

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System Memory	Two 262-pin DDR5 4800MHz SO-DIMM socket, supports up to 64GB Max (non ECC only)
Watchdog Timer	H/W Reset, 1sec. – 65535sec./min.1sec. or 1min. step
H/W Status Monitor	CPU temperature monitoring Voltage monitoring CPU fan speed control
TPM	Onboard NuvoTon NPCT754AABYX support SPI TPM 2.0
iAMT	Yes
Expansion Slot	
M.2	1 x M.2 Type B 3042/3052 (with 1 x PCI-e x1 (default), USB2.0, USB 3.2 Gen2 with 1 x SIM card slot support WWAN+GNSS 1 x M.2 Key E 2230 support Wi-Fi module and CNVi (1 x PCI-e x1 & USB 2.0 Signal) 1 x M.2 Key B 2242 (SATA Signal, shared with SATA2) slot support SSD storage 1 x M.2 Key M 2280 (PCI-e x4) slot for storage NVMe SSD
PCIe	H-series: 1 x PCI-e x8 Gen4 with PCI-e x8 slot P/U-series: 1 x PCI-e x4 Gen4 with PCI-e x8 slot
Storage	
M.2	1 x M.2 Key B 2242 (SATA Signal, shared with SATA2) slot support SSD storage 1 x M.2 Key M 2280 (PCI-e x4) slot for storage NVMe SSD
SATA	2 x SATA III *SATA 2 share with M.2 key B SATA
Edge I/O	
LAN	1 x Intel® I219LM Gigabit Ethernet PHY (LAN1) 3 x Intel® I226LM 2.5 Gigabit Ethernet (LAN2~4) *LAN3~4 for Full height
USB 3.1	2 x USB 3.2 Gen2 (USB2), 2 x USB 3.0 Gen.2 (USB1)
DP	2 x DP(DP3~4), 1 x DP++(DP2), 1 x DP++(DP1) or 1 x eDP, 1 x LVDS (Maximum: 6 for quadruple independent displays)
DC Input	Mini Din 4-pin DC in Jack
Onboard I/O	
COM	2 x 2 x 3 pin, pitch 2.00mm connector jumper for RS-422/485 4 x 2 x 5 pin, pitch 2.00mm connector support RS-232 (JCOM1,JCOM2,JCOM3,JCOM4) For 2 x RS/232 (JCOM1 & JCOM2)/RS422/485, 2 x RS-232 *MAX3243 for RS232, MAX13487 Transceiver & SP485 IC for RS422/485
USB 2.0	2 x 2 x 5 pin, pitch 2.54mm connector for 4 USB 2.0 (JUSB3,JUSB4)

GPIO	1 x 2 x 10 pin, pitch 2.00mm connector for GPIO: 16 bits & +3.3S Level SMBus (JDIO1)
SATA Power	1 x SATA Power (SPWR1)
CPU/System FAN	1 x 1 x 4 pin, pitch 2.54mm CPU fan connector with smart fan function supported (CPU_FAN1)
Buzzer	Onboard Buzzer (BZ1)
Front Panel	2 x 2 x 5 pin, pitch 2.54mm connector for front panel
RTC Battery	2 x 1 x 2 Pin Pitch 1.25mm horizontal type battery connector (CR2032 Battery)
AT/ATX Selector	1 x 1 x 3 pin pitch 2.54mm connector for AT/ATX jumper
Clear CMOS	1 x 3 pin, pitch 2.00mm connector for CMOS clear
LVDS	1 x 2 x 20 pin, pitch 1.25mm connector for LVDS
LCD Backlight Brightness	2 x 1 x 5 pin, pitch 2.00mm Wafer connector for LCD inverter backlight connector (5V/12V) (JBKL1,2) 1 x 1 x 3 pin, pitch 2.00mm connector LCD backlight brightness adjustment (PWM/DC)
LCD Inverter	2 x 1 x 5 pin, pitch 2.00mm Wafer connector for LCD inverter backlight connector
BIOS SPI	1 x 2 x 4 pin, pitch 2.00mm connector for BIOS SPI (JSPI1)
eSPI	1 x 2 x 6 pin, pitch 2.00mm connector for eSPI debug (JESPI1)
EC Debug	1 x 2 x 6 pin, pitch 2.00mm connector for EC SPI (JEC1)
Audio	1 x 2 x 6 pin, pitch 2.00mm connector for front Audio (JFAUD1)
DC-Input	Mini Din 4-pin DC in Jack
Amp Connector	1 x 4 pin, pitch wafer 2.00mm connector for 6W x 2 Speaker (SPK1)
Other	1 x 2 x 10 pin, pitch 1.25mm connector for eDP 1 x 2 x 2 pin, pitch 4.20mm connector for power input connector (PWR1)
Display	
Graphic Chipset	Intel® Iris® Xe Graphics (For Core i5 & i7) Intel® UHD Graphics for 12th Gen Intel® Processors (For Core i3 & Celeron)
Spec. & Resolution	DP1+DP3(DP1.4): Max: 8192 x 4320@60 Hz DP2+DP4 (DP1.4): Max: 8192 x 4320@60 Hz Single DP port (2021/2 Lab test): 5120 x 3200@60 Hz Note: This resolution is actual test result. Intel resolution as below: 2 x DP++ (DP1+DP2): 1920 x 1080@60 Hz 2 x DP ((DP3+DP4)): 5120 x 3200@60 Hz LVDS: 1920 x 1080 Dual channel 18/24-bits LVDS (Chrontel CH7511B eDP to LVDS) eDP: 5120 x 3200@60 Hz *2 x DP++, LVDS, eDP Default for Thin Mini ITX, Optional: DP3 and DP4 for full

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	height (Maximum: 6 for quadruple independent displays)
Multiple Display	Quadruple Independent Display: 2 x DP++, LVDS, eDP (Thin Mini ITX) Six display for quadruple Independent Display : 2 x DP++, LVDS, eDP, 2 x DP (Full height Mini ITX)
Audio	
Audio Codec	Realtek ALC888S-VD2-CG
Amplifier	TI TPA3113D2PWP Stereo Class-D 6W x 2 Audio Amplifier
Ethernet	
LAN Chipset	1 x Intel® I219LM Gigabit Ethernet PHY (LAN1) 3 x Intel® I225LM co-lay I226LM 2.5 Gigabit Ethernet (LAN2~4) *LAN3~4 for Full height
LAN Spec.	10/100/1000 Base-Tx GbE compatible & 2.5 Gigabit Ethernet
Mechanical & Environmental Specification	
Power Requirement	DC in +12V ~ +24V
ACPI	Single power ATX Support S0, S3, S4, S5 ACPI 5.0 Compliant
Power Mode	AT / ATX mode Switchable Through Jumper
Operating Temp.	0~60C (32~140°F) w/HDD/SSD, ambient with 0.5 m/s Air flow
Storage Temp.	-40~ +75°C
Operating Humidity	40°C @ 95% Relative Humidity, Non-condensing
Size (L x W)	6.7" x 6.7" (170mm x 170mm)
Weight	0.88lbs (0.4kg)
OS Information	Win11 64bit, Win10 64bit. Linux
Power Requirement	DC in +12V ~ +24V
ACPI	Single power ATX Support S0, S3, S4, S5 ACPI 5.0 Compliant

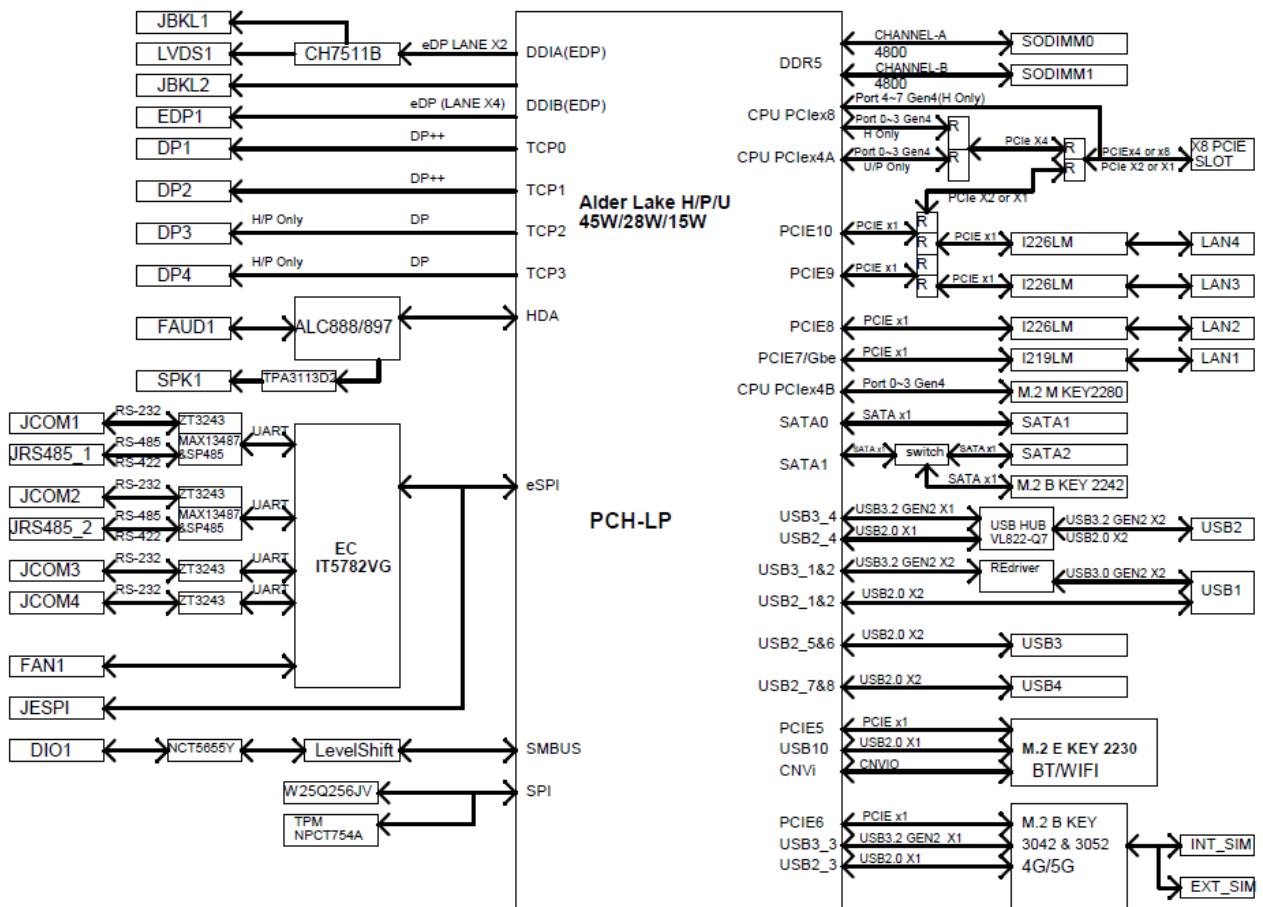


Note:

Specifications are subject to change without notice.

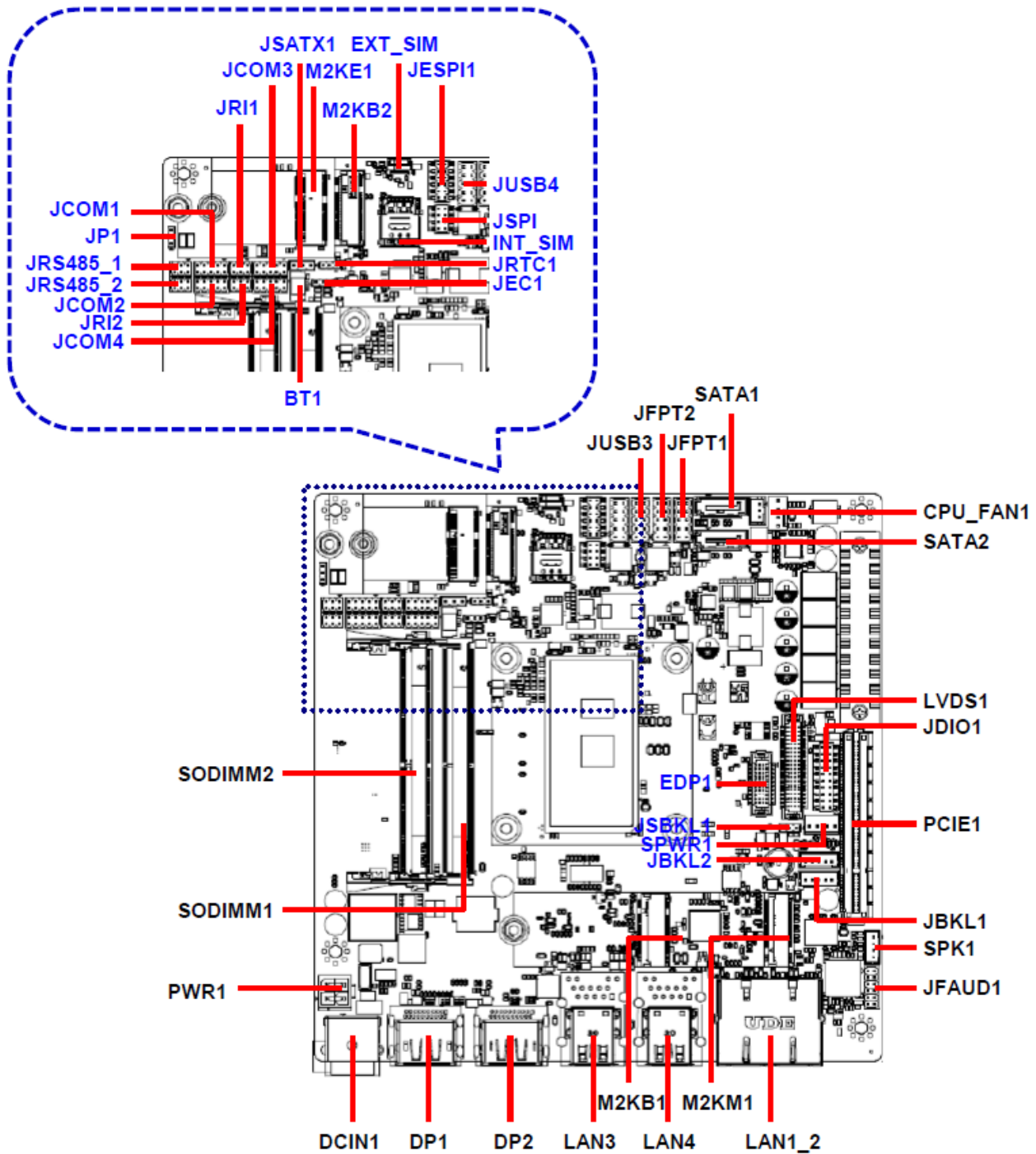
1.6 Architecture Overview—Block Diagram

The following block diagram shows the architecture and main components of EMX-ADLP.



2. Hardware Configuration

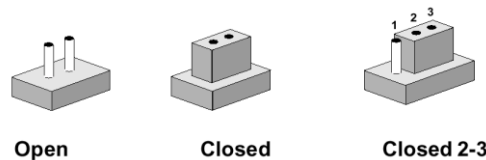
2.1 Product Overview



2.2 Jumper and Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

The following tables list the function of each of the board's jumpers and connectors.

Jumpers

Label	Function	Note
JRI1/2	Serial port 1/2 pin9 signal select	3 x 2 header, pitch 2.00mm
JSBKL1	LVDS Back Light power selection	3 x 1 header, pitch 2.00mm
JSATX1	AT/ATX Power Mode Select	3 x 1 header, pitch 2.54mm
JP1	M2KB2 Voltage setting	3 x 1 header, pitch 2.00mm
JRTC1	Clear CMOS	3 x 1 header, pitch 2.00mm

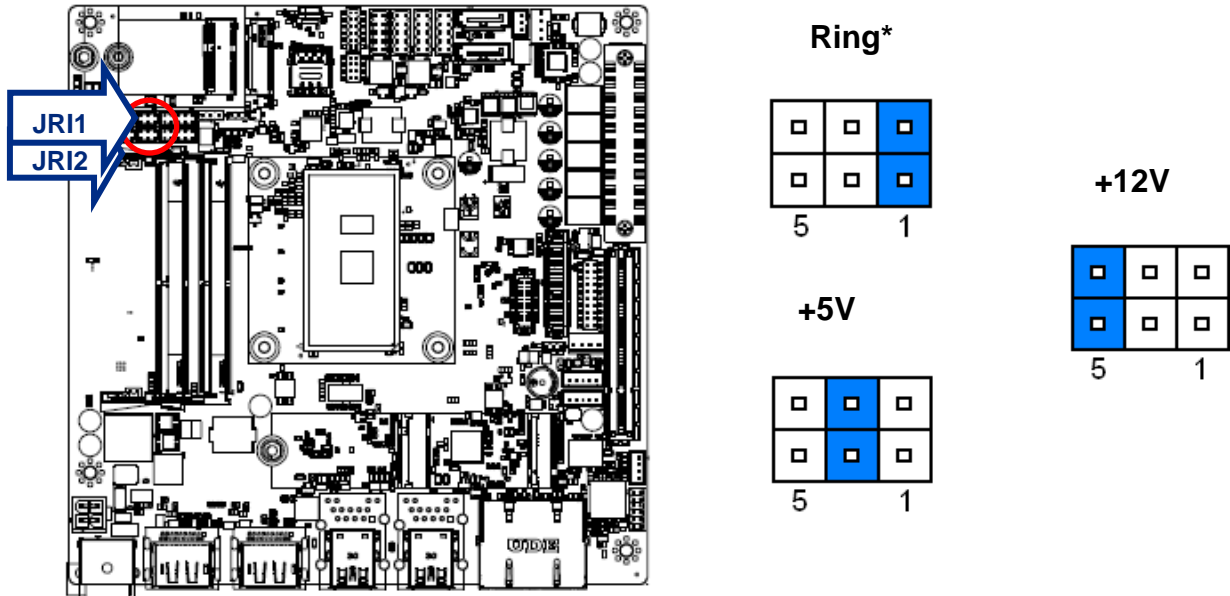
Connectors

Label	Function	Note
JFPT1/2	Miscellaneous setting connector 1/2	5 x 2 header, pitch 2.54mm
SODIMM1/2	206-pin DDR4 SO-DIMM socket	
JFAUD1	Front Audio connector	6 x 2 header, pitch 2.00mm
JBKL1/2	LCD Inverter connector	5 x 1 wafer, pitch 2.00mm

JSPI1	SPI connector	4 x 2 header, pitch 2.00mm
JESPI1	JESPI connector	6 x 2 header, pitch 2.00mm
JCOM1/2/3/4	Serial Port 1/2/3/4 connector	5 x 2 header, pitch 2.00mm
JDIO1	General purpose I/O connector	10 x 2 header, pitch 2.00mm
SPK1	Speaker connector	4 x 1 wafer, pitch 2.00mm
LVDS1	LVDS Connector	20 x 2 wafer, pitch 1.25mm
EDP1	eDP_Panel connector	10 x 2 wafer, pitch 1.25mm
JUSB3/4	USB connector 3/4	5 x 2 header, pitch 2.54mm
LAN1/2/3/4	RJ-45 Ethernet 1/2/3/4	
BT1	Battery connector	2 x 1 wafer, pitch 1.25mm
M2KE1	M.2 2230 Type E Slot	
M2KB1	M.2 2242 Type B Slot	
M2KB2	M.2 3052/3042/2242 Type B Slot	
DP1/2	DP connector 1/2	
JRS485_1/2	Serial Port 1/2 RS485/422 Mode connector	3 x 2 header, pitch 2.00mm
JEC1	JEC connector	3 x 1 header, pitch 2.00mm
DCIN1	DC Power-in connector	
PWR1	Power connector	2 x 2 wafer, pitch 4.20mm
SATA1/2	Serial ATA connector 1/2	
SPWR1	SATA Power connector 1	4 x 1 wafer, pitch 2.54mm
EXT_SIM	SIM card slot	Can Only use either one slot, not both at the same time.
INT_SIM	SIM card slot	
CPU_FAN1	CPU fan connector	4 x 1 wafer, pitch 2.54mm
PCIE1	PCIe connector	

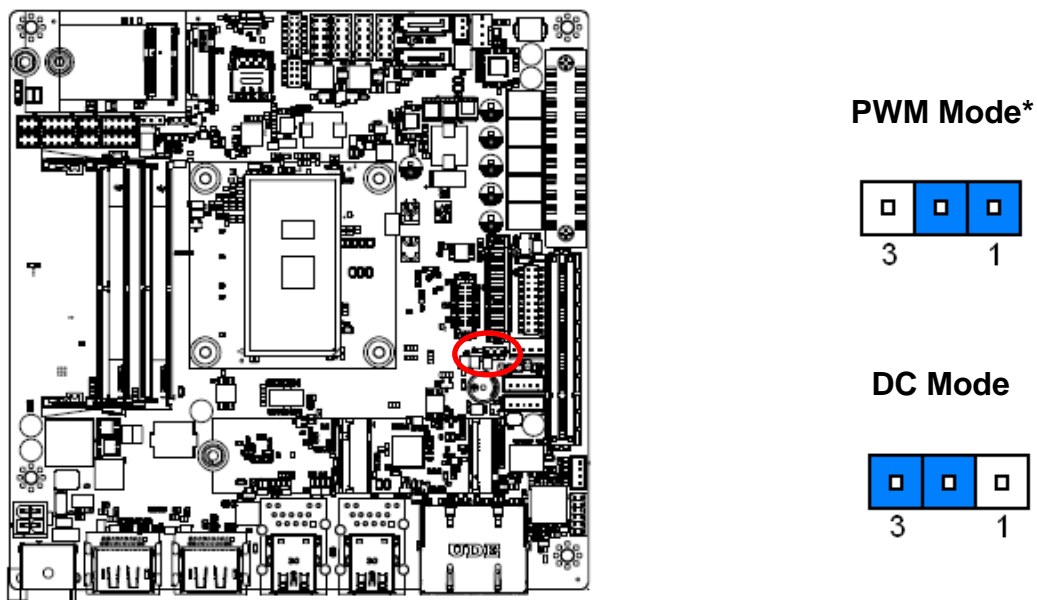
2.3 Setting Jumpers & Connectors

2.3.1 Serial port 1/2 pin9 signal select (JRI1/JRI2)



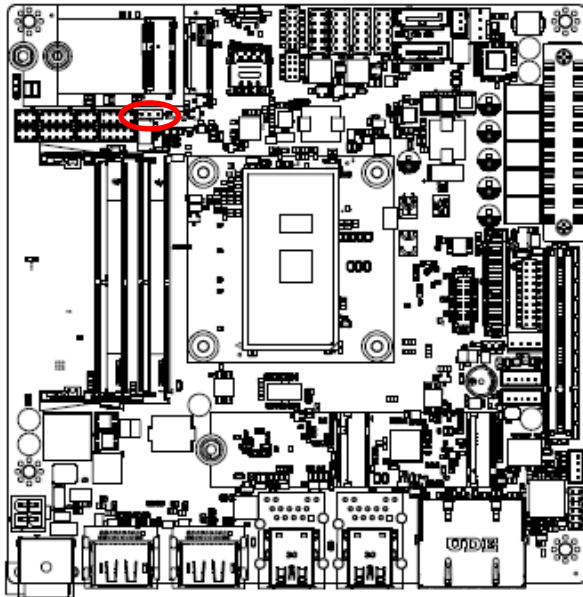
* Default

2.3.2 LVDS Back Light power selection (JSBKL1)

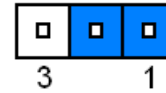


* Default

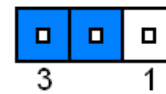
2.3.3 AT/ATX Power Mode Select (JSATX1)



ATX*

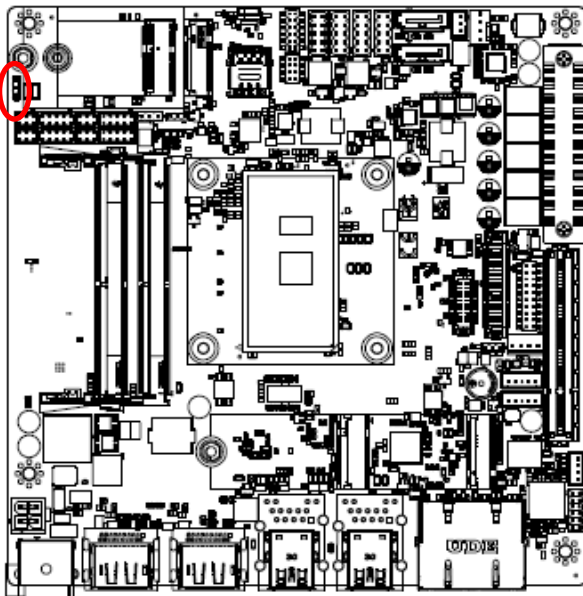


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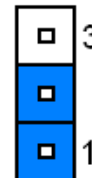


* Default

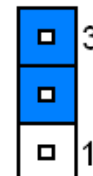
2.3.4 M2KB2 Voltage setting (JP1)



+3.8V

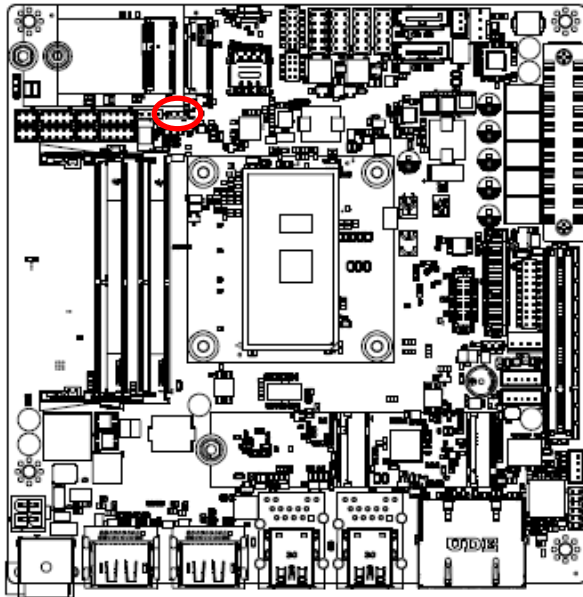


+3.3V*

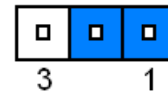


* Default

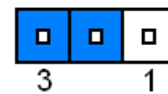
2.3.5 Clear CMOS (JRTC1)



Protect*

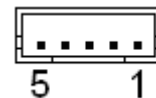
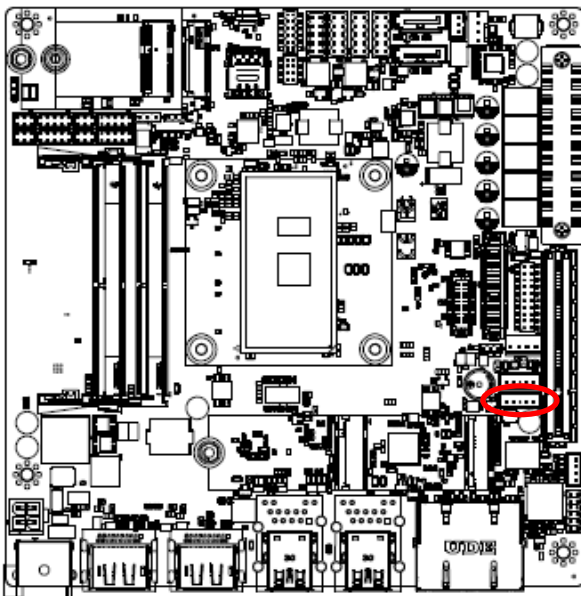


Clear CMOS



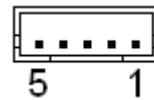
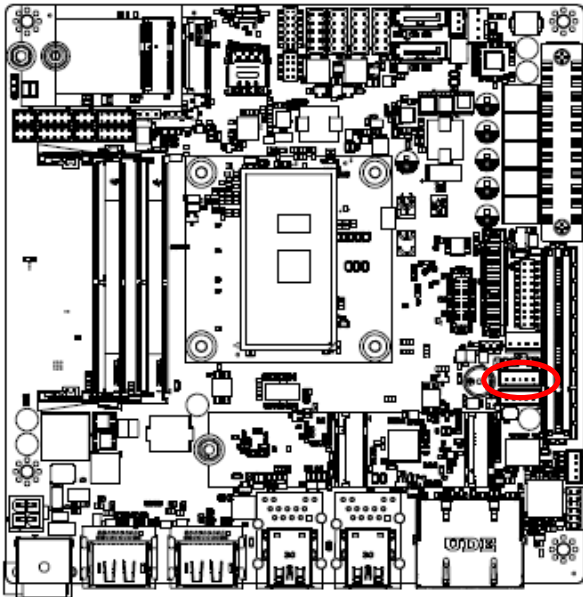
* Default

2.3.6 LCD Inverter connector (JBKL1)



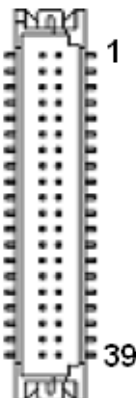
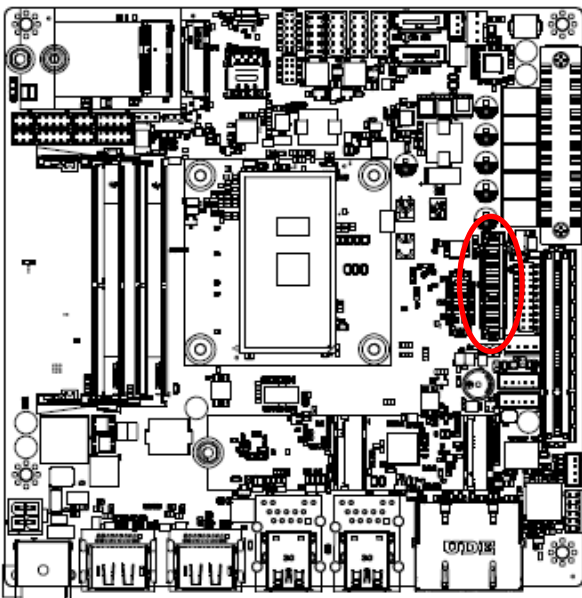
PIN	Signal
1	+12V
2	GND
3	LVDS_BKLT_EN
4	LVDS_BKLTCTL
5	+5V

2.3.7 LCD Inverter connector (JBKL2)



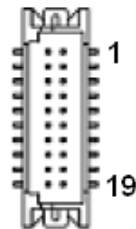
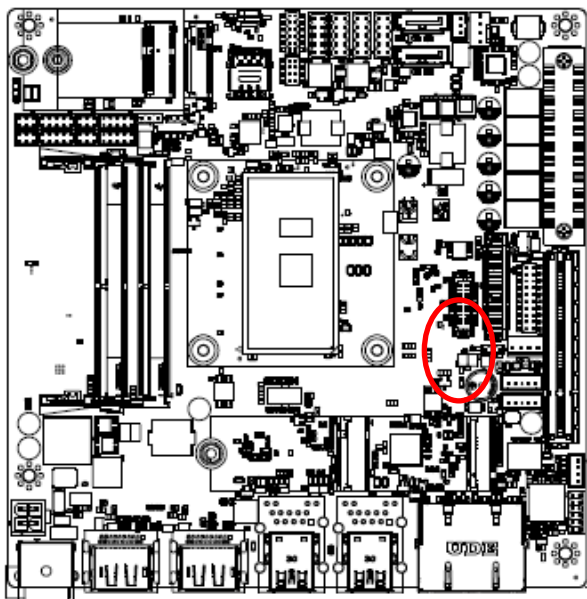
PIN	Signal
1	+12V
2	GND
3	EDP2_BKLTEN
4	EDP2_BKLT_CTL
5	+5V

2.3.8 LVDS connector (LVDS1)



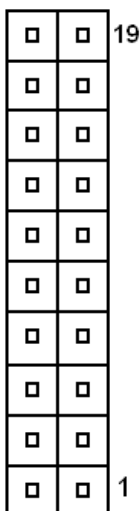
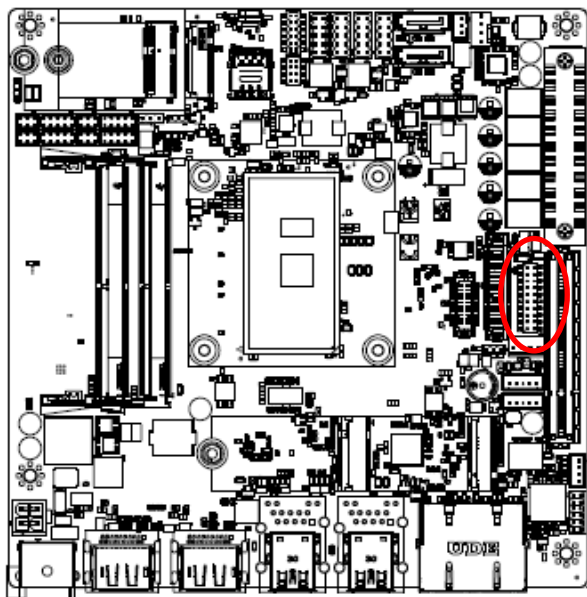
Signal	PIN	PIN	Signal
+V5S_LVDS	2	1	+ V3.3S_LVDS
+V5S_LVDS	4	3	+ V3.3S_LVDS
+V5S_LVDS	6	5	+ V3.3S_LVDS
GND	8	7	GND
LVDS_DATA0_P	10	9	LVDS_DATA1_P
LVDS_DATA0_N	12	11	LVDS_DATA1_N
GND	14	13	GND
LVDS_DATA2_P	16	15	LVDS_DATA3_P
LVDS_DATA2_N	18	17	LVDS_DATA3_N
GND	20	19	GND
LVDS_DATA4_P	22	21	LVDS_DATA5_P
LVDS_DATA4_N	24	23	LVDS_DATA5_N
GND	26	25	GND
LVDS_DATA6_P	28	27	LVDS_DATA7_P
LVDS_DATA6_N	30	29	LVDS_DATA7_N
GND	32	31	GND
LVDS_CLK1_P	34	33	LVDS_CLK2_P
LVDS_CLK1_N	36	35	LVDS_CLK2_N
GND	38	37	GND
+V12S_LVDS	40	39	+V12S_LVDS

2.3.9 eDP_Panel connector (EDP1)



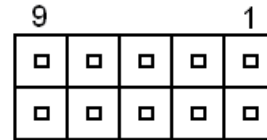
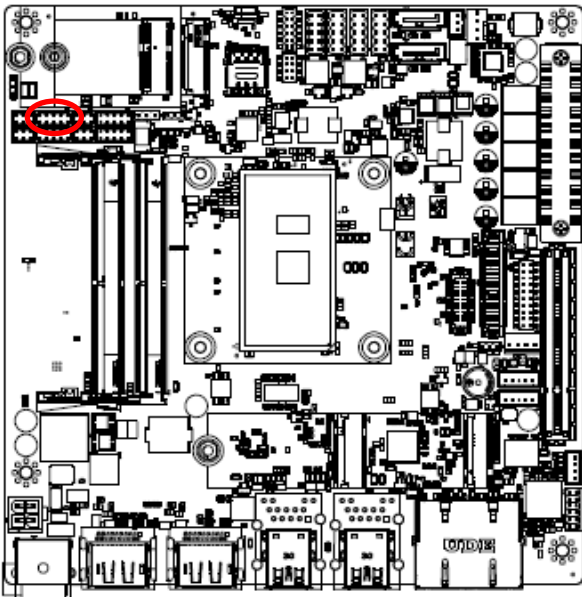
Signal	PIN	PIN	Signal
GND	1	2	GND
EDP_PANEL_TXN0	3	4	EDP_PANEL_TXN3
EDP_PANEL_TXP0	5	6	EDP_PANEL_TXP3
GND	7	8	NC
EDP_PANEL_TXN1	9	10	GND
EDP_PANEL_TXP1	11	12	EDP_PANEL_AUXN
GND	13	14	EDP_PANEL_AUXP
EDP_PANEL_TXN2	15	16	GND
EDP_PANEL_TXP2	17	18	EDP_PANEL_HPDP
+V35_EDP	19	20	+V35_EDP

2.3.10 General purpose I/O connector (JDIO1)



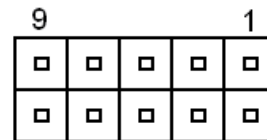
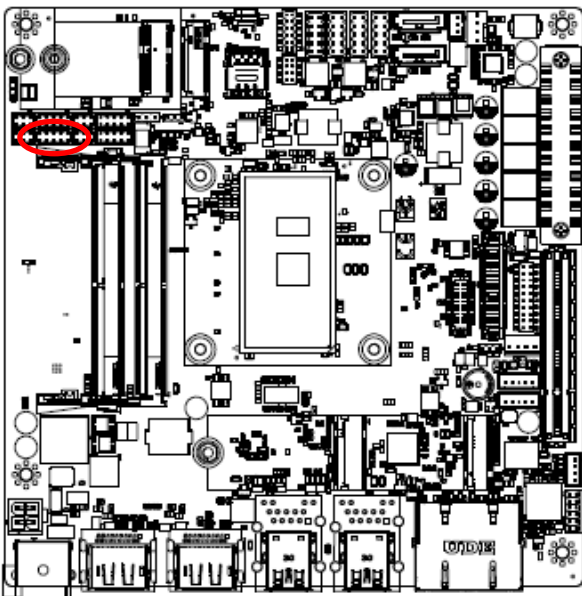
Signal	PIN	PIN	Signal
DI0	1	2	DO0
DI1	3	4	DO1
DI2	5	6	DO2
DI3	7	8	DO3
DI4	9	10	DO4
DI5	11	12	DO5
DI6	13	14	DO6
DI7	15	16	DO7
SMB_SCL_ S0_3P3EXT	17	18	SMB_SDA_ S0_3P3EXT
GND	19	20	+5V (Max current = 0.5A)

2.3.11 Serial port1 connector (JCOM1)



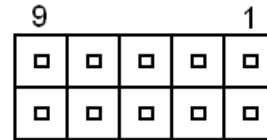
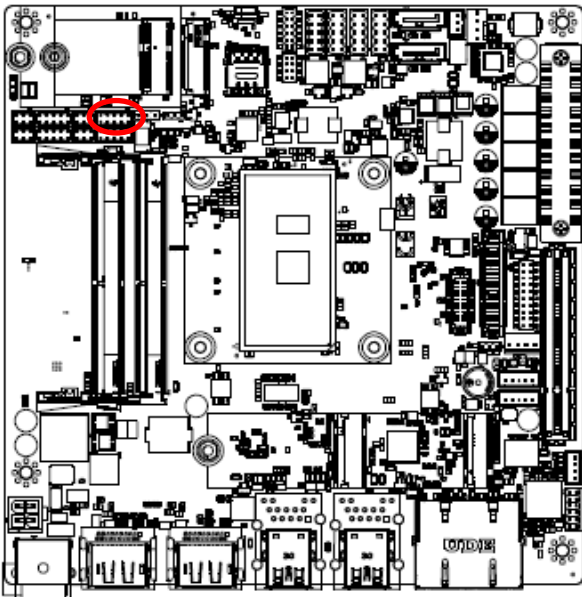
Signal	PIN	PIN	Signal
COM_DCD#_1	1	2	COM_RXD_1
COM_TXD_1	3	4	COM_DTR#_1
GND	5	6	COM_DSR#_1
COM_RTS#_1	7	8	COM_CTS#_1
COM_RI#_1	9	10	NC

2.3.12 Serial port2 connector (JCOM2)



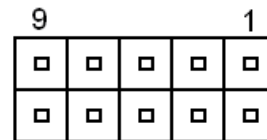
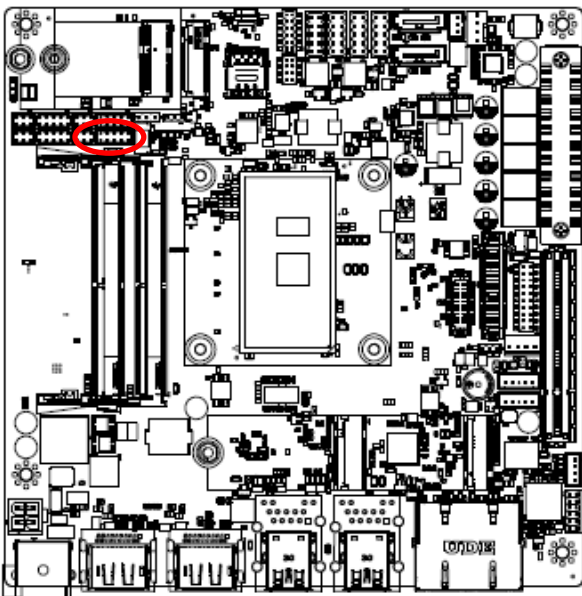
Signal	PIN	PIN	Signal
COM_DCD#_2	1	2	COM_RXD_2
COM_TXD_2	3	4	COM_DTR#_2
GND	5	6	COM_DSR#_2
COM_RTS#_2	7	8	COM_CTS#_2
COM_RI#_2	9	10	NC

2.3.13 Serial port3 connector (JCOM3)



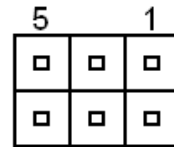
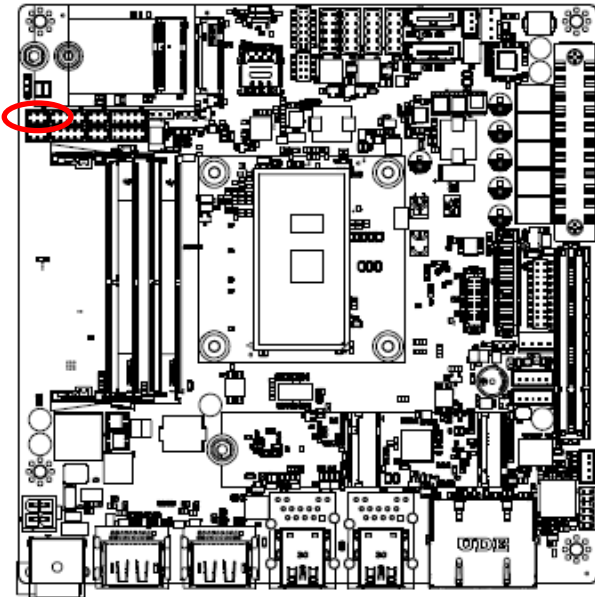
Signal	PIN	PIN	Signal
COM_DCD#_3	1	2	COM_RXD_3
COM_TXD_3	3	4	COM_DTR#_3
GND	5	6	COM_DSR#_3
COM_RTS#_3	7	8	COM_CTS#_3
COM_RI#_3	9	10	NC

2.3.14 Serial port4 connector (JCOM4)



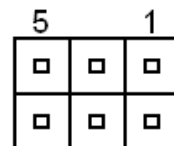
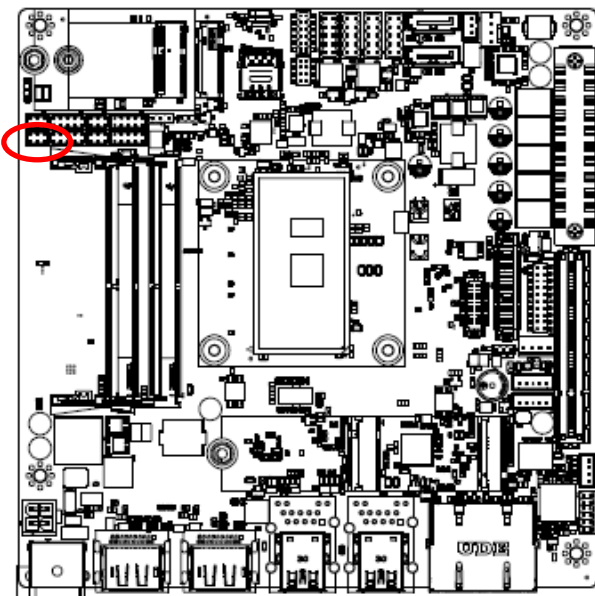
Signal	PIN	PIN	Signal
COM_DCD#_4	1	2	COM_RXD_4
COM_TXD_4	3	4	COM_DTR#_4
GND	5	6	COM_DSR#_4
COM_RTS#_4	7	8	COM_CTS#_4
COM_RI#_4	9	10	NC

2.3.15 Serial Port 1 RS485/422 Mode connector (JRS485_1)



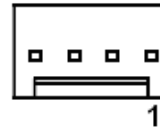
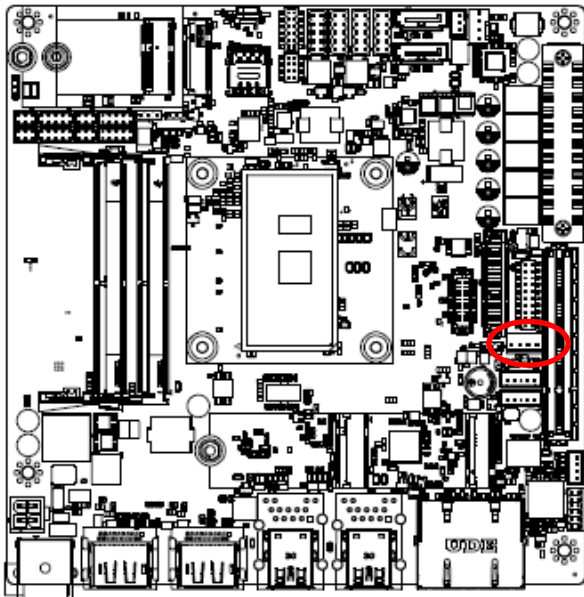
Signal	PIN	PIN	Signal
485_422TX1-	1	2	485_422TX1+
485RX1+	3	4	485RX1-
+5V (Max current = 0.5A)	5	6	GND

2.3.16 Serial Port 2 RS485/422 Mode connector (JRS485_2)



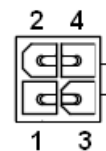
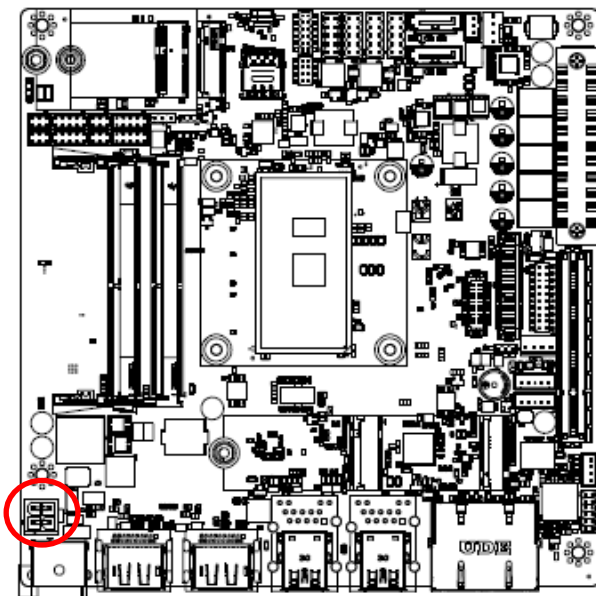
Signal	PIN	PIN	Signal
485_422TX2-	1	2	485_422TX2+
485RX2+	3	4	485RX2-
+5V	5	6	GND

2.3.17 SATA Power connector 1 (SPWR1)



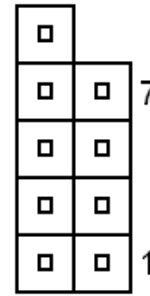
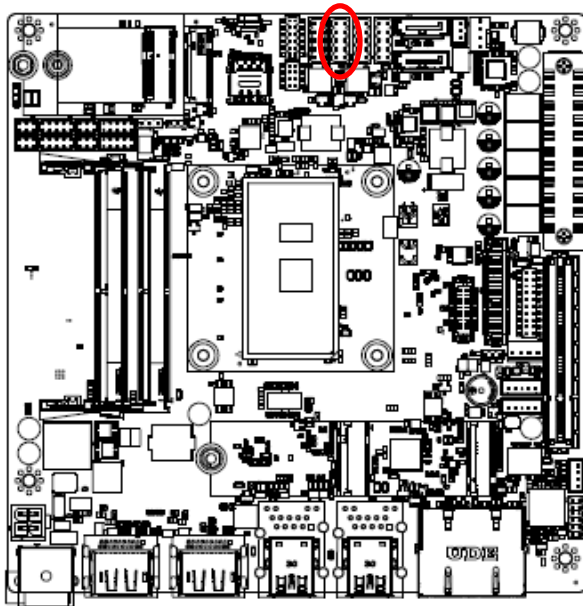
PIN	Signal
1	+V5S_SATA
2	GND
3	GND
4	+V12S_SATA

2.3.18 Power connector (PWR1)



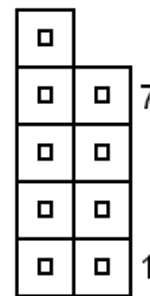
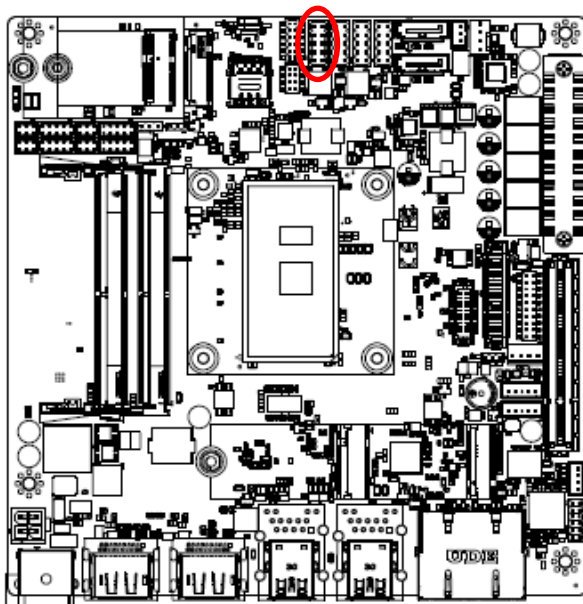
Signal	PIN	PIN	Signal
GND	2	4	+VIN
GND	1	3	+VIN

2.3.19 USB connector 3 (JUSB3)



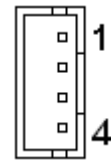
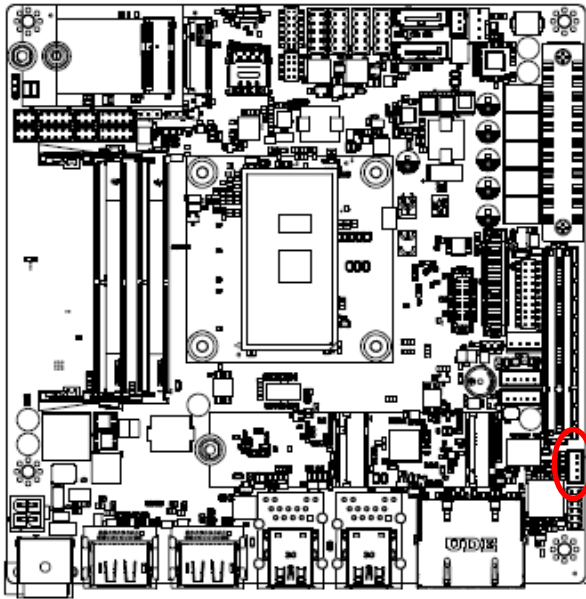
Signal	PIN	PIN	Signal
+V5A_USB56	1	2	+V5A_USB56
USB_DN5	3	4	USB_DN6
USB_DP5	5	6	USB_DP6
GND	7	8	GND
		10	GND

2.3.20 USB connector 4 (JUSB4)



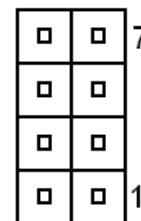
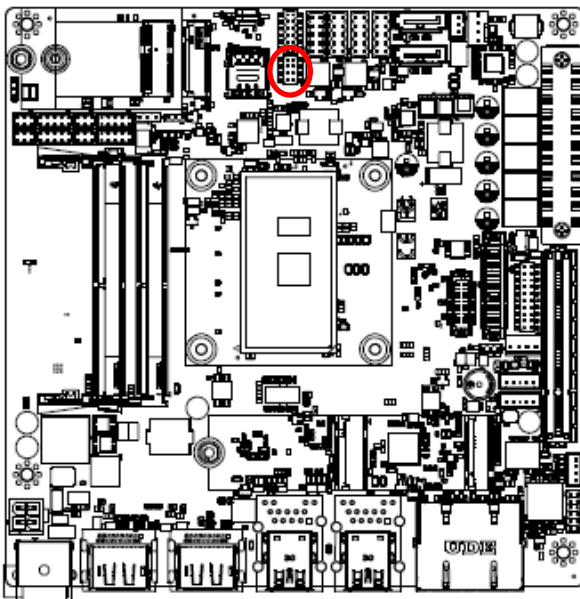
Signal	PIN	PIN	Signal
+V5A_USB78	1	2	+V5A_USB78
USB_DN7	3	4	USB2_DN8
USB_DP7	5	6	USB2_DP8
GND	7	8	GND
		10	GND

2.3.21 Speaker connector (SPK1)



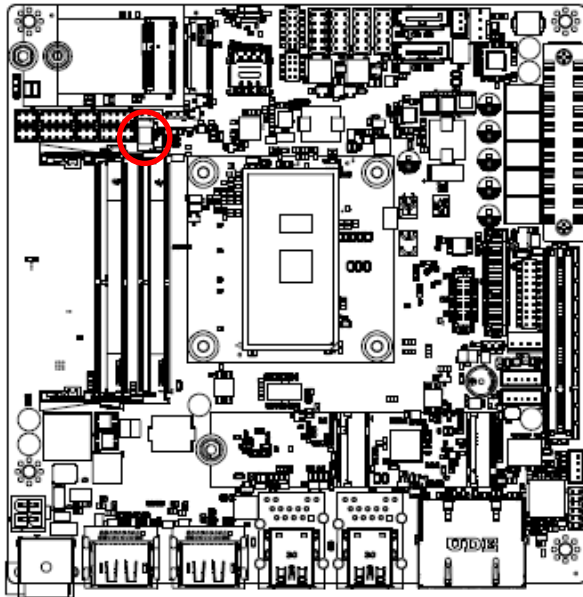
Signal	PIN
SPK_L+	1
SPK_L-	2
SPK_R+	3
SPK_R-	4

2.3.22 SPI connector (JSPI)



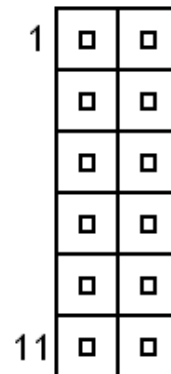
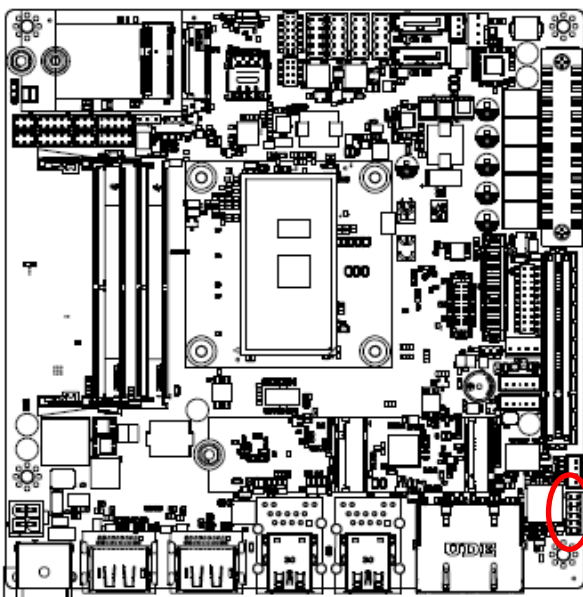
Signal	PIN	PIN	Signal
BIOS_WP#	8	7	BIOS_HOLD#
SPI0_BIOS_MOSI	6	5	SPI0_BIOS_MISO
SPI0_BIOS_CLK	4	3	SPI0_CS0#
GND	2	1	+V3.3A_1.8A_SPI

2.3.23 Battery connector (BT1)



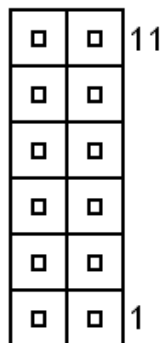
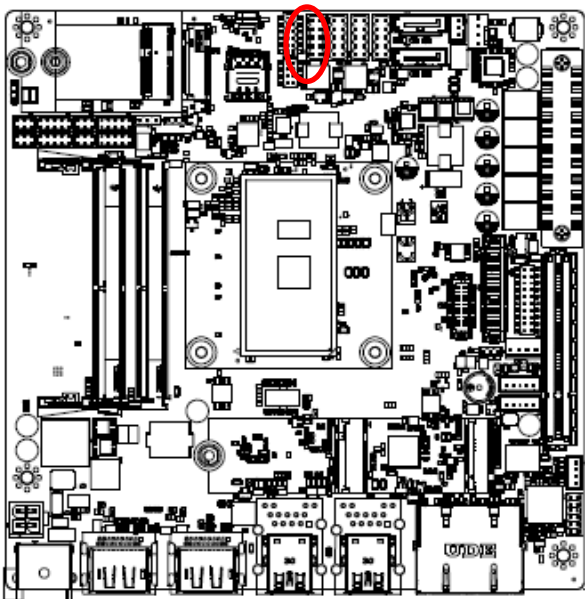
PIN	Signal
1	+RTCBAT
2	GND

2.3.24 Audio connector (JFAUD1)



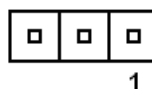
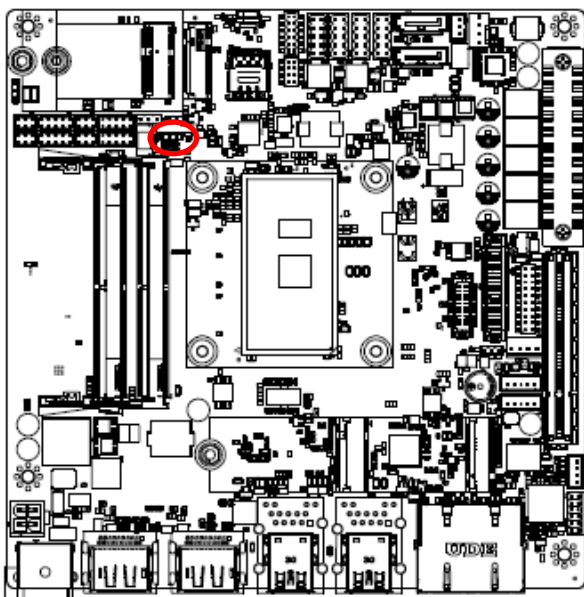
Signal	PIN	PIN	Signal
LINEOUT_R	1	2	LINEOUT_L
GND_AUD	3	4	GND_AUD
LINEIN_R	5	6	LINEIN_L
MICIN_R	7	8	MICIN_L
LINEOUT1_JD	9	10	LINE1-JD
MIC1_JD	11	12	GND_AUD

2.3.25 JESPI1 connector (JESPI1)



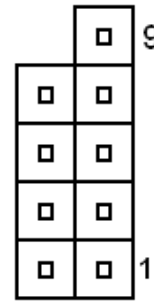
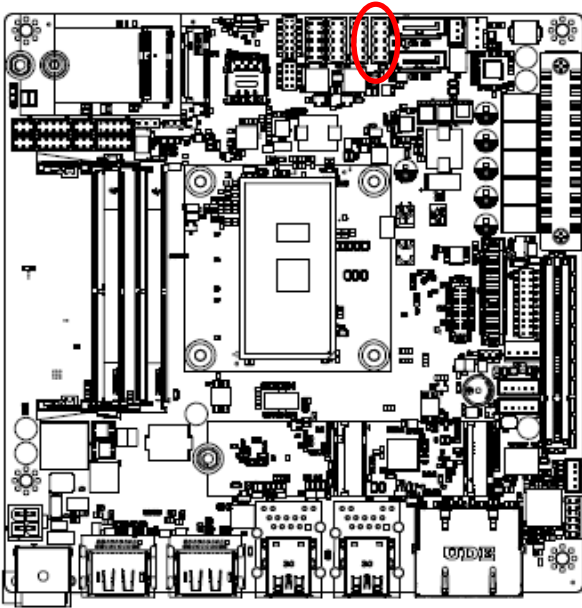
Signal	PIN	PIN	Signal
ESPI_ALER1#	12	11	ESPI_RST#
GND	10	9	ESPI_CS1#
ESPI_CLK_80P	8	7	ESPI_IO3_80P
ESPI_CS#	6	5	ESPI_IO2_80P
PLT_BUF_RST#	4	3	ESPI_IO1_80P
+3.3V	2	1	ESPI_IO0_80P

2.3.26 JEC1 connector (JEC1)



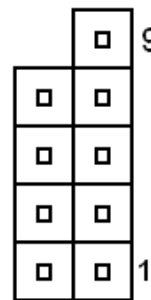
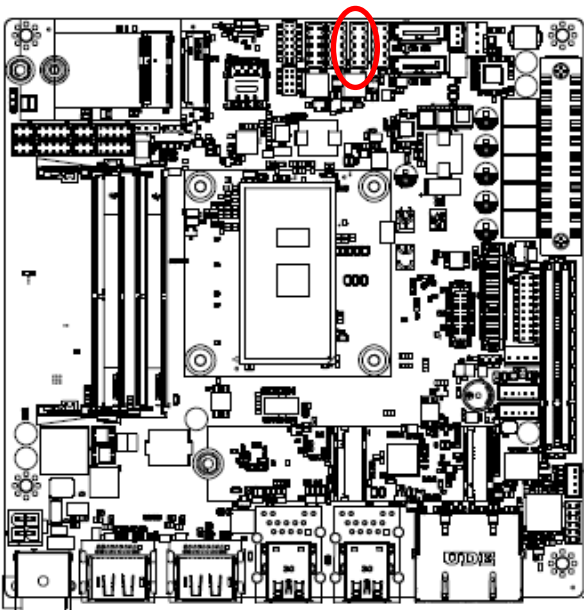
Signal	PIN
EC_SMDAT_DEBUG	1
EC_SMCLK_DEBUG	2
GND	3

2.3.27 Miscellaneous setting connector 1 (JFPT1)



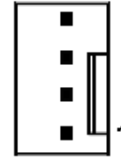
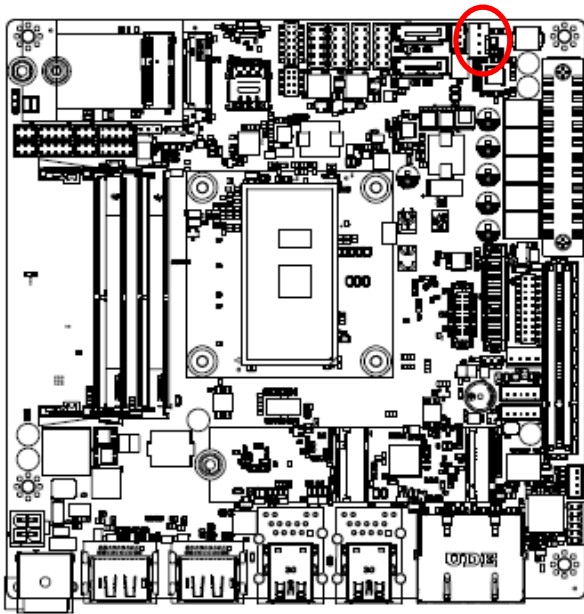
Signal	PIN	PIN	Signal
		9	NC
-PWR_BNT	8	7	-Reset
+PWR_BNT	6	5	+Reset
-PWR_LED	4	3	-HD_LED
+PWR_LED	2	1	+HD_LED

2.3.28 Miscellaneous setting connector 2 (JFPT2)



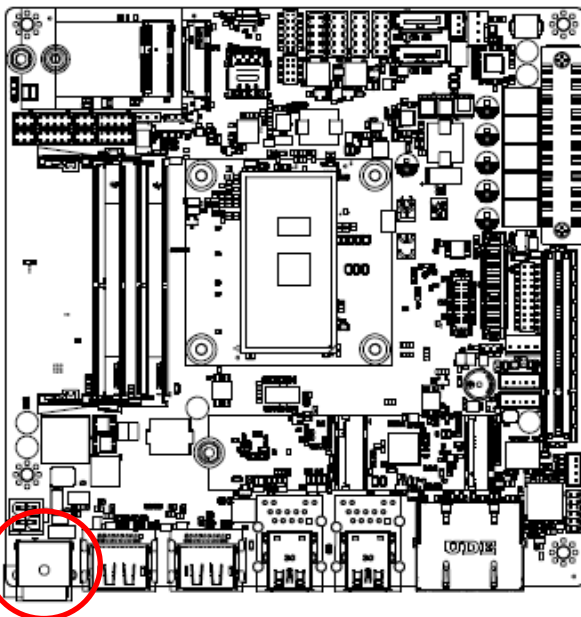
Signal	PIN	PIN	Signal
		9	NC
GND	8	7	Speaker-
BLK_DN	6	5	NC
BLK_UP	4	3	NC
BLK_VR(10K)	2	1	Speaker+

2.3.29 CPU fan connector (CPU_FAN1)



Signal	PIN
FAN_PWM0	4
CPU_FANIN	3
+12V	2
GND	1

2.3.30 Power connector (DCIN1)



Signal	PIN	PIN	Signal
+VIN_12-24V	1	2	+VIN_12-24V
GND	3	4	GND

Note: Vin = 12~24V

3. BIOS Setup

3.1 Introduction

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

3.2 Starting Setup

The AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the NVRAM and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

By pressing or <F2> immediately after switching the system on, or

By pressing the or <F2> key when the following message appears briefly at the left-top of the screen during the POST (Power On Self Test).

Press or <F2> to enter SETUP

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to.

Press F1 to Continue, DEL to enter SETUP

3.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Button	Description
↑↓→←	Move
Enter	Select
+/-	Value
Esc	Exit
F1	General Help
F2	Previous Values
F3	Optimized Defaults
F4	Save & Exit Setup
<K>	Scroll help area upwards
<M>	Scroll help area downwards

- **Navigating Through The Menu Bar**

Use the left and right arrow keys to choose the menu you want to be in.



Note: Some of the navigation keys differ from one screen to another.

- **To Display a Sub Menu**

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A "➤" pointer marks all sub menus.

3.4 Getting Help

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or <Enter> key.

3.5 In Case of Problems

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the AMI BIOS supports an override to the NVRAM settings which resets your system to its defaults.

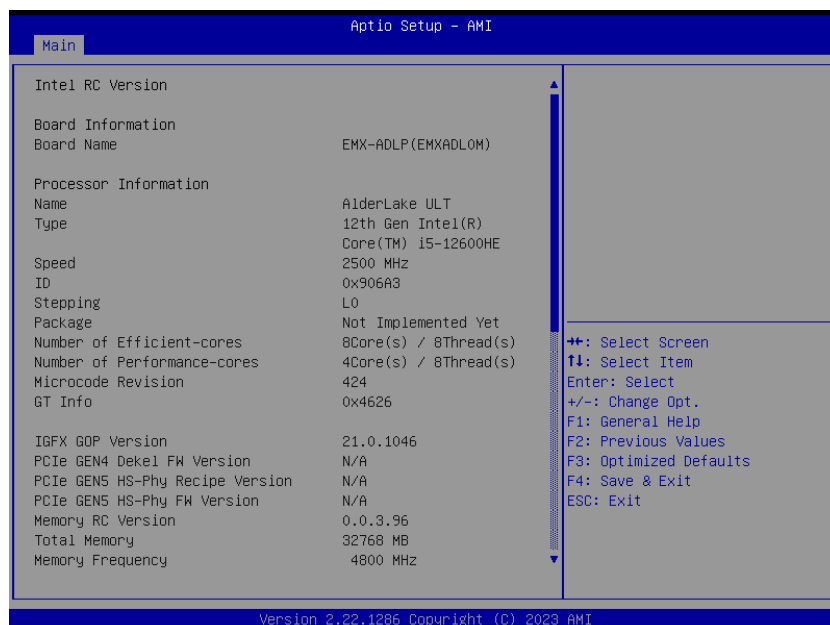
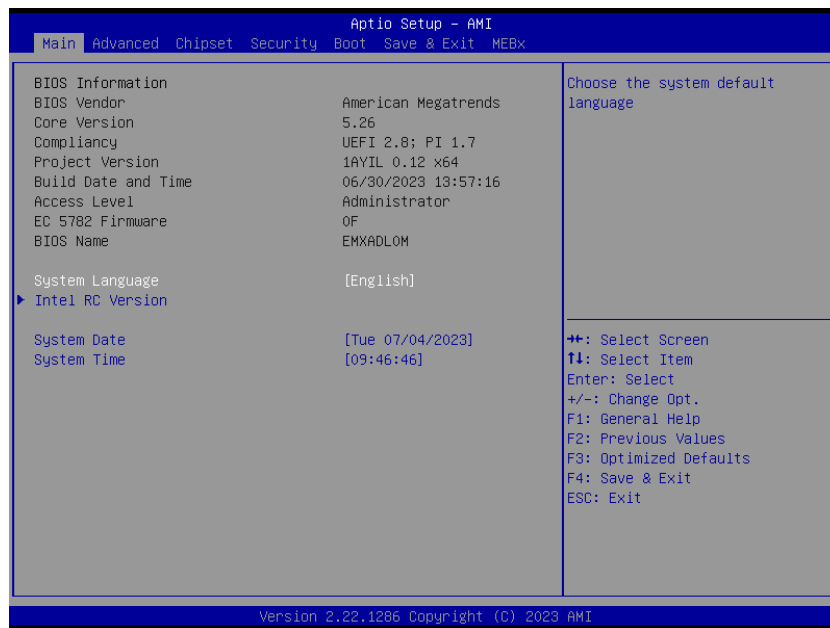
The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both BIOS Vendor and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

3.6 BIOS setup

Once you enter the Aptio Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

3.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.



EMX-ADLP User's Manual

3.6.1.1 System Language

This option allows choosing the system default language.

3.6.1.2 System Date

Use the system date option to set the system date. Manually enter the day, month and year.

3.6.1.3 System Time

Use the system time option to set the system time. Manually enter the hours, minutes and seconds.

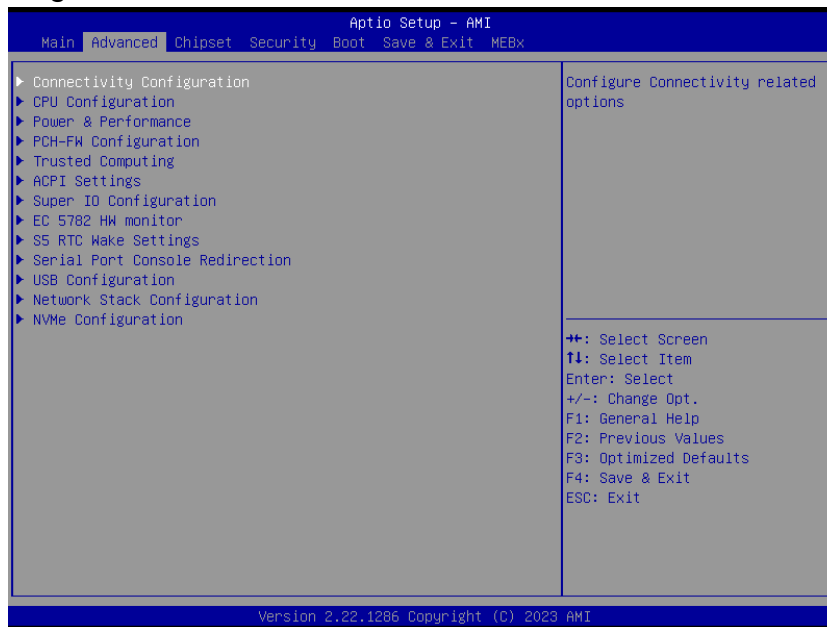


Note: The BIOS setup screens shown in this chapter are for reference purposes only, and may not exactly match what you see on your screen.

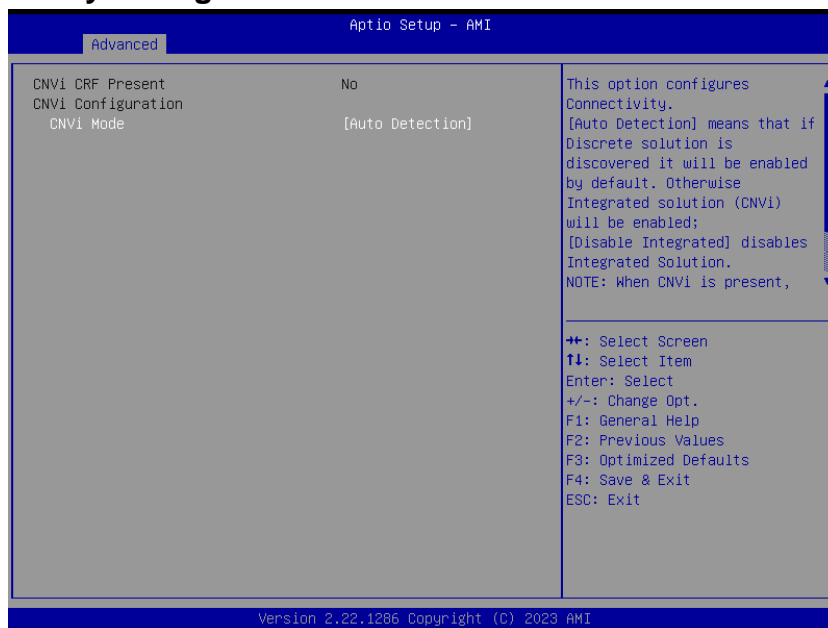
Visit the Avalue website (www.avalue.com.tw) to download the latest product and BIOS information.

3.6.2 Advanced Menu

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.

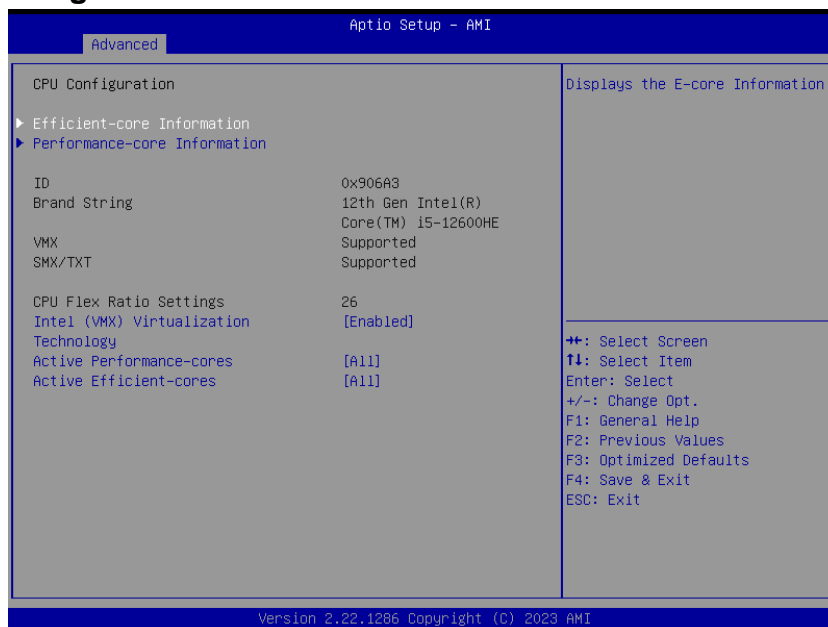


3.6.2.1 Connectivity Configuration



Item	Options	Description
CNVi Mode	Disable Integrated Auto Detection[Default]	This option configures Connectivity. [Auto Detection] means that if Discrete solution is discovered it will be enabled by default. Otherwise Integrated solution (CNVi) will be enabled; [Disable Integrated] disables Integrated Solution. NOTE: When CNVi is present, the GPIO pins that are used for radio

3.6.2.2 CPU Configuration

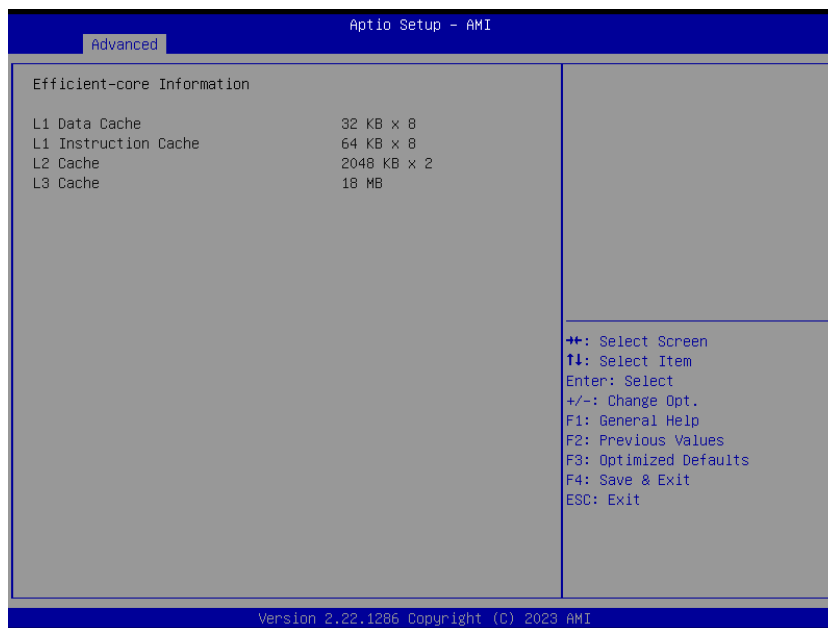


Item	Options	Description
Intel (VMX) Virtualization Technology	Disabled Enabled[Default],	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.

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Active Processor Cores	All[Default], 7/6/5/4/3/2/1	Number of cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable all cores.
Active Efficient-cores	All[Default], 7/6/5/4/3/2/1/0	Number of E-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable all cores.

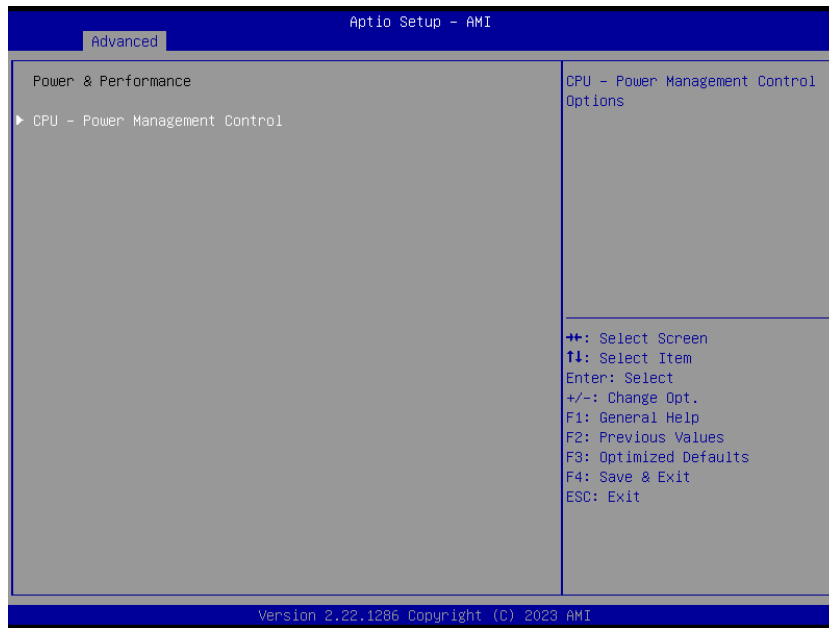
3.6.2.2.1 Efficient-core Information



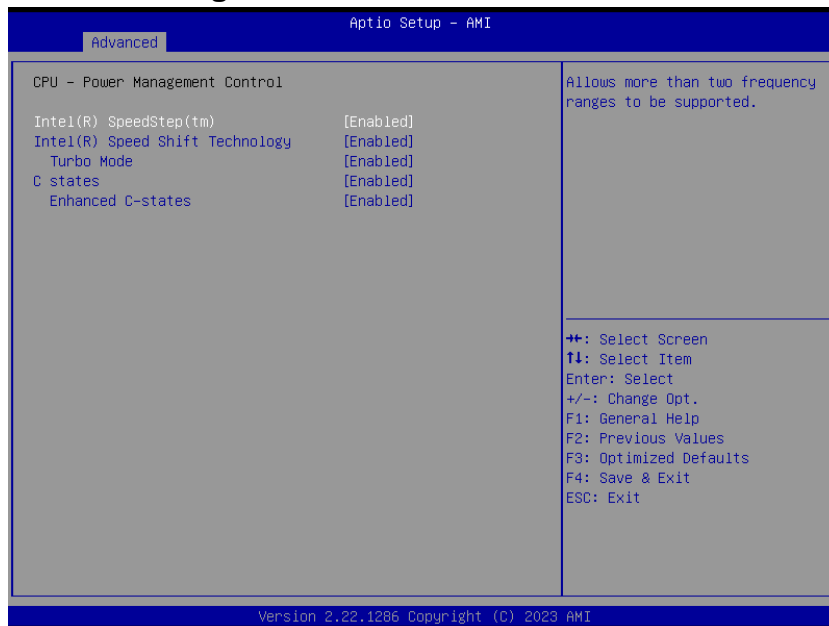
3.6.2.2.2 Performance-core Information



3.6.2.3 Power & Performance



3.6.2.3.1 CPU - Power Management Control

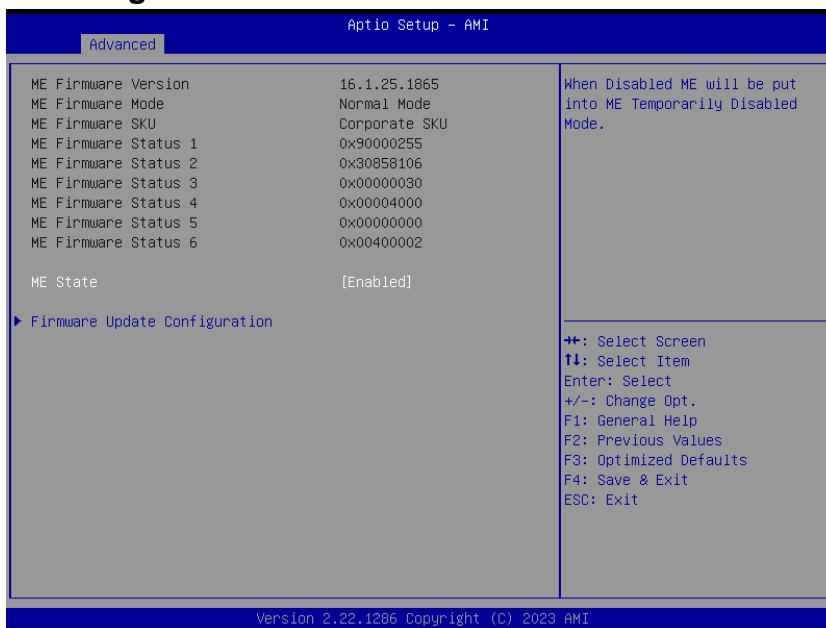


Item	Options	Description
Intel(R) SpeedStep(tm)	Disabled Enabled[Default],	Allows more than two frequency ranges to be supported.
Intel(R) Speed Shift Technology	Disabled Enabled[Default],	Enable/Disable Intel(R) Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states.
Turbo Mode	Disabled Enabled[Default],	Enable/Disable processor Turbo Mode (requires EMTTM enabled too). AUTO means enabled.
C states	Disabled Enabled[Default],	Enable/Disable CPU Power Management. Allows CPU to go to C states when it's not 100% utilized.

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Enhanced C-states	Disabled Enabled[Default],	Enable/Disable C1E. When enabled, CPU will switch to minimum speed when all cores enter C-State.
--------------------------	--	--

3.6.2.4 PCH-FW Configuration



Item	Options	Description
ME State	Disabled Enabled[Default],	When Disabled ME will be put into ME Temporarily Disabled Mode.

3.6.2.4.1 Firmware Update Configuration



Item	Options	Description
Me FW Image Re-Flash	Disabled[Default], Enabled	Enable/Disable Me FW Image Re-Flash function.

3.6.2.5 Trusted Computing



Item	Options	Description
Security Device Support	Disabled Enabled[Default],	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

3.6.2.6 ACPI Settings



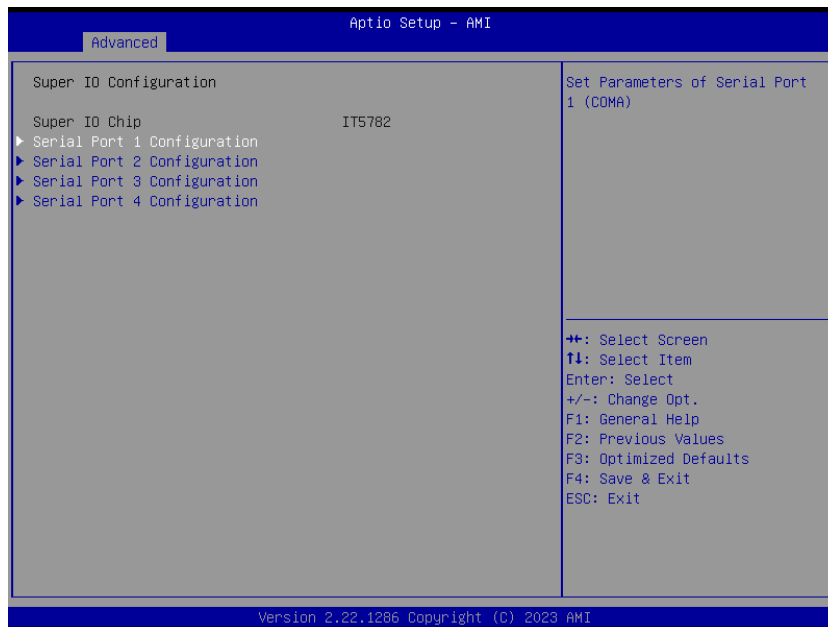
Item	Options	Description
Enable Hibernation	Disabled Enabled[Default],	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may not be effective with some Operating Systems.

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ACPI Sleep State	Suspend Disabled, S3 (Suspend to RAM) [Default]	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.
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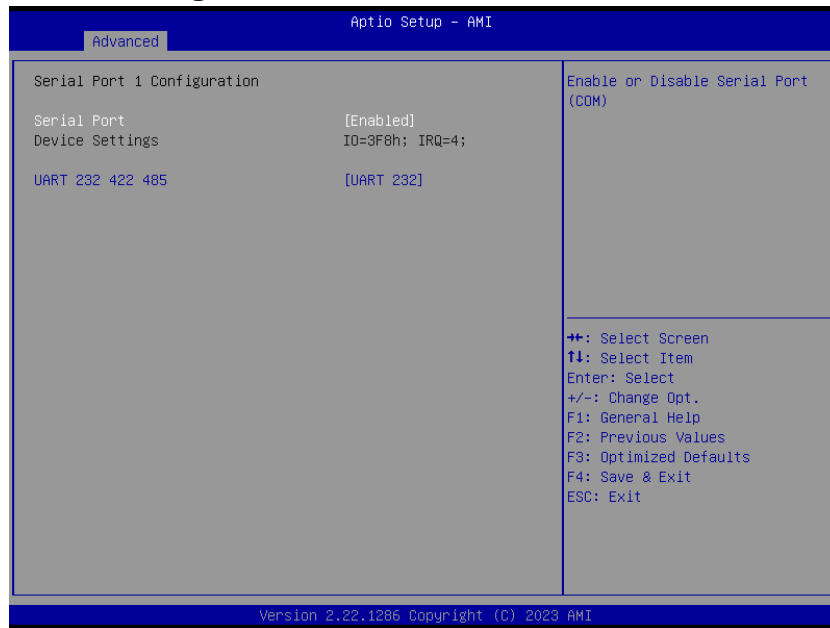
3.6.2.7 Super IO Configuration

You can use this item to set up or change the IT5782 Super IO configuration for serial ports. Please refer to 3.6.2.7.1~ 3.6.2.7.4 for more information.



Item	Description
Serial Port 1 Configuration	Set Parameters of Serial Port 1 (COMA).
Serial Port 2 Configuration	Set Parameters of Serial Port 2 (COMB).
Serial Port 3 Configuration	Set Parameters of Serial Port 3 (COMC).
Serial Port 4 Configuration	Set Parameters of Serial Port 4 (COMD).

3.6.2.7.1 Serial Port 1 Configuration



Item	Option	Description
Serial Port	Disabled Enabled[Default],	Enable or Disable Serial Port (COM).
UART 232 422 485	UART 232[Default], UART 422, UART 485	Change the Serial Port as RS232/422/485.

3.6.2.7.2 Serial Port 2 Configuration



Item	Option	Description
Serial Port	Disabled Enabled[Default],	Enable or Disable Serial Port (COM).

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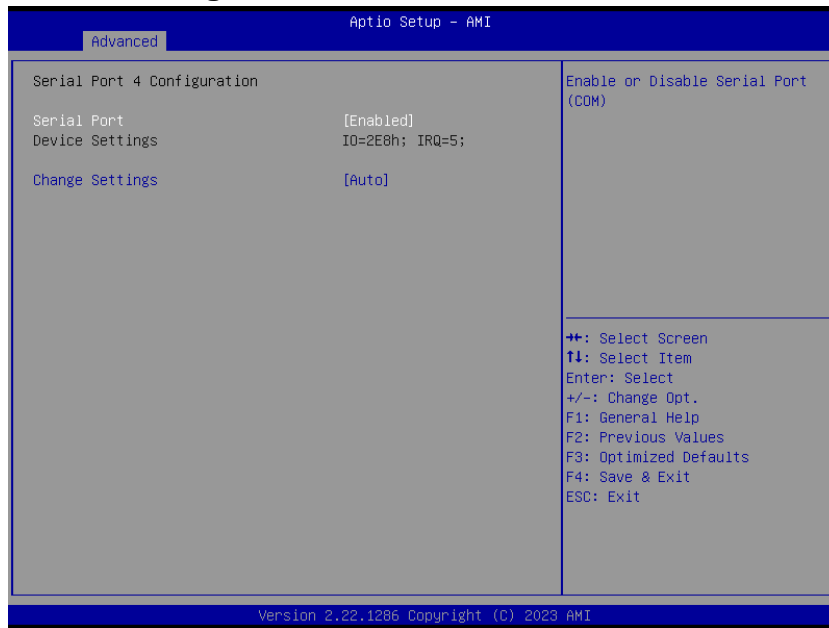
UART 232 422 485	UART 232[Default], UART 422, UART 485	Change the Serial Port as RS232/422/485.
------------------	---	--

3.6.2.7.3 Serial Port 3 Configuration



Item	Option	Description
Serial Port	Disabled Enabled[Default],	Enable or Disable Serial Port (COM).
Change Settings	Auto[Default], IO=3E8h;IRQ=7; IO=3E8h;IRQ=3,4,5,6,7,9,10,11 IO=2E8h;IRQ=3,4,5,6,7,9,10,11 IO=2F0h;IRQ=3,4,5,6,7,9,10,11 IO=2E0h;IRQ=3,4,5,6,7,9,10,11	Select an optimal settings for Super IO Device

3.6.2.7.4 Serial Port 4 Configuration



Item	Option	Description
Serial Port	Disabled Enabled[Default],	Enable or Disable Serial Port (COM).
Change Settings	Auto[Default], IO=3E8h;IRQ=7; IO=3E8h;IRQ=3,4,5,6,7,9,10,11 IO=2E8h;IRQ=3,4,5,6,7,9,10,11 IO=2F0h;IRQ=3,4,5,6,7,9,10,11 IO=2E0h;IRQ=3,4,5,6,7,9,10,11	Select an optimal settings for Super IO Device

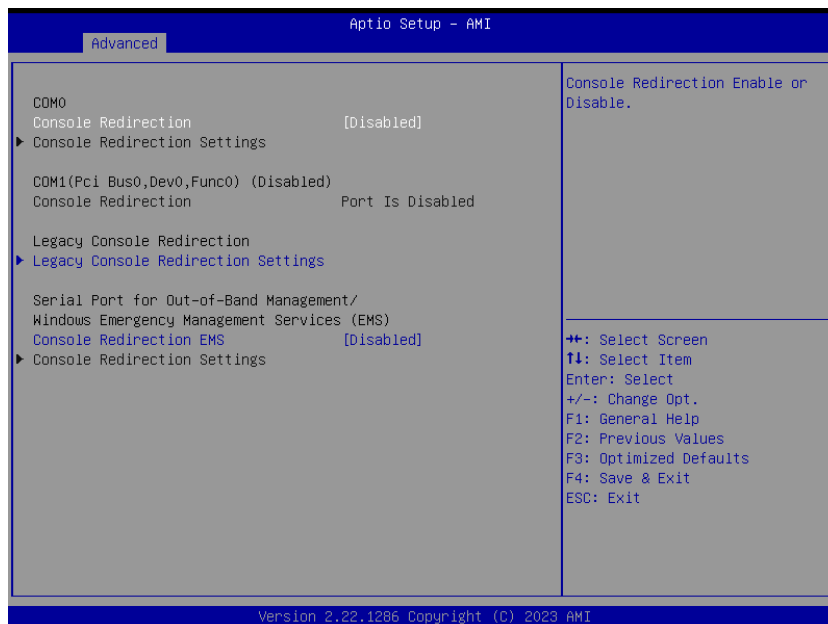
3.6.2.8 S5 RTC Wake Settings



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Item	Options	Description
Wake system from S5	Disabled[Default], Fixed Time Dynamic Time	Enable or disable System wake on alarm event. Select FixedTime, system will wake on the hr::min::sec specified. Select DynamicTime, System will wake on the current time + Increase minutes(s).

3.6.2.9 Serial Port Console Redirection



Item	Options	Description
Console Redirection	Disabled[Default], Enabled	Console Redirection Enable or Disable.
Console Redirection EMS	Disabled[Default], Enabled	Console Redirection Enable or Disable.

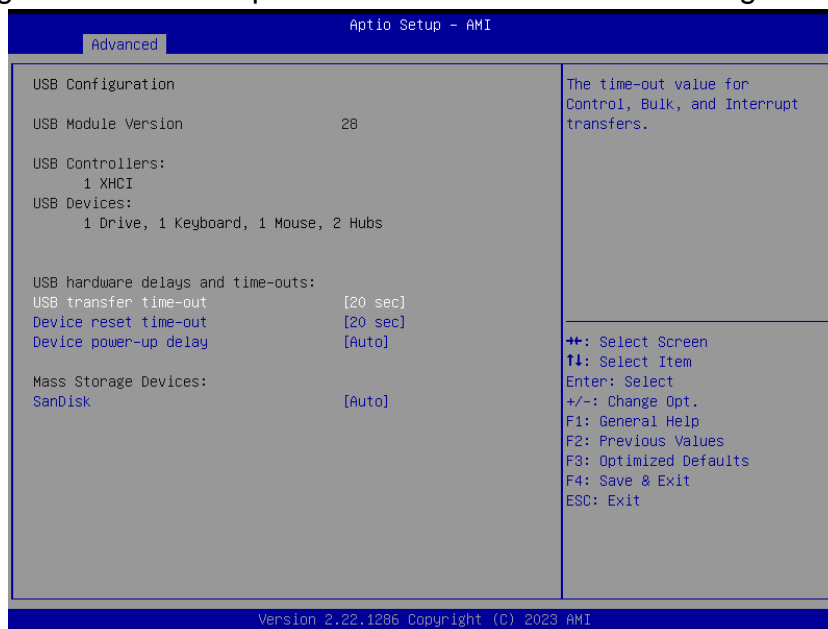
3.6.2.9.1 Legacy Console Redirection Settings



Item	Options	Description
Redirection COM Port	COM0	Select a COM port to display redirection of Legacy OS and Legacy OPROM Messages

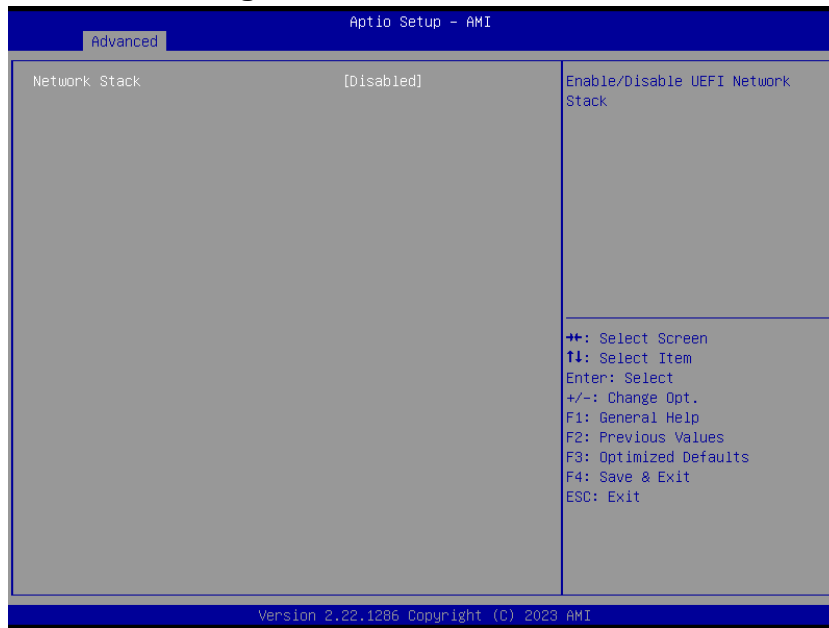
3.6.2.10 USB Configuration

The USB Configuration menu helps read USB information and configures USB settings.



Item	Options	Description
USB transfer time-out	1 sec 5 sec 10 sec 20 sec[Default]	The time-out value for Control, Bulk, and Interrupt transfers.
Device reset time-out	10 sec 20 sec[Default] 30 sec 40 sec	USB mass storage device Start Unit command time-out.
Device power-up delay	Auto[Default] Manual	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100ms, for a Hub port the delay is taken form Hub descriptor.
SanDisk	Auto[Default] Floppy Forced FDD Hard Disk CD-ROM	Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type.

3.6.2.11 Network Stack Configuration

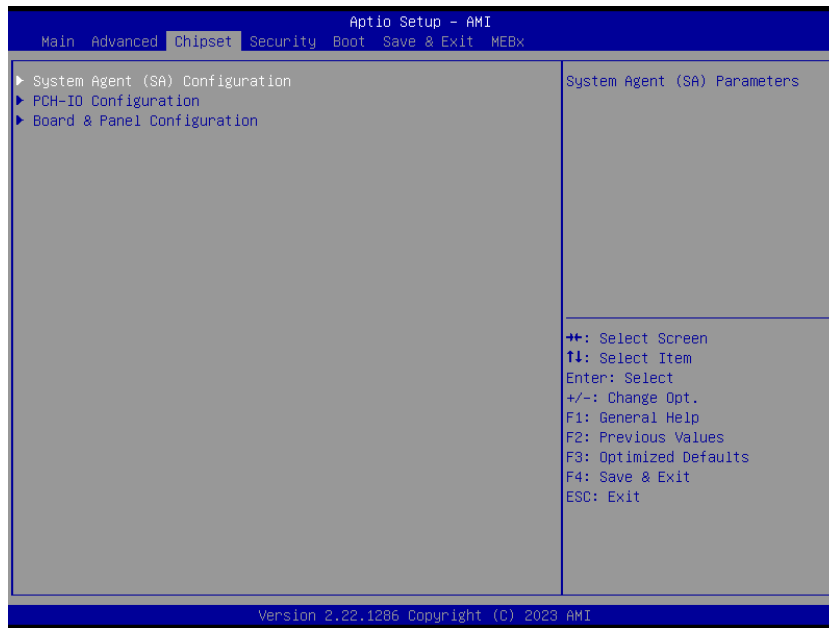


Item	Options	Description
Network Stack	Disabled[Default] Enabled	Enable/Disable UEFI Network Stack.

3.6.2.12 NVMe Configuration



3.6.3 Chipset



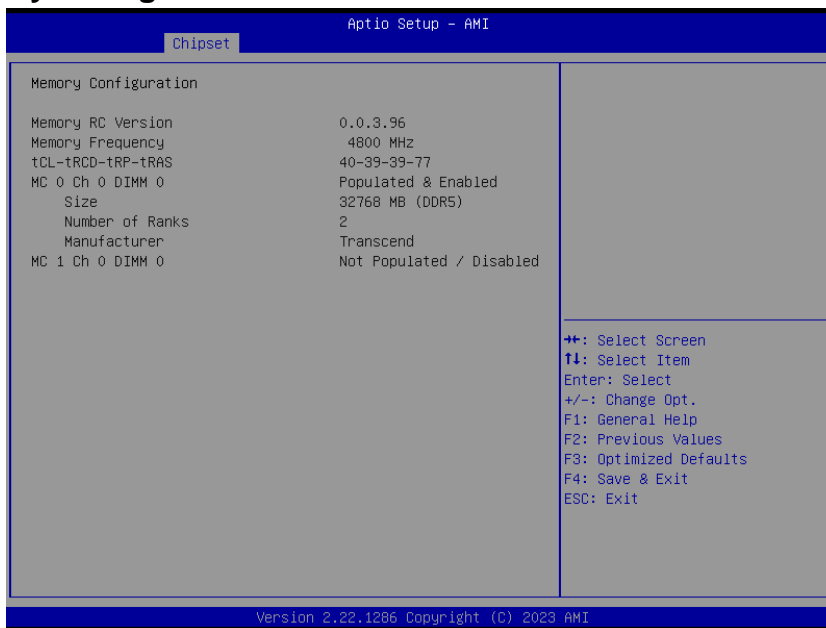
3.6.3.1 System Agent (SA) Configuration



Item	Option	Description
VT-d	Disabled[Default] Enabled	VT-d capability.

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3.6.3.1.1 Memory Configuration

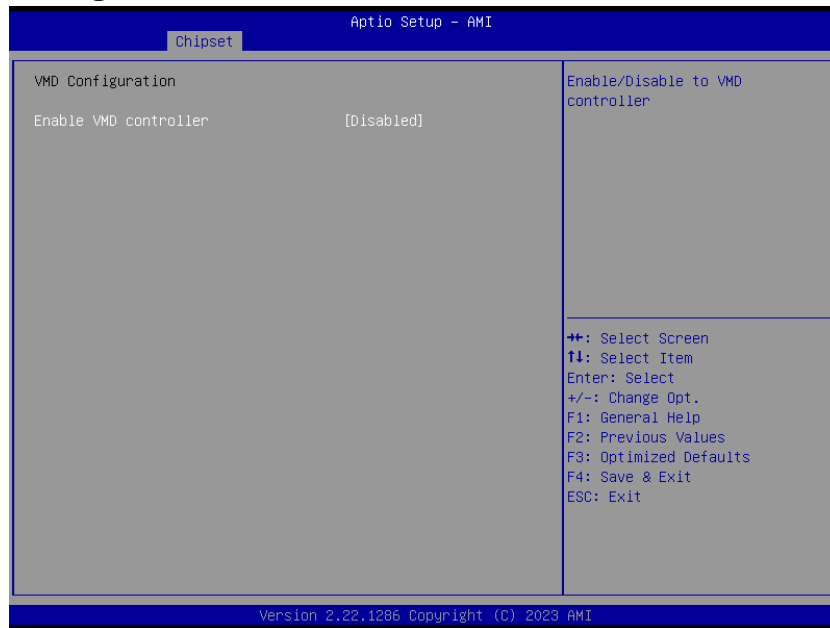


3.6.3.1.2 Graphics Configuration



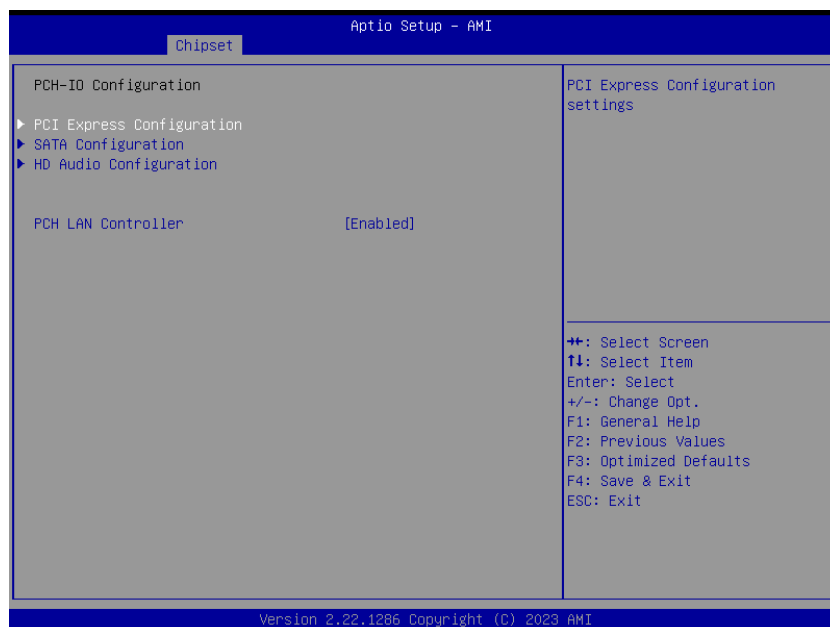
Item	Option	Description
Primary Display	Auto[Default] IGFX PEG Slot PCH PCI HG	Select which of IGFX/PEG/PCI Graphics device should be Primary Display Or select HG for Hybrid Gfx.

3.6.3.1.3 VMD Configuration



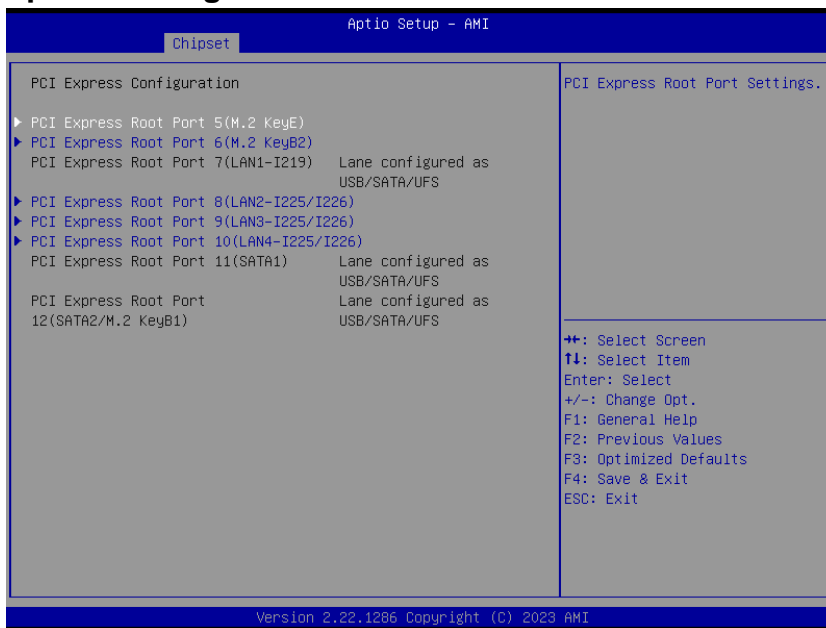
Item	Option	Description
Enable VMD controller	Disabled[Default] Enabled	Enable/Disable to VMD controller

3.6.3.2 PCH-IO Configuration

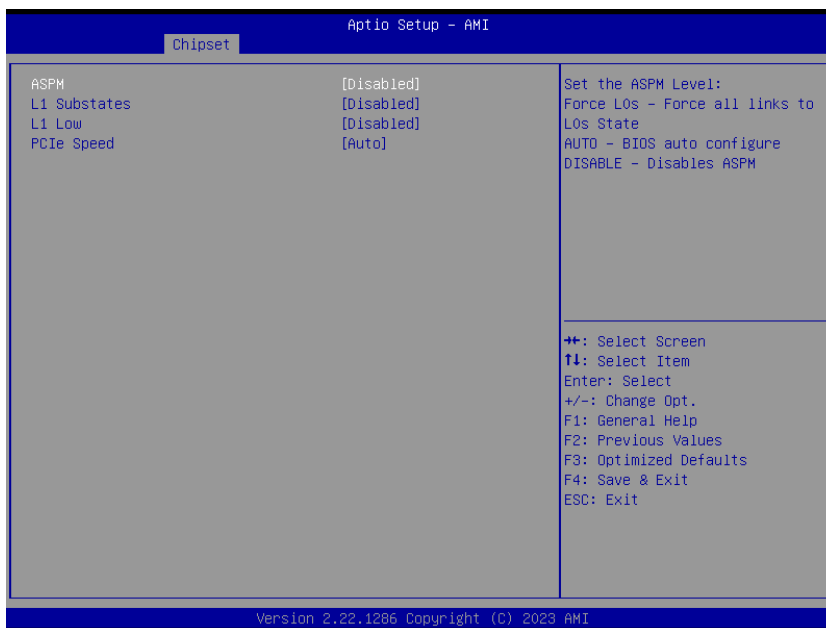


Item	Option	Description
PCH LAN Controller	Enabled[Default] Disabled	Enable/Disable onboard NIC.

3.6.3.2.1 PCI Express Configuration



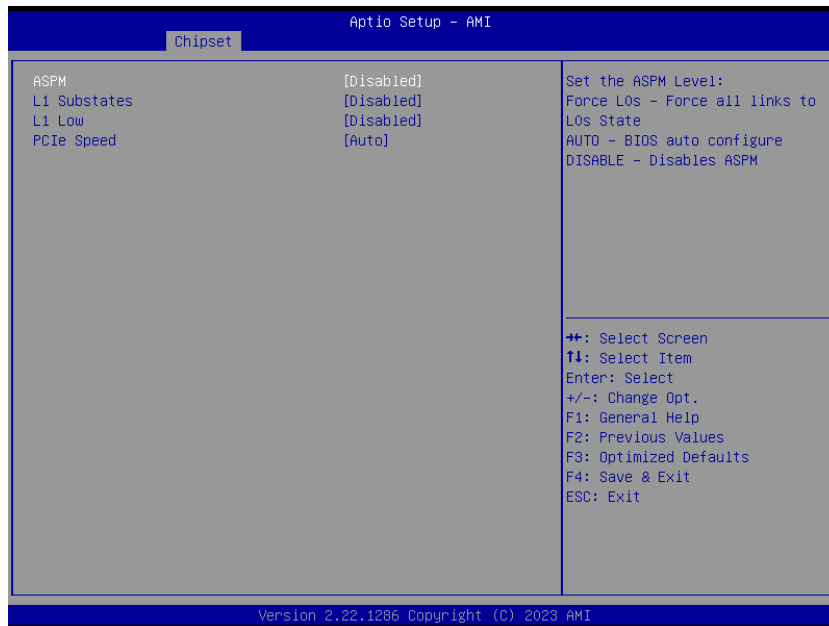
3.6.3.2.1.1 PCI Express Root Port 5(M.2 KeyE)



Item	Option	Description
ASPM	Disabled[Default] L1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM
L1 Substates	Disabled[Default], L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.

L1 Low	Disabled[Default] Enabled	PCI Express L1 Low Substate Enable/Disable.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.2 PCI Express Root Port 6(M.2 KeyB2)



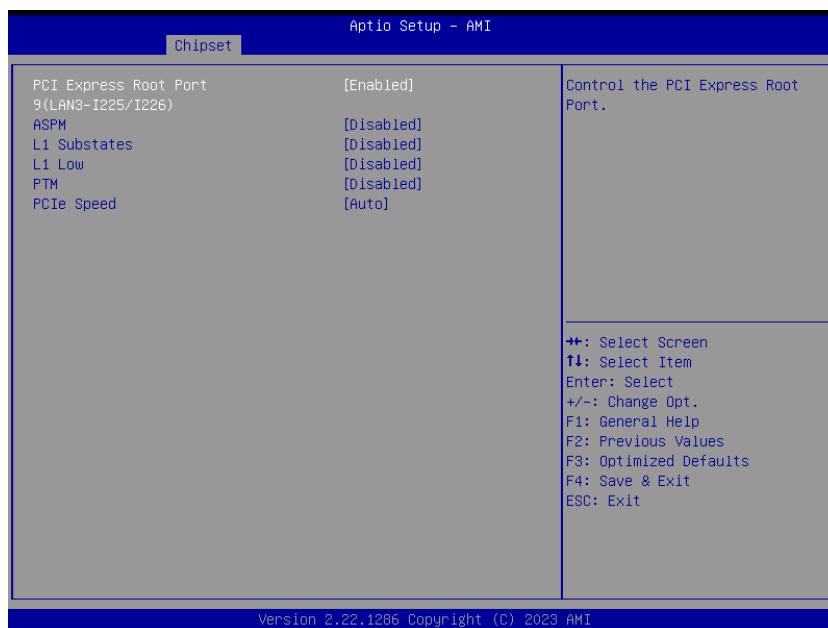
Item	Option	Description
ASPM	Disabled[Default] L1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM
L1 Substates	Disabled[Default], L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
L1 Low	Disabled[Default] Enabled	PCI Express L1 Low Substate Enable/Disable.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.3 PCI Express Root Port 8(LAN2-I225/I226)



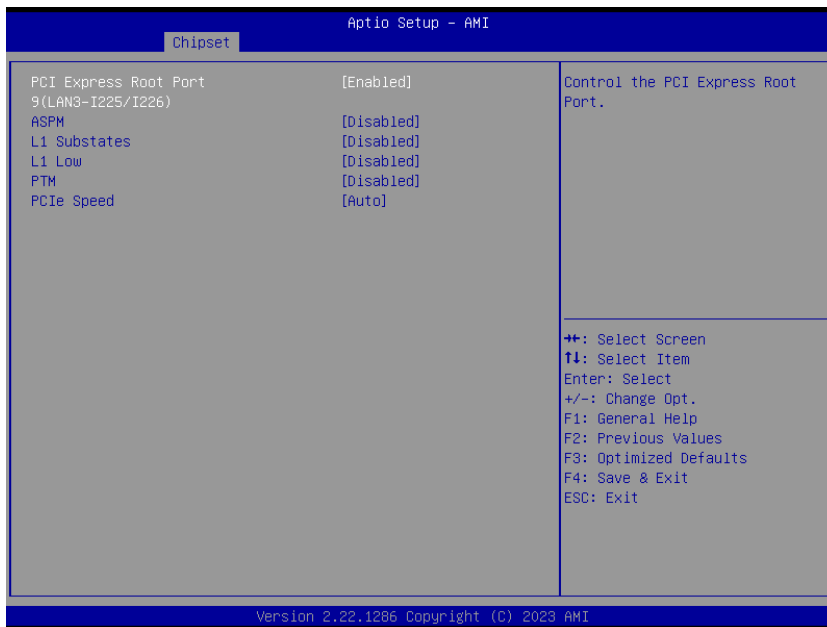
Item	Option	Description
PCI Express Root Port 8 (LAN2-I225/I226)	Disabled Enabled[Default],	Control the PCI Express Root Port.
ASPM	Disabled[Default] L1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled[Default], L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
L1 Low	Disabled[Default] Enabled	PCI Express L1 Low Substate Enable/Disable.
PTM	Disabled[Default] Enabled	Enable/Disable Precision Time Measurement
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.4 PCI Express Root Port 9(LAN3-I225/I226)



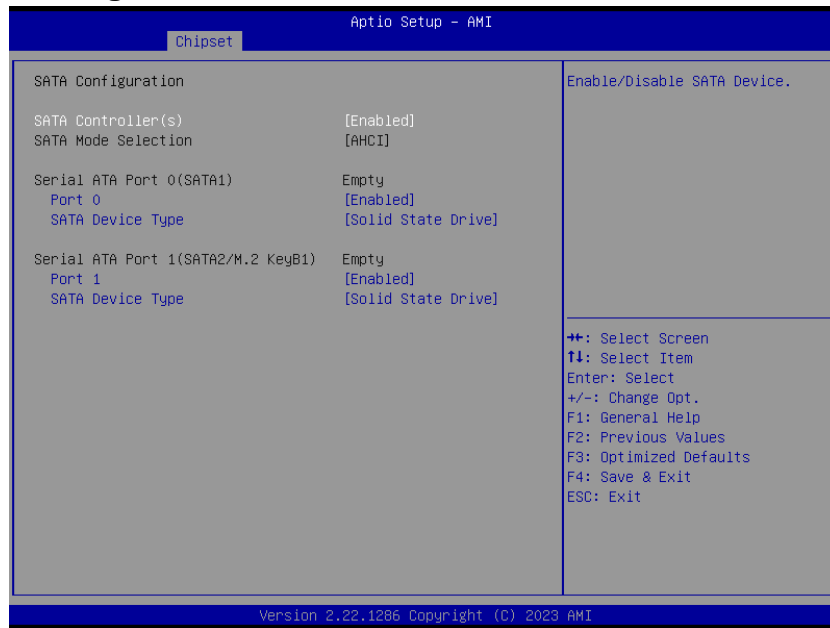
Item	Option	Description
PCI Express Root Port 9 (LAN3-I225/I226)	Disabled Enabled[Default],	Control the PCI Express Root Port.
ASPM	Disabled[Default] L1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled[Default], L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
L1 Low	Disabled[Default] Enabled	PCI Express L1 Low Substate Enable/Disable.
PTM	Disabled[Default] Enabled	Enable/Disable Precision Time Measurement
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.5 PCI Express Root Port 10(LAN4-I225/I226)



Item	Option	Description
PCI Express Root Port 10 (LAN4-I225/I226)	Disabled Enabled[Default],	Control the PCI Express Root Port.
ASPM	Disabled[Default] L1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled[Default], L1.1 L1.1 & L1.2	PCI Express L1 Substates settings.
L1 Low	Disabled[Default] Enabled	PCI Express L1 Low Substate Enable/Disable.
PTM	Disabled[Default] Enabled	Enable/Disable Precision Time Measurement
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.2 SATA Configuration



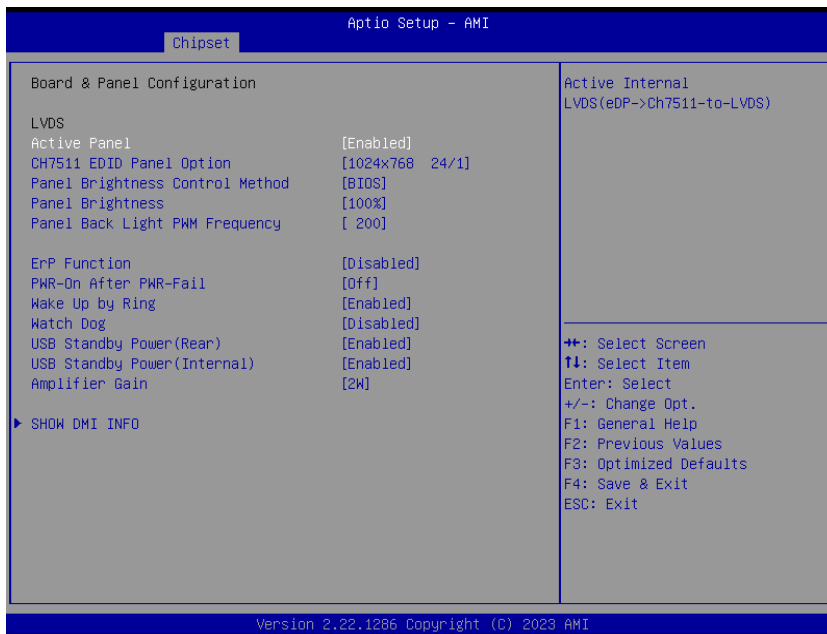
Item	Option	Description
SATA Controller(s)	Disabled Enabled[Default],	Enable/Disable SATA Device.
Port 0	Disabled Enabled[Default],	Enable or Disable SATA Port
SATA Device Type	Hard Disk Drive Solid State Drive[Default],	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive
Port 1	Disabled Enabled[Default],	Enable or Disable SATA Port
SATA Device Type	Hard Disk Drive Solid State Drive[Default],	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive

3.6.3.2.3 HD Audio Configuration



Item	Option	Description
HD Audio	Disabled Enabled[Default],	Control Detection of the HD-Audio device. Disable = HDA will be unconditionally disabled Enabled = HDA will be unconditionally enabled.

3.6.3.3 Board & Panel Configuration



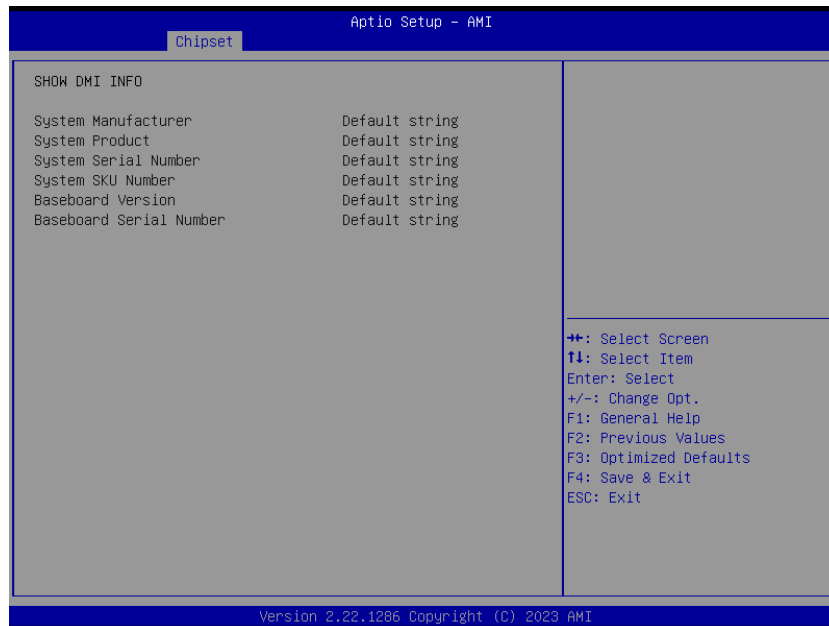
Item	Option	Description
Active Panel	Disabled Enabled[Default]	Active Internal LVDS(eDP->Ch7511-to-LVDS)

CH7511 EDID Panel Option	1024 x 768 24/1 [Default] 800 x 600 18/1 1024 x 768 18/1 1366 x 768 18/1 1024 x 600 18/1 1280 x 800 18/1 1920 x 1200 24/2 1920 x 1080 18/2 1280 x 1024 24/2 1440 x 900 18/2 1600 x 1200 24/2 1366 x 768 24/1 1920 x 1080 24/2 1680 x 1050 24/2	Port1-EDP to LVDS(Chrotel 7511)Panel EDID Option
Panel Brightness Control Method	BIOS[Default] BR Button VR OS Driver	Panel Brightness Control Method. 1.BIOS 2. Brightness Button 3. VR 4.OS Driver
Panel Brightness	00% 25% 50% 75% 100% [Default]	Select Panel back light PWM duty.
Panel Back Light PWM Frequency	200 [Default] 300 400 500 700 1k 2k 3k 5k 10k 20k	Select Panel back light PWM Frequency
ErP Function	Disabled [Default] , Enabled	ErP Function (Deep S5).
PWR-On After PWR-Fail	Off [Default] , On Last State	AC loss resume.
Wake Up by Ring	Disabled Enabled [Default] ,	Wake Up by Ring from S3/S4/S5
Watch Dog	Disabled [Default] , 30 sec 40 sec 50 sec 1 min 2 min	Select WatchDog.

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	10 min 30 min	
USB Standby Power(Rear)	Disabled Enabled[Default],	Enabled/Disabled USB1/2 Standby Power during S3/S4/S5
USB Standby Power(Internal)	Disabled Enabled[Default],	Enabled/Disabled JUSB3/4 Standby Power during S3/S4/S5
Amplifier Gain	2W[Default], 6W	Amplifier Gain

3.6.3.3.1 SHOW DMI INFO



3.6.4 Security



- **Administrator Password**

Set setup Administrator Password

- **User Password**

Set User Password

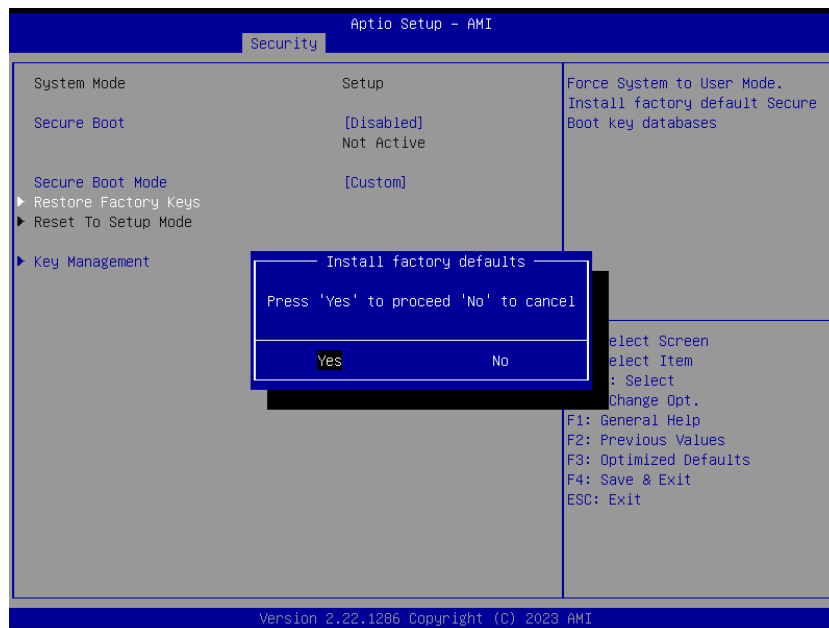
3.6.4.1 Secure Boot menu



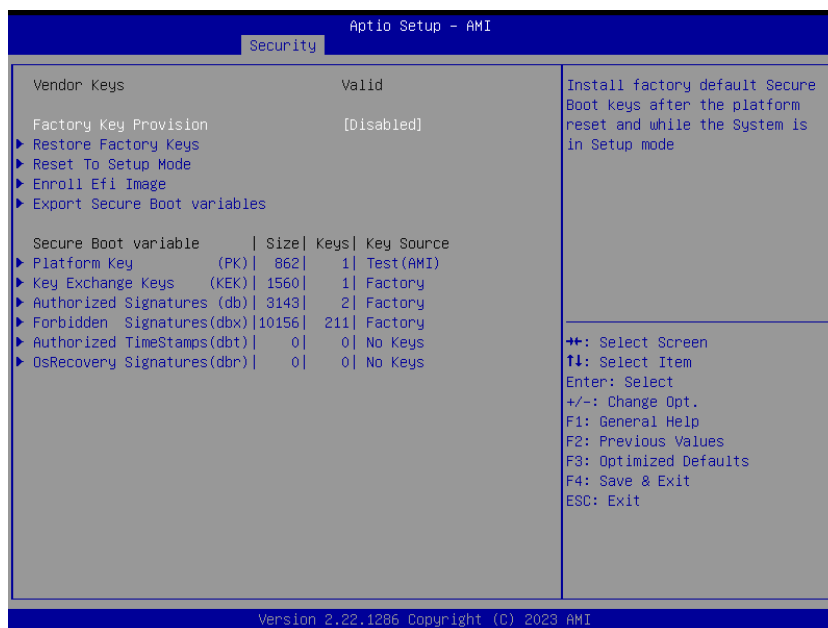
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Item	Option	Description
Secure Boot	Disabled Enabled[Default]	Secure Boot feature is Active if Secure Boot is Enabled, Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset
Secure Boot Mode	Standard[Default] Custom	Secure Boot mode options: Standard or Custom. In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication

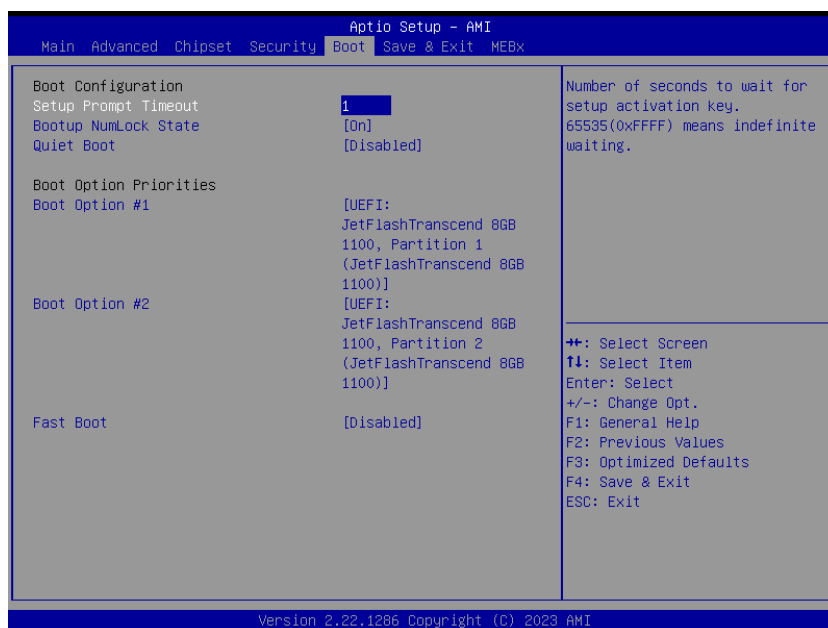
3.6.4.1.1 Restore Factory Keys



3.6.4.1.2 Key Management

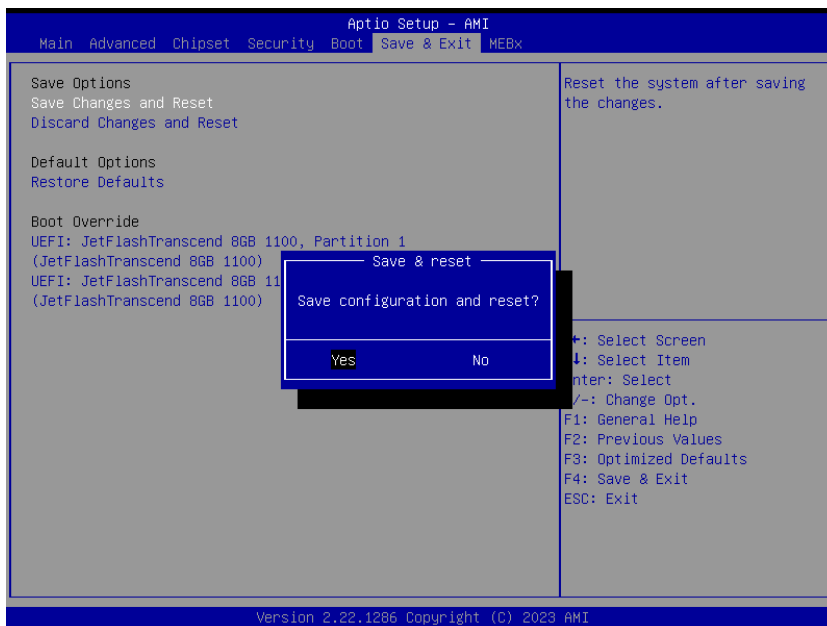
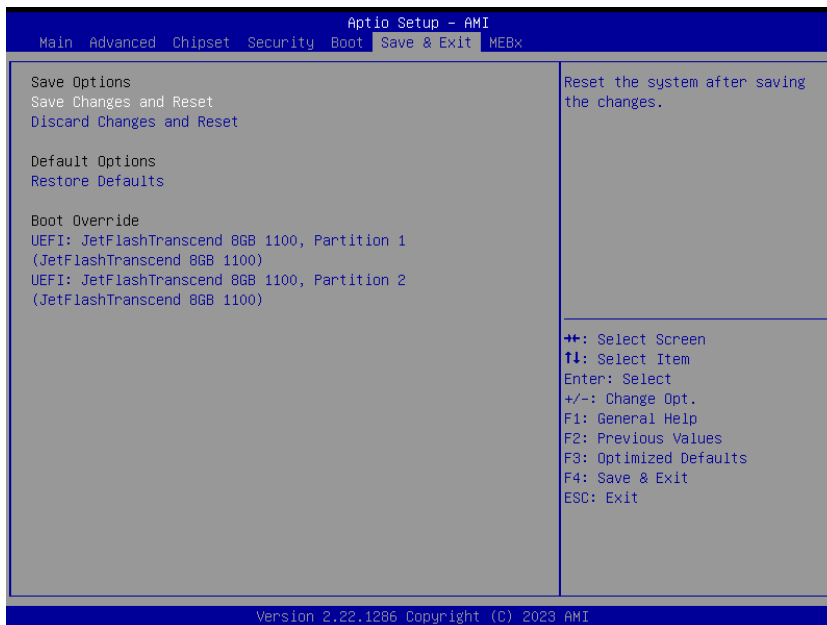


3.6.5 Boot



Item	Option	Description
Setup Prompt Timeout	1	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Bootup NumLock State	On[Default] Off	Select the Keyboard NumLock state
Quiet Boot	Disabled[Default] Enabled	Enables or disables Quiet Boot option
Boot Option #1	Set the system boot order.	
Boot Option #2	Set the system boot order.	

3.6.6 Save and exit



3.6.6.1 Save Changes and Reset

Reset the system after saving the changes.

3.6.6.2 Discard Changes and Reset

Reset system setup without saving any changes.

3.6.6.3 Restore Defaults

Restore/Load Default values for all the setup options.

3.6.6.4 Launch EFI Shell from filesystem device

Attempts to Launch EFI Shell application (Shell.efi) from one of the available filesystem devices.

3.6.7 MEBx



4. Drivers Installation



Note: Installation procedures and screen shots in this section are for your reference and may not be exactly the same as shown on your screen.

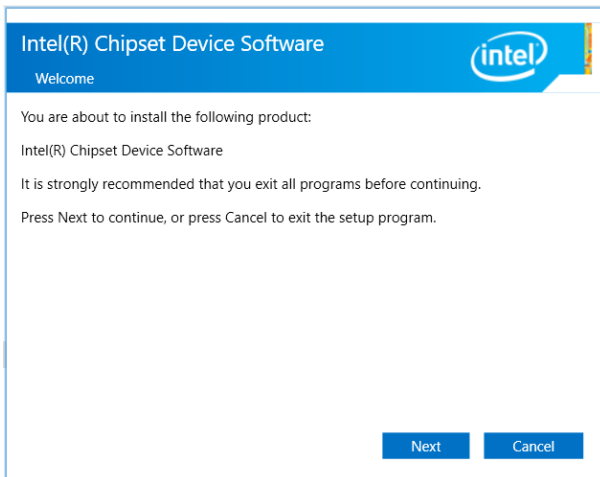
4.1 Install Chipset Driver

All drivers can be found on the Avalue Official Website:

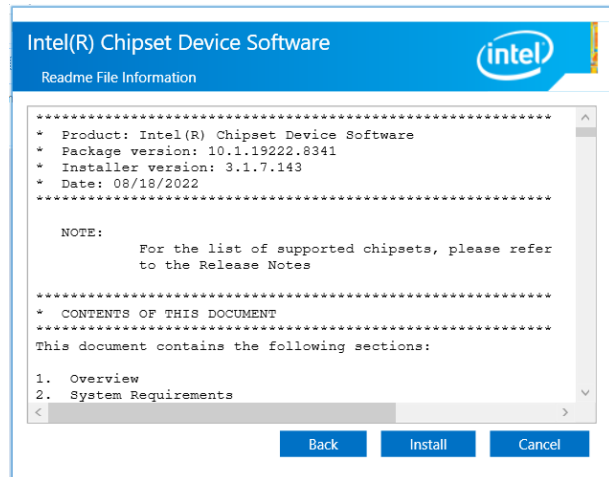
<http://www.avalue.com.tw>.



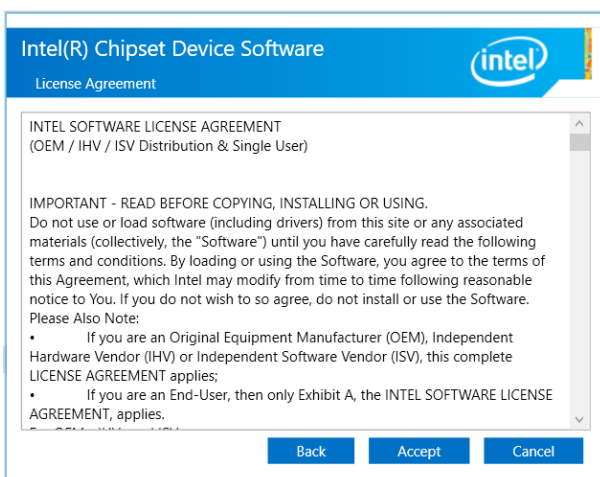
Note: The installation procedures and screen shots in this section are based on Windows 11 operation system. If the warning message appears while the installation process, click Continue to go on.



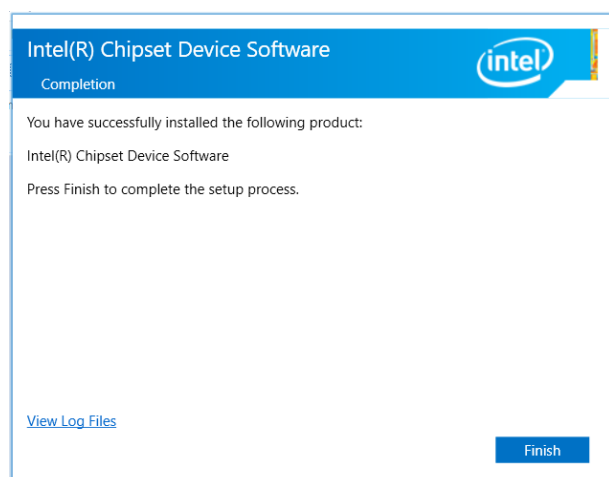
Step 1. Click Next.



Step 3. Click Install.



Step 2. Click Accept.



Step 4. Complete setup.

4.2 Install VGA Driver

All drivers can be found on the Avalue Official Website:

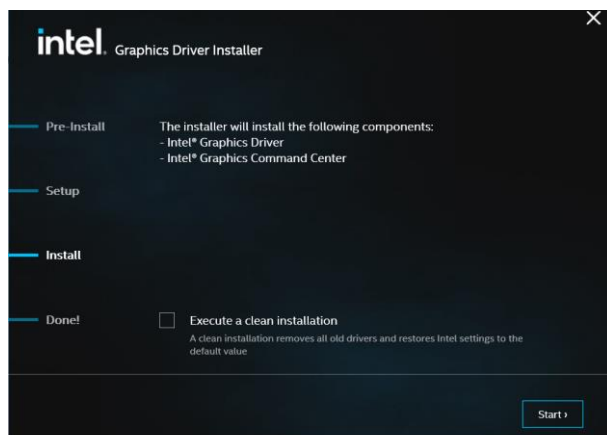
<http://www.avalue.com.tw>.



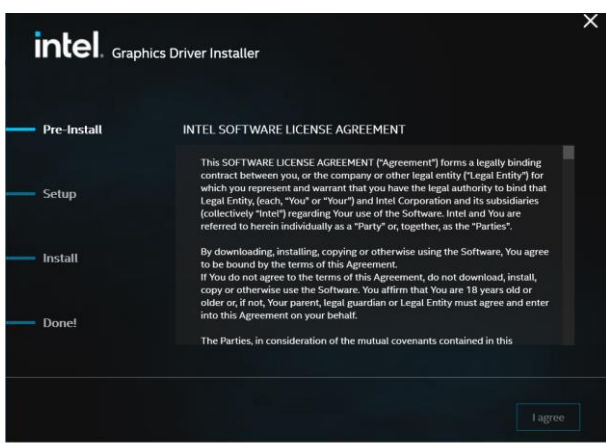
Note: The installation procedures and screen shots in this section are based on Windows 11 operation system. If the warning message appears while the installation process, click Continue to go on.



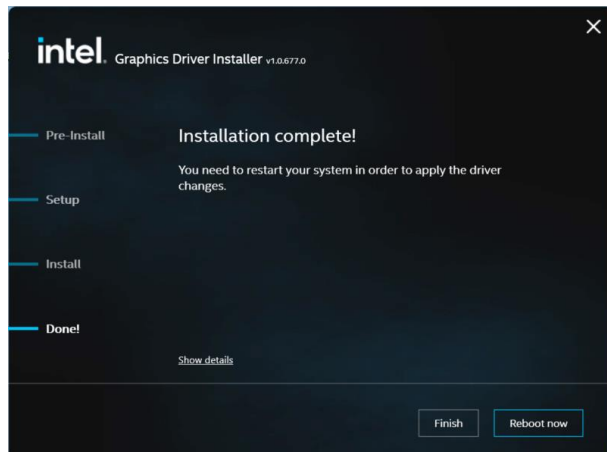
Step 1. Click Begin installation.



Step 3. Click Start.



Step 2.
Click **Next** to accept license agreement.



Step 4. Click Reboot now.

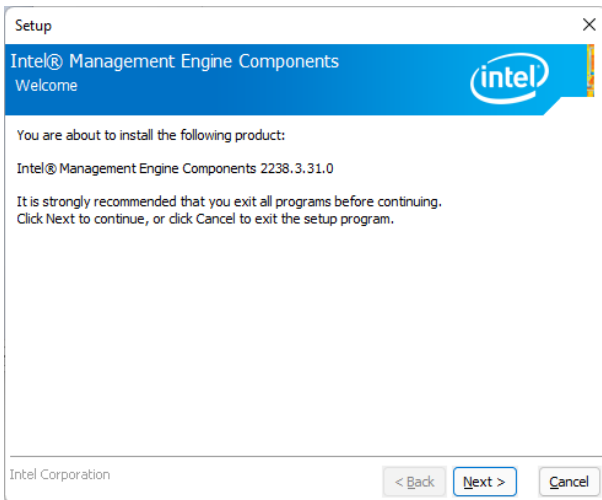
4.3 Install ME Driver

All drivers can be found on the Avalue Official Website:

<http://www.avalue.com.tw>.



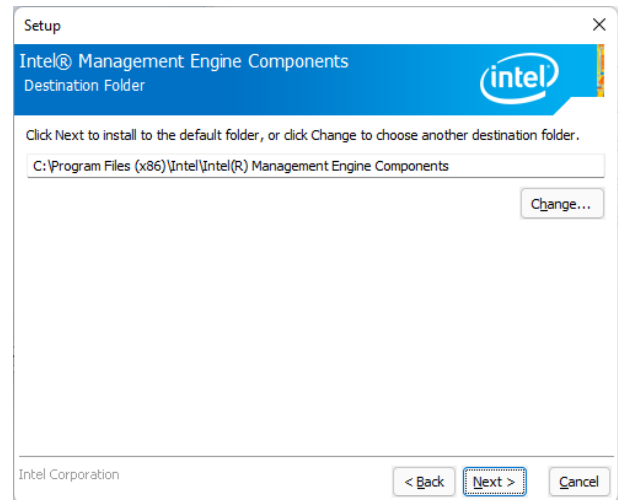
Note: The installation procedures and screen shots in this section are based on Windows 11 operation system. If the warning message appears while the installation process, click Continue to go on.



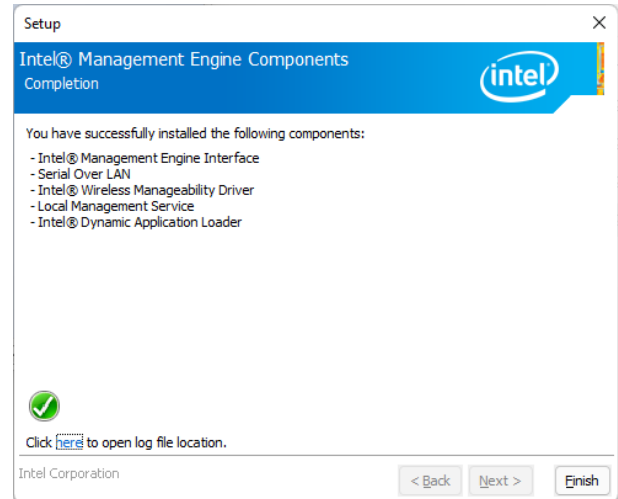
Step 1. Click **Next** to continue setup.



Step 2. Click **Next**.



Step 3. Click **Next**.



Step 4. Click **Finish** to complete setup.

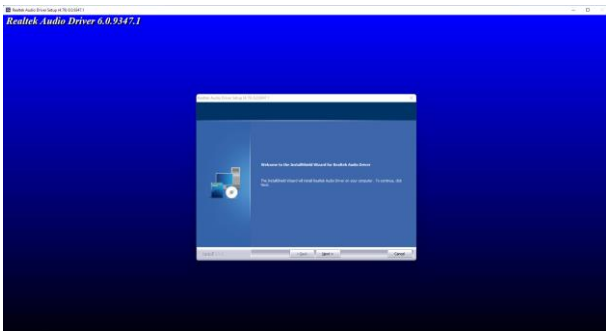
4.4 Install Audio Driver (For Realtek ALC897 and ALC888S HD Audio)

All drivers can be found on the Avalue Official Website:

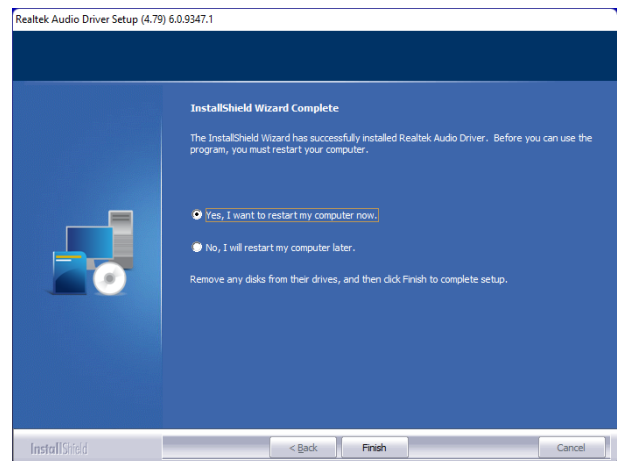
<http://www.avalue.com.tw>.



Note: The installation procedures and screen shots in this section are based on Windows 11 operation system. If the warning message appears while the installation process, click Continue to go on.



Step1. Click **Next** to Install.



Step 2. Click **Finish** to complete setup.

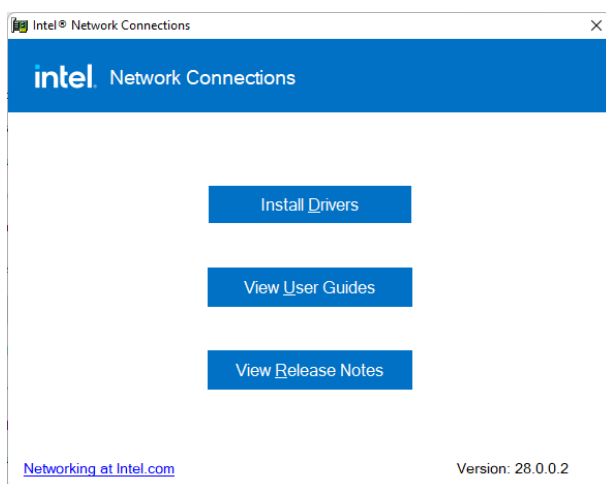
4.5 Install LAN Driver

All drivers can be found on the Avalue Official Website:

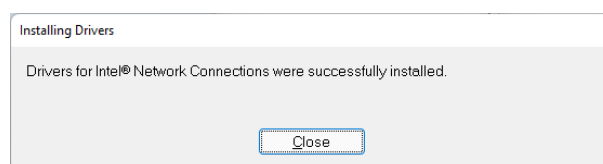
<http://www.avalu.com.tw>.



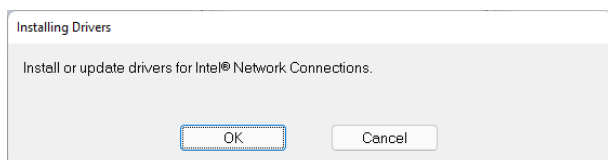
Note: The installation procedures and screen shots in this section are based on Windows 11 operation system. If the warning message appears while the installation process, click Continue to go on.



Step 1. Click **Next** to continue installation.



Step 3. Click **Close**.



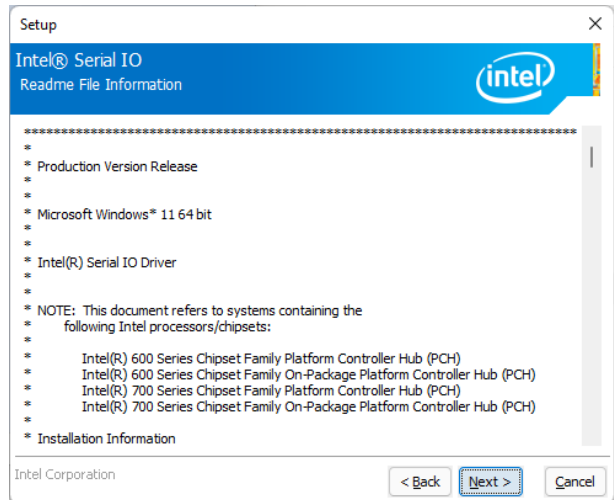
Step 2. Click **OK**.

4.6 Install Serial IO Driver

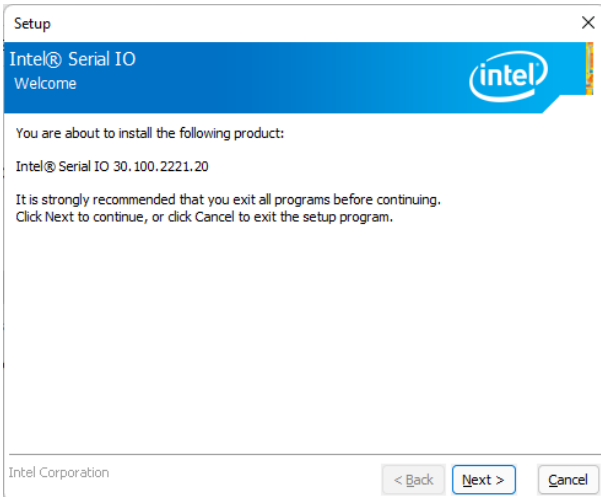
All drivers can be found on the Avalue Official Website:
<http://www.avalue.com.tw>.



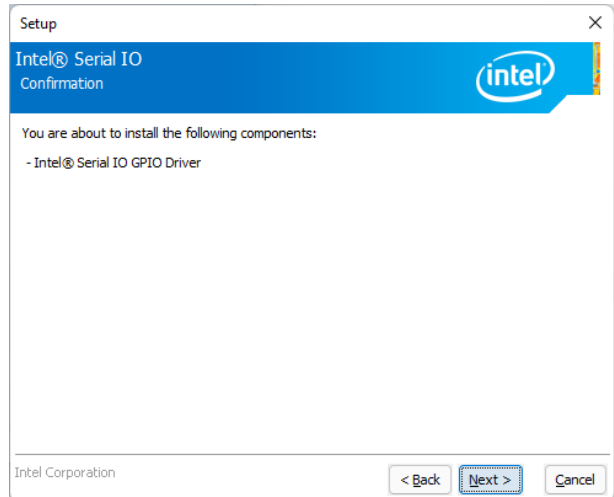
Note: The installation procedures and screen shots in this section are based on Windows 11 operation system. If the warning message appears while the installation process, click Continue to go on.



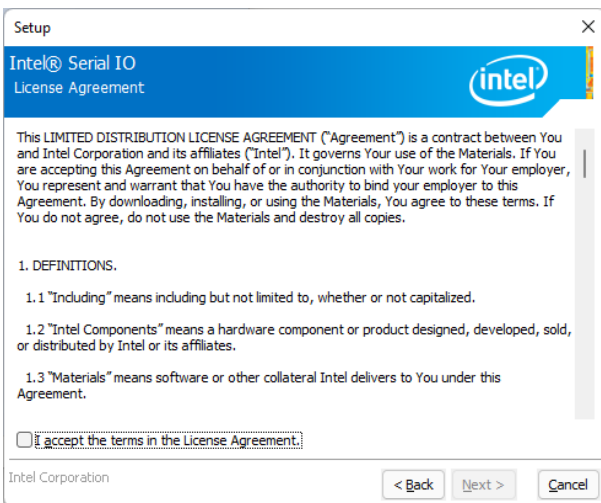
Step 3. Click Next.



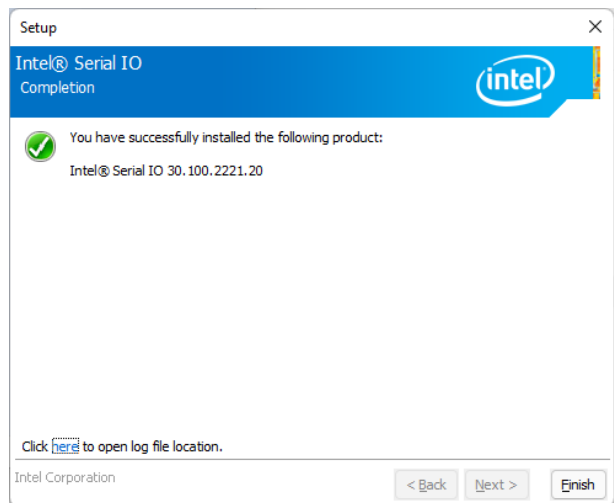
Step 1. Click Next to continue installation.



Step 4. Click Yes.



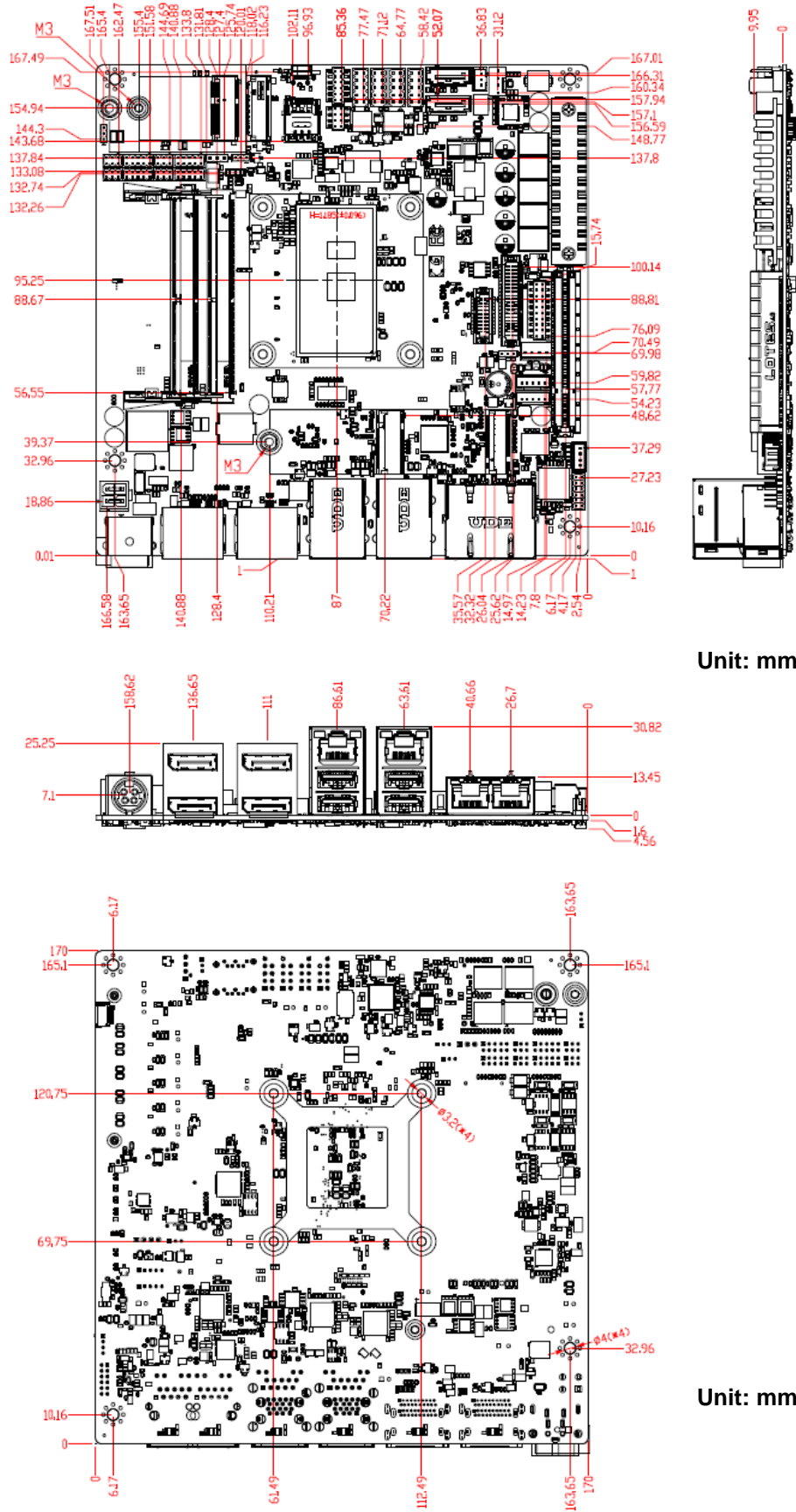
Step 2. Click Next.



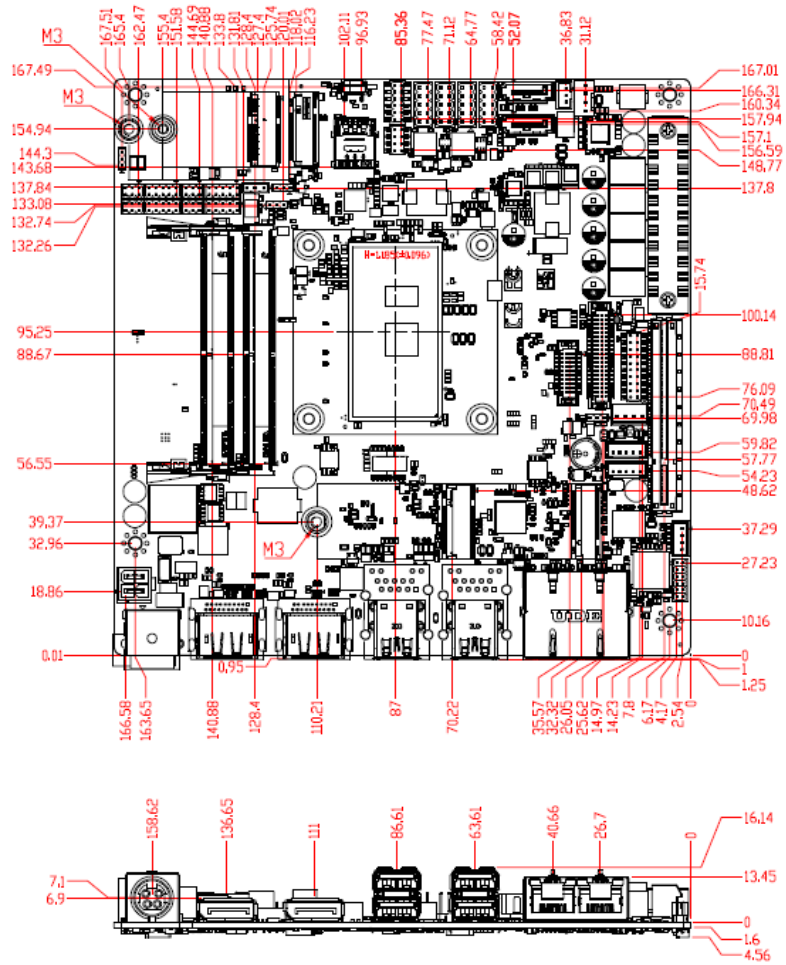
Step 5. Click Finish to complete setup.

5. Mechanical Drawing

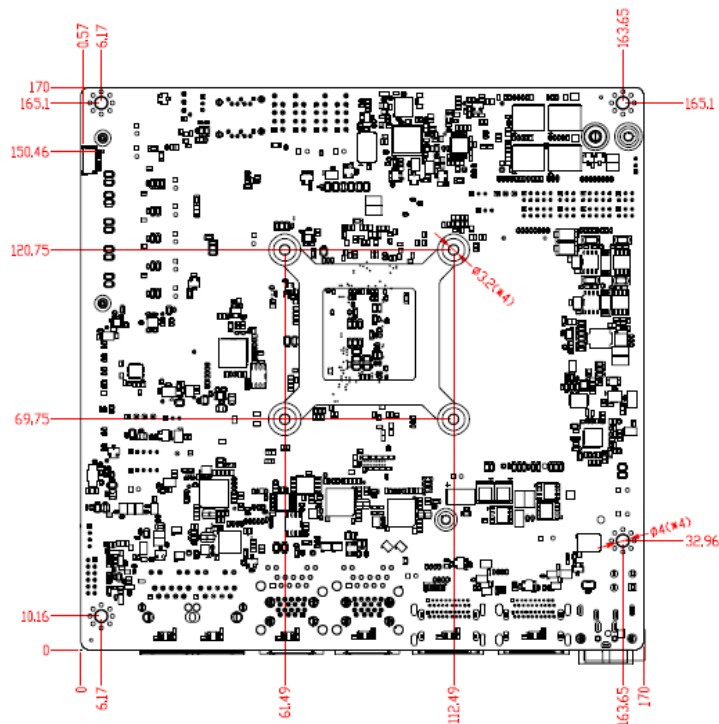
5.1 This is 4LAN4DP (Full height) version.



5.2 This is 2LAN2DP (Thin) version.



Unit: mm



Unit: mm

