

# BC170Q

Intel® Core™ Processors with Intel® Q170 ATX Motherboard

## User's Manual

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1<sup>st</sup> Ed – 12 June 2017

## FCC Statement



THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

- (1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.
- (2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRE OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

OPERATION OF THIS EQUIPMENT IN A RESIDENTIAL AREA IS LIKELY TO CAUSE HARMFUL INTERFERENCE IN WHICH CASE THE USER WILL BE REQUIRED TO CORRECT THE INTERFERENCE AT HIS OWN EXPENSE.

## Notice

This guide is designed for experienced users to setup the system within the shortest time. For detailed information, please always refer to the electronic user's manual.

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into body, or (b) support or sustain life and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
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We warrant to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for two years from the date of purchase.

This warranty does not apply to any products which have been repaired or altered by persons other than repair personnel authorized, or which have been subject to misuse, abuse, accident or improper installation. We assume no liability under the terms of this warranty as a consequence of such events. Because of our high quality-control standards and rigorous testing, most of our customers never need to use our repair service. If any of the product is defective, it will be repaired or replaced at no charge during the warranty period. For out-of-warranty repairs, you will be billed according to the cost of replacement materials, service time, and freight. Please consult your dealer for more details. If you think you have a defective product, follow these steps:

1. Collect all the information about the problem encountered. (For example, CPU type and speed, our products model name, hardware & BIOS revision number, other hardware and software used, etc.) Note anything abnormal and list any on-screen messages you get when the problem occurs.
2. Call your dealer and describe the problem. Please have your manual, product, and any helpful information available.
3. If your product is diagnosed as defective, obtain an RMA (return material authorization) number from your dealer. This allows us to process your good return more quickly.
4. Carefully pack the defective product, a complete Repair and Replacement Order Card and a photocopy proof of purchase date (such as your sales receipt) in a shippable container. A product returned without proof of the purchase date is not eligible for warranty service.
5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

# Content

<b>1. Getting Started</b>	<b>8</b>
1.1 Safety Precautions	8
1.2 Packing List	8
1.3 Document Amendment History	9
1.4 Manual Objectives	10
1.5 System Specifications	11
1.6 Architecture Overview—Block Diagram	15
<b>2. Hardware Configuration</b>	<b>16</b>
2.1 Product Overview	17
2.2 Jumper and Connector List	18
2.3 Setting Jumpers & Connectors	20
2.3.1 Serial port 1 pin9 signal select (JRI1)	20
2.3.2 Serial port 2 pin9 signal select (JRI2)	20
2.3.3 AT/ATX Power Mode Select (JATATX1)	21
2.3.4 Clear CMOS (JCMOS1)	21
2.3.5 Serial port 2 connector (JCOM2A)	22
2.3.6 COM2 RS485/422 connector (JCOM2B)	22
2.3.7 Serial port 3/4/5/6 connector (JCOM3/4/5/6)	23
2.3.8 General purpose I/O connector (JDIO1)	23
2.3.9 SGPIO connector (JSGPIO1)	24
2.3.10 ATX Power connector (ATXPWR1)	24
2.3.11 Power connector (ATX12V1)	25
2.3.12 SMBus connector (JSMB1)	25
2.3.13 USB connector 1 (JUSB1)	26
2.3.14 USB connector 2 (JUSB2)	26
2.3.15 USB connector 4 (JUSB4)	27
2.3.16 Battery connector (JBAT1)	27
2.3.17 Audio connector (JAUDIO1)	28
2.3.17.1 Signal Description –Audio connector (JAUDIO1)	28
2.3.18 LPC connector (JLPC1)	28
2.3.19 SPI connector (JSPI1)	29
2.3.20 Speaker connector (JSPK1)	29
2.3.21 Miscellaneous setting connector (JFP1)	30
2.3.22 CPU fan connector (CPUFAN1)	30
2.3.23 System fan connector 1 (SYSFAN1)	31
2.3.24 System fan connector 2 (SYSFAN3)	31

## BC170Q User's Manual

2.3.25	PS/2 keyboard & mouse connector (JKBMS1)	32
2.3.26	S/PDIF connector (JSPDIF1)	32
2.3.27	External Speaker connector (JBZ1)	33
2.3.28	LPT connector (LPT1)	33
2.3.29	Auxiliary Panel connector (JAUXP1)	34
<b>3</b>	<b>BIOS Setup</b>	<b>35</b>
3.1	Introduction	36
3.2	Starting Setup	36
3.3	Using Setup	37
3.4	Getting Help	38
3.5	In Case of Problems	38
3.6	BIOS setup	39
3.6.1	Main Menu	39
3.6.1.1	System Language	40
3.6.1.2	System Date	40
3.6.1.3	System Time	40
3.6.2	Advanced Menu	41
3.6.2.1	CPU Configuration	41
3.6.2.1.1	CPU – Power management Control	42
3.6.2.2	PCH-FW Configuration	43
3.6.2.2.1	Firmware Update Configuration	43
3.6.2.2.2	OEM Flags Settings	44
3.6.2.3	Trusted Computing	44
3.6.2.4	APCI Settings	45
3.6.2.5	S5 RTC Wake Settings	46
3.6.2.6	Super IO Configuration	48
3.6.2.6.1	Serial Port 1 Configuration	49
3.6.2.6.2	Serial Port 2 Configuration	50
3.6.2.6.3	Serial Port 3 Configuration	50
3.6.2.6.4	Serial Port 4 Configuration	51
3.6.2.6.5	Serial Port 5 Configuration	51
3.6.2.6.6	Serial Port 6 Configuration	52
3.6.2.6.7	Parallel Port Configuration	52
3.6.2.7	NCT6106D H/W Monitor	53
3.6.2.7.1	Smart Fan Configuration	53
3.6.2.8	Serial Port Console Redirection	54
3.6.2.8.1	COM1	55
3.6.2.9	Intel TXT Configuration	56
3.6.2.10	Network Stack Configuration	57
3.6.2.11	CSM Configuration	58

3.6.2.12	USB Configuration .....	59
3.6.3	Chipset.....	60
3.6.3.1	System Agent (SA) Configuration.....	60
3.6.3.1.1	Graphics Configuration .....	61
3.6.3.1.2	DMI/OPI Configuration .....	61
3.6.3.1.3	PEG Port Configuration .....	62
3.6.3.1.4	Memory Configuration .....	63
3.6.3.2	PCH-IO Configuration.....	63
3.6.3.2.1	PCI Express Configuration .....	64
3.6.3.2.2	SATA And RST Configuration .....	69
3.6.3.2.3	HD Audio Configuration .....	71
3.6.4	Security.....	72
3.6.4.1	Secure Boot menu .....	73
3.6.4.1.1	Key Management.....	74
3.6.5	Boot .....	74
3.6.6	Save and exit.....	75
3.6.6.1	Save Changes and Reset.....	75
3.6.6.2	Discard Changes and Reset.....	75
3.6.6.3	Restore Defaults .....	76
3.6.6.4	Launch EFI Shell from filesystem device .....	76
<b>4.</b>	<b>Drivers Installation.....</b>	<b>77</b>
4.1	Install Chipset Driver .....	78
4.2	Install VGA Driver.....	79
4.3	Install SOL Driver .....	80
4.4	Install Audio Driver (For Realtek ALC892 HD Audio).....	81
4.5	Install LAN Driver .....	82
4.6	Install RST Driver .....	84
<b>5.</b>	<b>Mechanical Drawing .....</b>	<b>86</b>

# 1. Getting Started

## 1.1 Safety Precautions

### Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

### Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

## 1.2 Packing List

Before you begin installing your single board, please make sure that the following materials have been shipped:

- 1 x BC170Q motherboard
- 2 x SATA cable
- 1 x I/O Shield



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If any of the above items is damaged or missing, contact your retailer.

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### 1.3 Document Amendment History

Revision	Date	By	Comment
1 <sup>st</sup>	June 2017		Initial Release

### 1.4 Manual Objectives

This manual describes in details of the BC170Q ATX Motherboard.

We have tried to include as much information as possible but we have not duplicated information that is provided in the standard IBM Technical References, unless it proved to be necessary to aid in the understanding of this board.

We strongly recommend that you study this manual carefully before attempting to set up BC170Q or change the standard configurations. Whilst all the necessary information is available in this manual we would recommend that unless you are confident, you contact your supplier for guidance.

Please be aware that it is possible to create configurations within the CMOS RAM that make booting impossible. If this should happen, clear the CMOS settings, (see the description of the Jumper Settings for details).

If you have any suggestions or find any errors regarding this manual and want to inform us of these, please contact our Customer Service department with the relevant details.

## 1.5 System Specifications

<b>System</b>	
<b>CPU</b>	Intel® LGA1151 Socket Supports 6/7th Generation Core™ i7/ i5/ i3 Processors (Max. TDP at 95W)
<b>BIOS</b>	AMI uEFI BIOS, 128Mbit SPI Flash ROM
<b>System Chipset</b>	Intel® Q170 Express Chipset
<b>I/O Chip</b>	Nuvoton® NCT6106D
<b>System Memory</b>	Four 288-pin DDR4 2400MHz DIMM socket, supports up to 64GB Max
<b>Watchdog Timer</b>	H/W Reset, 5 ~ 255 seconds/5 ~ 255 minutes (*1)
<b>H/W Status Monitor</b>	CPU temperature monitoring Voltages monitoring CPU fan speed control
<b>Expansion</b>	1 x PCI-e x 16 1 x PCI-e x 4 1 x PCI-e x 1 4 x PCI 1 x M.2 2230 KeyA Slot support WiFi module 1 x SIM card slot 1 x Full Size Mini-PCIe with mSATA Support (SATA III)
<b>S3/S4</b>	Yes (S0/S3/S4/S5)
<b>I/O</b>	
<b>USB</b>	4 x USB 3.0, 2 x USB 2.0
<b>GPIO</b>	8-bit GPIO
<b>Display</b>	
<b>Chipset</b>	Intel® Q170 Express chipset
<b>Resolution</b>	VGA: 2048 x 1536@50 Hz HDMI: 4096 x 2160@24 Hz, 2560 x 1600@60 Hz DP: 4096 x 2304@60Hz
<b>Multiple Display</b>	Triple Display
<b>Audio</b>	
<b>AC97 Codec</b>	Realtek ALC892 HD Audio Decoding Controller
<b>Audio Amp</b>	2 x 6W Amplifier
<b>Ethernet</b>	
<b>LAN Chip</b>	1 x Intel® I219LM Gigabit Ethernet PHY 1 x Intel® I211AT PCI-e Gigabit Ethernet
<b>Ethernet Interface</b>	Gigabit Ethernet
<b>Internal I/O</b>	

## BC170Q User's Manual

Connectors	
<b>Fan</b>	1 x 1 x 4 pin, pitch 2.54mm CPU fan connector with smart fan function supported 1 x 1 x 4 pin, pitch 2.54mm System fan connector with smart fan function supported
<b>Buzzer</b>	Onboard
<b>CMOS Battery</b>	1 x Vertical type battery connector Co-lay 1 x 2 Pin Pitch 1.25mm horizontal type battery connector
<b>Power ON</b>	1 x 2 x 5 pin, pitch 2.54mm connector for front panel
<b>Audio</b>	1 x 2 x 5 pin, pitch 2.54mm connector for front Audio
<b>Internal I/O Connector</b>	<p>Storage:</p> <ul style="list-style-type: none"> <li>- 1 x SATA III or 1 x full size Mini PCI-e support mSATA by auto switch IC</li> <li>- 5 x SATA III</li> <li>1 x M.2 KeyA 2230 Slot support WiFi module</li> <li>1 x Full Size Mini PCI-e with mSATA Support (SATA III)</li> </ul> <p>SIM card slot</p> <p>Onboard Infineon SLB9665 support TPM 2.0(co-lay TPM 1.2)</p> <p>COM:</p> <p>COM 1 Pin9 power selection:</p> <ul style="list-style-type: none"> <li>- 1 x 1 x 3 pin, pitch 2.0mm connector for COM1 &amp; COM2 support RS232 with Pin 9, +5V&amp;+12V/RI</li> </ul> <p>COM 2~6:</p> <ul style="list-style-type: none"> <li>- 1 x 2 x 5 pin, pitch 2.54mm BOX connector for COM2 support RS232 with Pin 9, +5V&amp;+12V/RI</li> <li>- 1 x 1 x 3 pin, pitch 2.0mm connector for COM1 &amp; COM2 support RS232 with Pin 9, +5V&amp;+12V/RI</li> <li>- 1 x 2 x 3 pin, pitch 2.00mm connector for COM2: support RS422/485 connector, Pin 5 with +5V</li> </ul> <p>By BIOS setting RS232/422/485 Selection.</p> <p>COM3 ~ 6:</p> <ul style="list-style-type: none"> <li>- 4 x 2 x 5 pin, pitch 2.54mm BOX connector for COM3 ~ 6: support RS-232 connector, Pin 9 with RI Supported</li> </ul> <p>3 x USB 2.0 by pin header, 1 x USB 2.0 By Vertical type A connector</p> <p>1 x 2 x 10 pin, pitch 2.0mm connector for USB 3.0</p> <p>USB Wake up by BIOS Setting</p> <ul style="list-style-type: none"> <li>1 x 1 x 4 pin, pitch 2.54mm CPU fan connector with smart fan function supported</li> <li>1 x 1 x 4 pin, pitch 2.54mm System fan connector with smart fan function supported</li> <li>1 x 1 x 3 pin, pitch 2.54mm System fan connector</li> <li>1 x 2 x 5 pin, pitch 2.54mm connector for front panel</li> <li>1 x 2 x 10 pin, pitch 2.54mm connector for Auxiliary panel</li> <li>1 x 4 pin, pitch 2.54mm connector for Speaker Buzzer</li> <li>1 x 2 x 5 pin, pitch 2.54mm connector for front Audio</li> </ul>

	<p>1 x 4 pin, pitch wafer 2.00mm connector for 6W x 2 Speaker</p> <p>1 x 1 x 4 pin, pitch 2.54mm connector for S/PDIF</p> <p>1 x 1 x 3 pin, pitch 2.54mm connector for COMS Clear</p> <p>1 x Vertical type battery connector</p> <p>Co-lay 1 x 2 pin Pitch 1.25mm horizontal type battery connector</p> <p>1 x 2 x 6 pin, pitch 2.00mm connector for 8 bits GPIO</p> <p>1 x 6 pin, pitch 2.00mm connector for SGPIO (Only support C236 PCH platform)</p> <p>1 x 5 pin, pitch 2.54mm connector for SMBus</p> <p>1 x 2 x 4 pin, pitch 2.00mm connector for BIOS SPI</p> <p>1 x 2 x 5 pin, pitch 2.0mm connector for LPC</p> <p>Onboard buzzer</p> <p>1 x 2 x 13 pin, pitch 2.54mm wafer connector for LPT (Only at Intel® LGA1151 Socket Supports 6th Generation)</p> <p>1 x 1 x 6 pin, pitch 2.5mm wafer connector for PS2 KB / Mouse</p> <p>1 x 1 x 3 pin pitch 2.00mm connector for AT/ATX jumper</p> <p>1 x 2 x 12 pin ATX power connector</p> <p>1 x 2 x 4 pin ATX 12V power connector</p>
<b>Rear I/O Connectors</b>	
<b>USB</b>	4 x USB3.0
<b>LAN</b>	<p>1 x Intel® I219LM Gigabit Ethernet PHY</p> <p>1 x Intel® I211AT PCI-e Gigabit Ethernet</p>
<b>HDMI</b>	1 x HDMI
<b>Rear Side External I/O Connector</b>	<p>2 x RJ-45 with Dual deck USB3.0 connector</p> <p>1 x VGA</p> <p>1 x DP</p> <p>1 x HDMI</p> <p>COM1 support RS-232 DB9 connector, Pin 9 with / +5V&amp;+12V/RI Supported</p> <p>1 x Line-out ,1 x Mic-In,1 x Line-in</p> <p>PS/2 KB/MS + USB2.0 connector</p>
<b>Mechanical &amp; Environmental</b>	
<b>Power Requirement</b>	+12V/+5V/5VSB/+3.3V/-12V
<b>ACPI</b>	Single power ATX Support S0, S3, S4, S5
<b>Power on Type</b>	AT/ATX mode
<b>Operating Temp.</b>	0 ~ 60°C (32~140°F)
<b>Storage Temp.</b>	-40 ~ 75°C
<b>Operating Humidity</b>	0% ~ 90% relative humidity, non-condensing

## BC170Q User's Manual

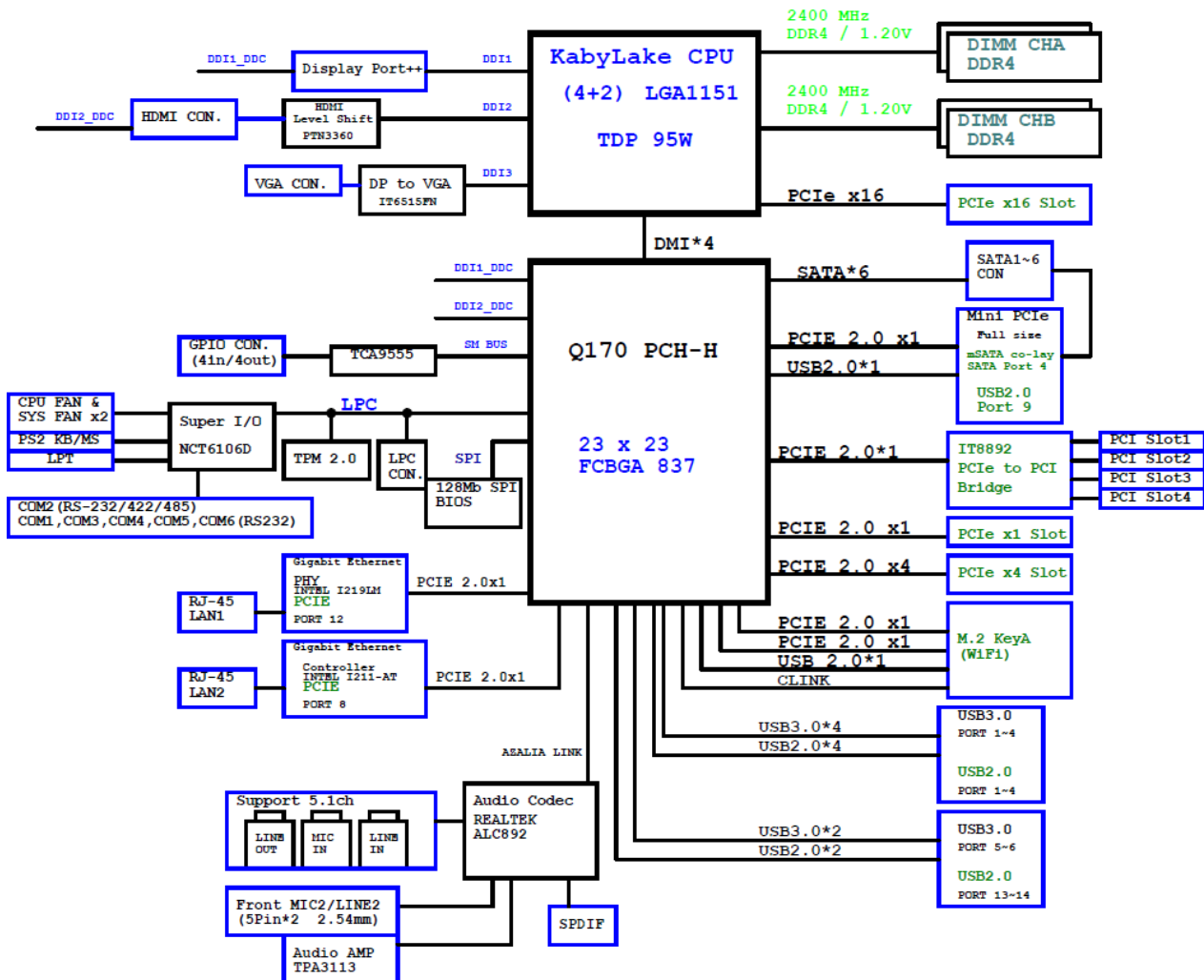
<b>Size (L x W)</b>	12" x 9.6" (304.8mm x 243.84mm)
<b>Weight</b>	0.60 kg



**Note:** Specifications are subject to change without notice.

## 1.6 Architecture Overview—Block Diagram

The following block diagram shows the architecture and main components of BC170Q.

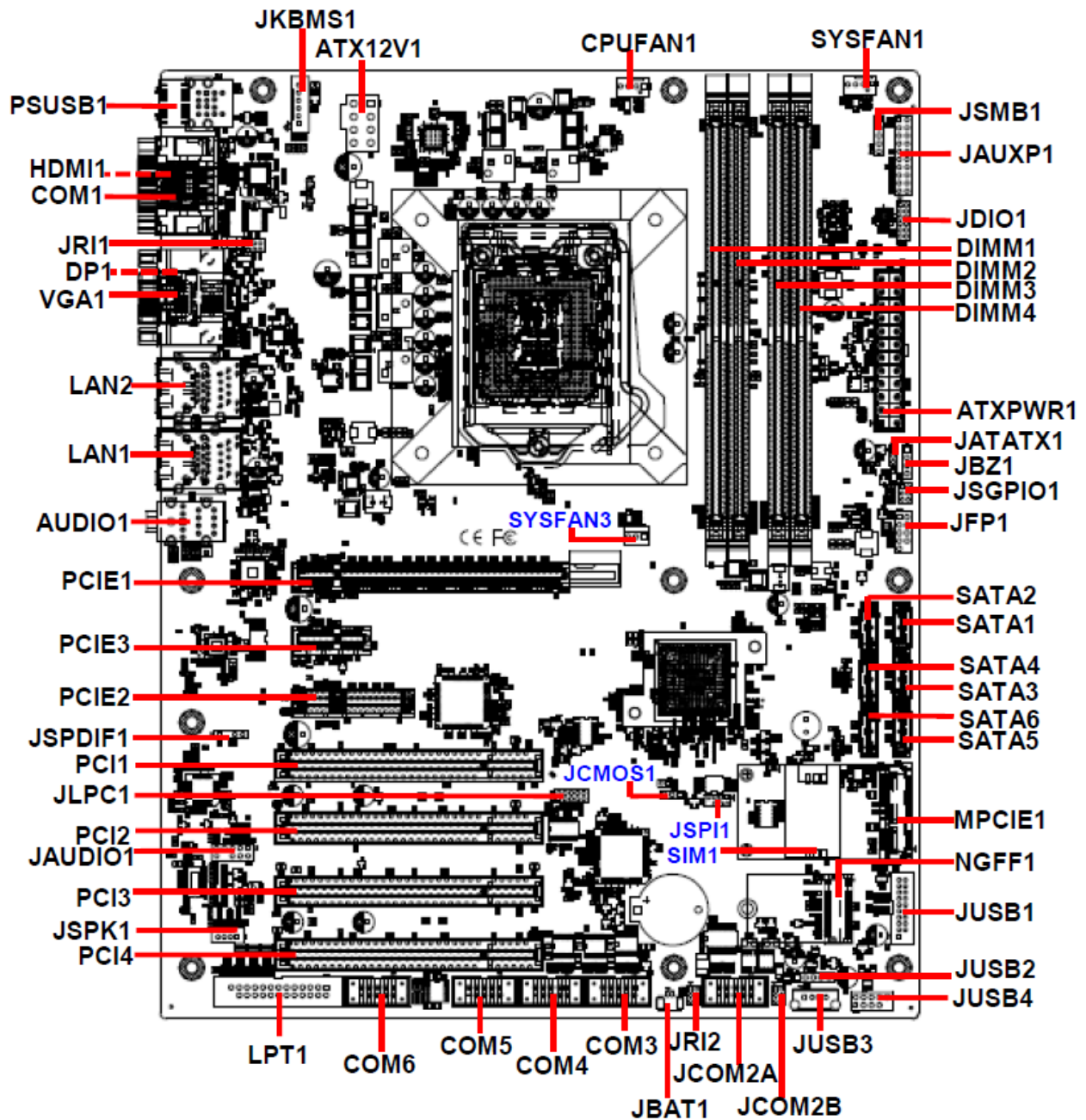


# 2. Hardware Configuration

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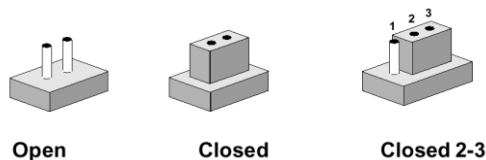
## 2.1 Product Overview



## 2.2 Jumper and Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

The following tables list the function of each of the board's jumpers and connectors.

### Jumpers

Label	Function	Note
JRI1/2	Serial port 1/2 pin9 signal select	3 x 2 header, pitch 2.00mm
JATATX1	AT/ATX Power Mode Select	3 x 1 header, pitch 2.00mm
JCMOS1	Clear CMOS	3 x 1 header, pitch 2.54mm

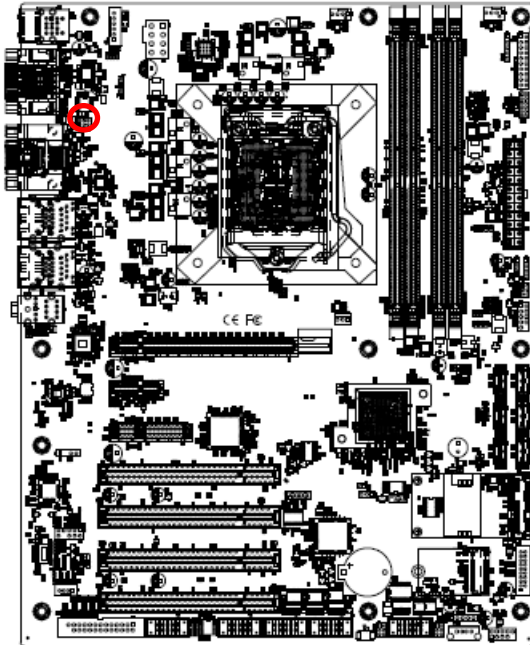
### Connectors

Label	Function	Note
CPUFAN1	CPU fan connector	4 x 1 wafer, pitch 2.54mm
SYSFAN1	System fan connector 1 (with smart fan function supported)	4 x 1 wafer, pitch 2.54mm
SYSFAN3	System fan connector 2	3 x 1 wafer, pitch 2.54mm
JFP1	Miscellaneous setting connector	5 x 2 header, pitch 2.54 mm
DIMM1/2/3/4	288-pin DDR4 DIMM socket	
AUDIO1	Audio connector	

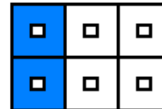
<b>JAUDIO1</b>	Audio connector 2	5 x 2 header, pitch 2.54 mm
<b>JAUXP1</b>	Auxiliary Panel connector	10 x 2 header, pitch 2.54 mm
<b>JSPI1</b>	SPI connector	4 x 2 header, pitch 2.00mm
<b>COM1</b>	Serial Port 1 connector	D-sub 9 pin, male
<b>JCOM2A</b>	Serial Port 2 connector	5 x 2 wafer, pitch 2.54mm
<b>JCOM2B</b>	COM2 RS485/422 connector	3 x 2 header, pitch 2.00 mm
<b>JCOM3/4/5/6</b>	Serial Port 3/4/5/6 connector	5 x 2 wafer, pitch 2.54mm
<b>JBZ1</b>	External Speaker connector	4 x 1 header, pitch 2.54 mm
<b>JDIO1</b>	General purpose I/O connector	6 x 2 header, pitch 2.00mm
<b>JSGPIO1</b>	SGPIO connector	3 x 2 header, pitch 2.00 mm
<b>JSPK1</b>	Speaker connector	1 x 4 wafer, pitch 2.00 mm
<b>PSUSB1</b>	PS/2 keyboard & mouse connector 2 x USB 2.0 connector	
<b>JKBMS1</b>	PS/2 keyboard & mouse connector	6 x 1 wafer, pitch 2.50 mm
<b>LAN1/2</b>	2 x RJ-45 with Dual deck USB 3.0 connector	
<b>JUSB1</b>	USB connector 1	10 x 2 wafer, pitch 2.00mm
<b>JUSB2</b>	USB connector 2	5 x 1 header, pitch 2.54mm
<b>JUSB3</b>	USB connector 3	
<b>JUSB4</b>	USB connector 4	5 x 2 header, pitch 2.54mm
<b>JLPC1</b>	LPC connector	5 x 2 header, pitch 2.00mm
<b>PCIE1/2/3</b>	PCIe slot 1/2/3	
<b>PCI1/2/3/4</b>	PCI slot 1/2/3/4	
<b>JBAT1</b>	Battery connector	2 x 1 wafer, pitch 1.25mm
<b>MPCIE1</b>	Mini-PCI connector	
<b>ATXPWR1</b>	ATX Power connector	12 x 2 wafer, pitch 4.20mm
<b>ATX12V1</b>	Power connector	2 x 4 wafer, pitch 4.20mm
<b>SATA1~6</b>	Serial ATA connector 1~6	
<b>HDMI1</b>	HDMI connector	
<b>DP1</b>	DP connector	
<b>VGA1</b>	VGA connector	
<b>NGFF1</b>	M.2 2230 KeyA Slot support WiFi module	
<b>LPT1</b>	LPT connector	13 x 2 header, pitch 2.54mm
<b>JSIM1</b>	SIM card slot	
<b>JSPDIF1</b>	S/PDIF connector	
<b>JSMB1</b>	SMBus connector	5 x 1 header, pitch 2.54mm

## 2.3 Setting Jumpers & Connectors

### 2.3.1 Serial port 1 pin9 signal select (JRI1)

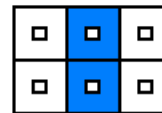


Ring\*



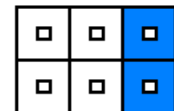
1 5

+5V



1 5

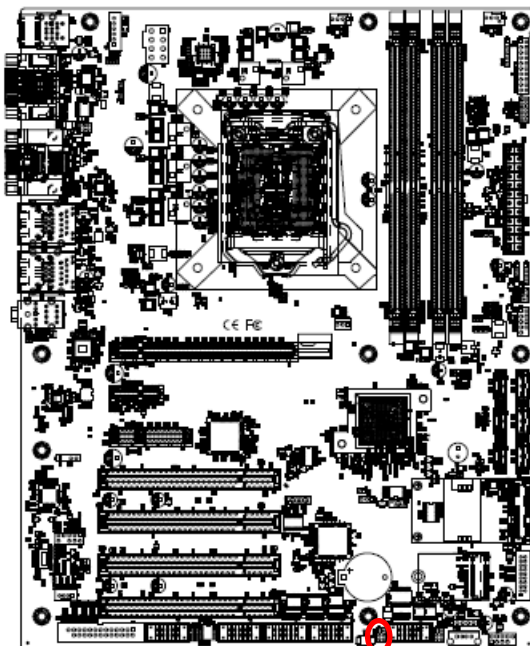
+12V



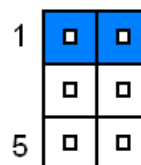
1 5

\* Default

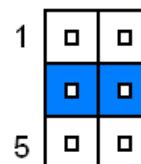
### 2.3.2 Serial port 2 pin9 signal select (JRI2)



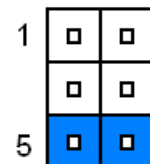
Ring\*



+5V

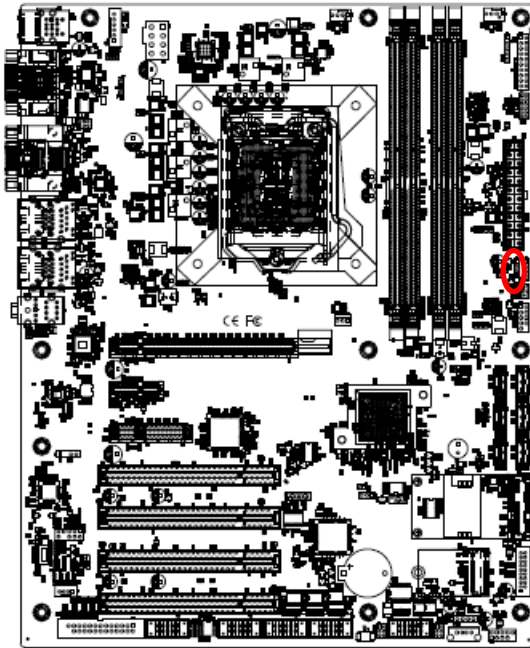


+12V

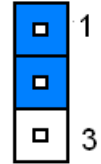


\* Default

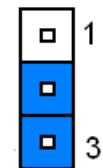
### 2.3.3 AT/ATX Power Mode Select (JATATX1)



AT\*

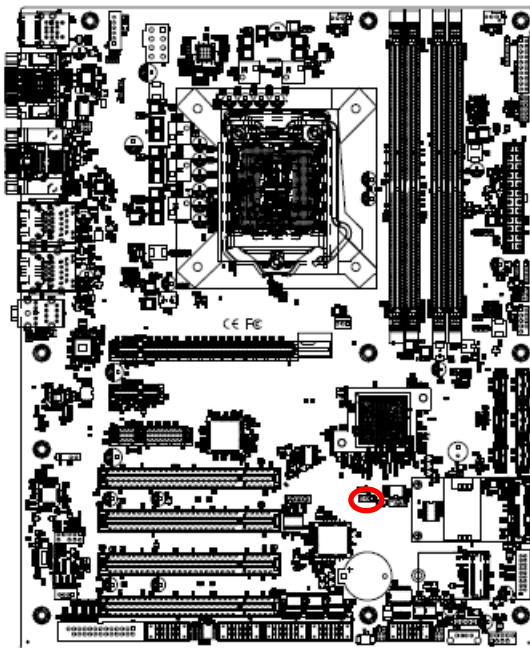


ATX

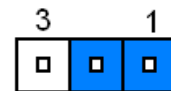


\* Default

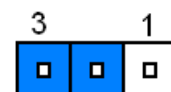
### 2.3.4 Clear CMOS (JCMOS1)



Protect\*

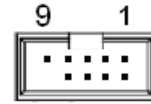
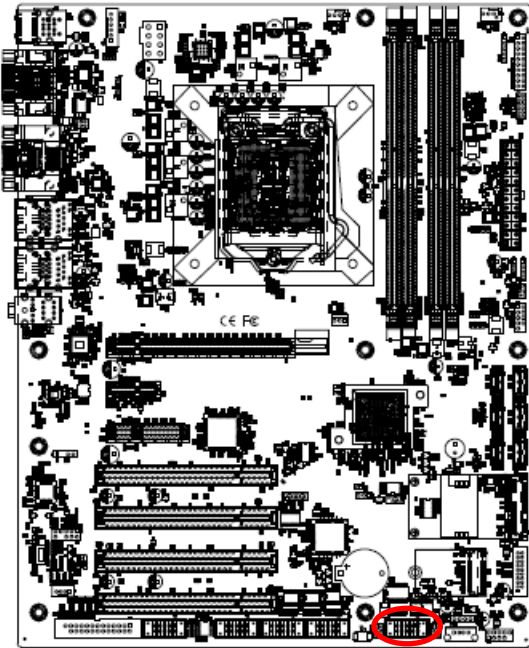


Clear CMOS



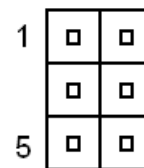
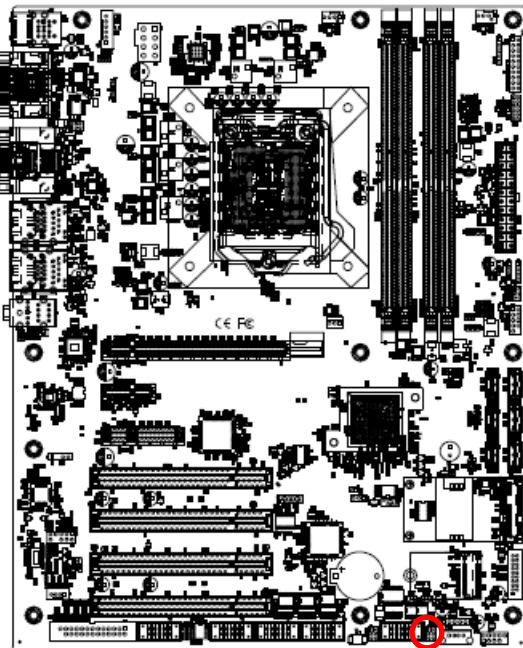
\* Default

2.3.5 Serial port 2 connector (JCOM2A)



Signal	PIN	PIN	Signal
NRXD	2	1	NDCD#
NDTR#	4	3	NTXD
NDSR#	6	5	GND
NCTS#	8	7	NRTS#
		9	JNRI#

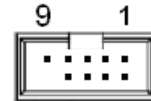
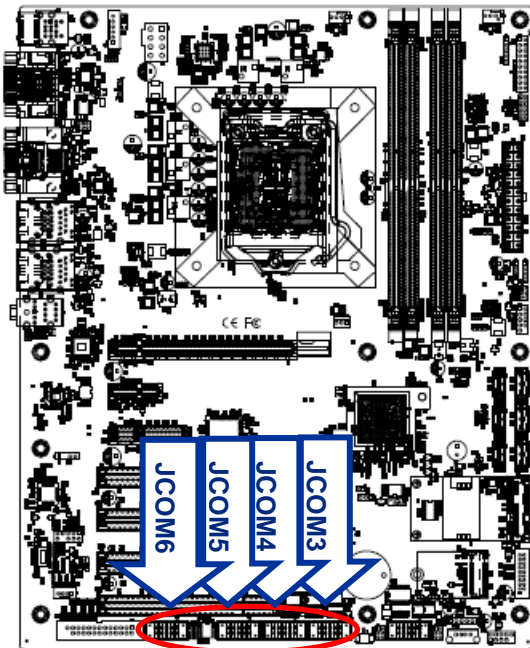
2.3.6 COM2 RS485/422 connector (JCOM2B)



Signal	PIN	PIN	Signal
485TX-	1	2	422RX-
485TX+	3	4	422RX+
+5V	5	6	GND

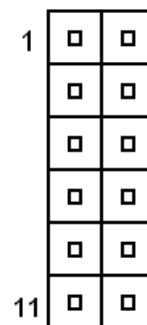
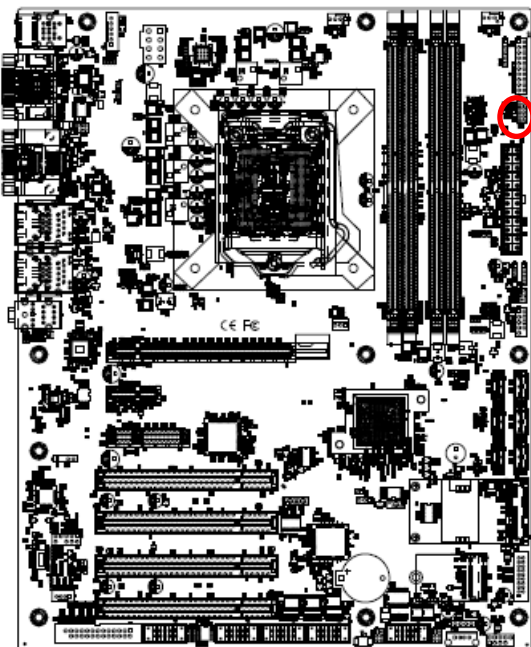


### 2.3.7 Serial port 3/4/5/6 connector (JCOM3/4/5/6)



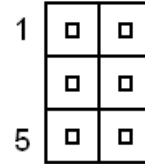
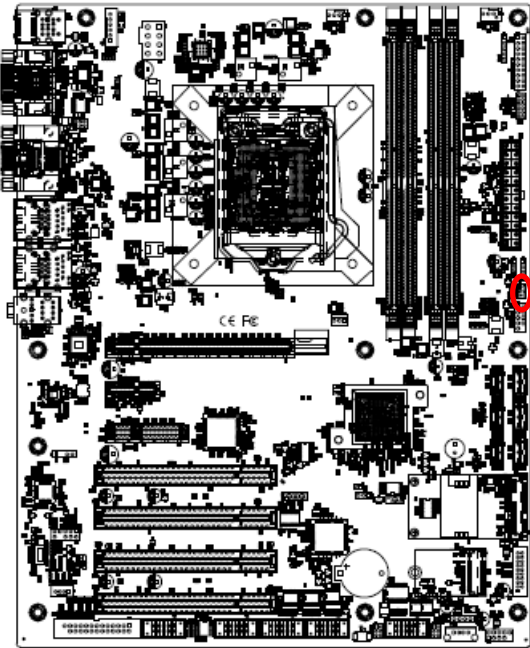
Signal	PIN	PIN	Signal
NRXD	2	1	NDCD#
NDTR#	4	3	NTXD
NDSR#	6	5	GND
NCTS#	8	7	NRTS#
NC	10	9	NRI#

### 2.3.8 General purpose I/O connector (JDIO1)



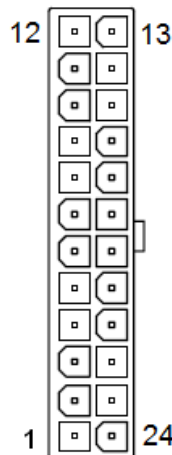
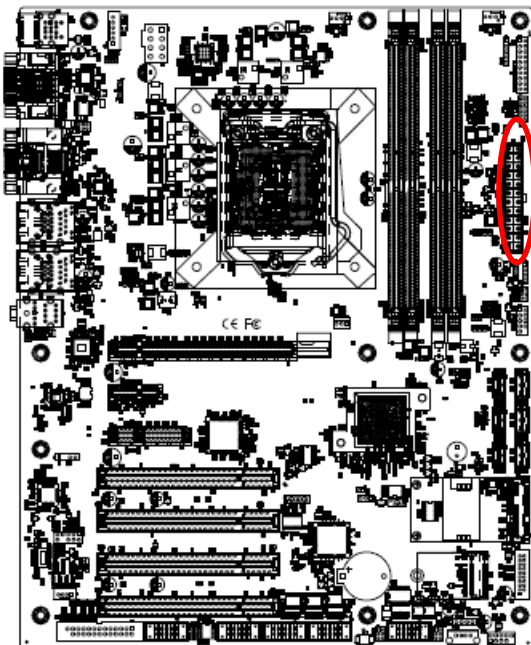
Signal	PIN	PIN	Signal
DI0	1	2	DO0
DI1	3	4	DO1
DI2	5	6	DO2
DI3	7	8	DO3
SMB_CLK_9555	9	10	SMB_DATA_9555
GND	11	12	+5V

2.3.9 SGPIO connector (JSGPIO1)



Signal	PIN	PIN	Signal
GND	1	2	GND
SGIO_LOAD	3	4	SGIO_DATOUT0
SGIO_CLK	5	6	SGIO_DATOUT1

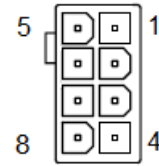
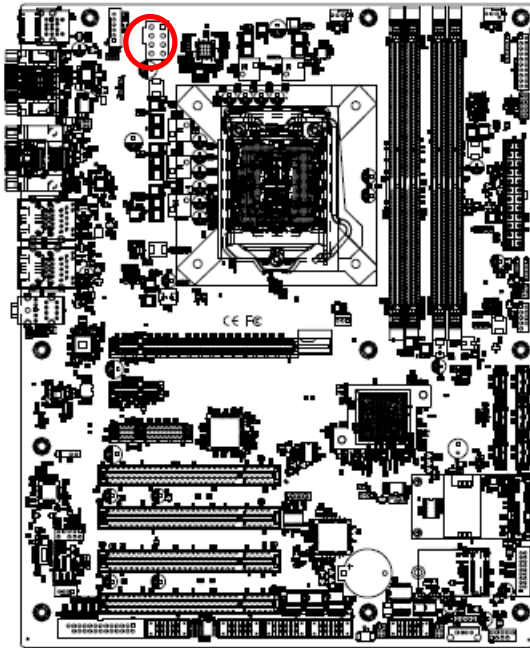
2.3.10 ATX Power connector (ATXPWR1)



Signal	PIN	PIN	Signal
+3.3V	12	24	GND
+12V	11	23	+5V
+12V	10	22	+5V
+V5SB	9	21	+5V
ATX_PWRGD	8	20	-5V
GND	7	19	GND
+5V	6	18	GND
GND	5	17	GND
+5V	4	16	ATX_PSON#
GND	3	15	GND
+3.3V	2	14	-12V
+3.3V	1	13	+3.3V

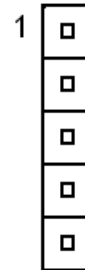
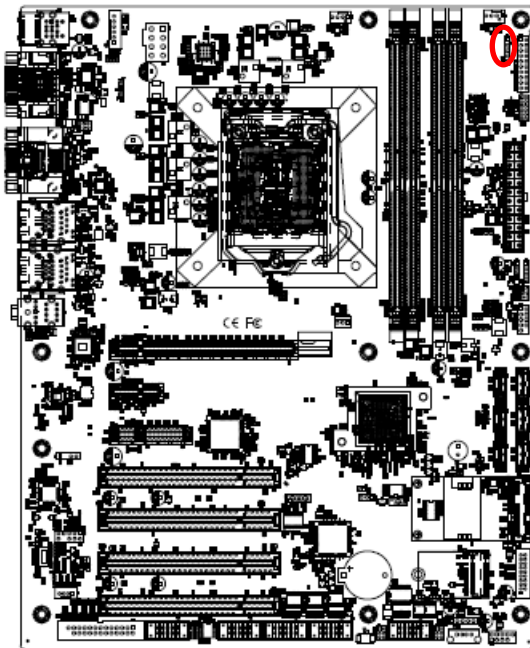


### 2.3.11 Power connector (ATX12V1)



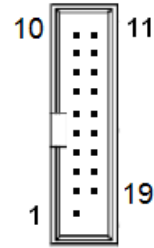
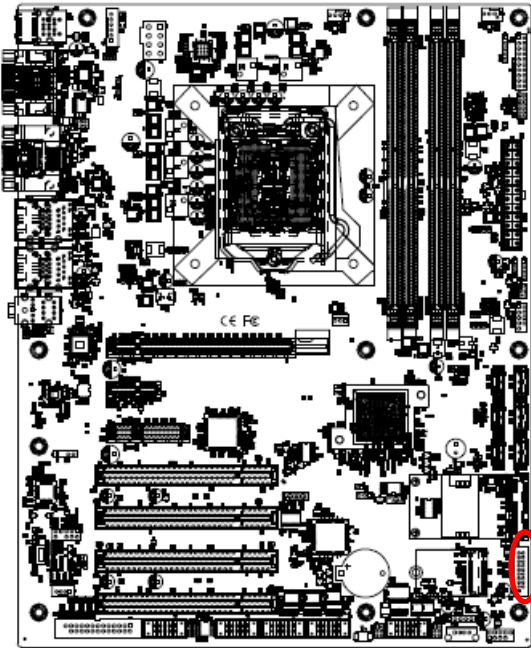
Signal	PIN	PIN	Signal
+12V	5	1	GND
+12V	6	2	GND
+12V	7	3	GND
+12V	8	4	GND

### 2.3.12 SMBus connector (JSMB1)



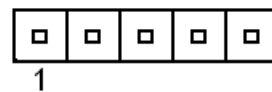
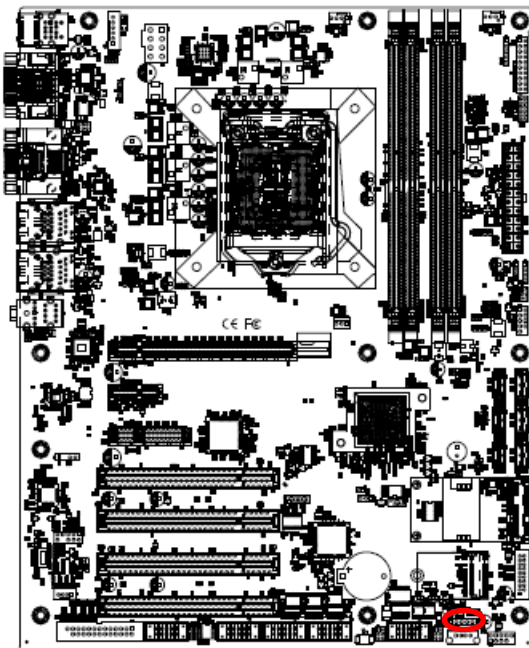
Signal	PIN
SMB_CLK_MAIN	1
SMB_DATA_MAIN	2
SMB_ALERT#_MAIN	3
GND	4
+3.3V	5

2.3.13 USB connector 1 (JUSB1)



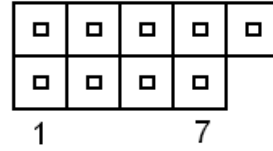
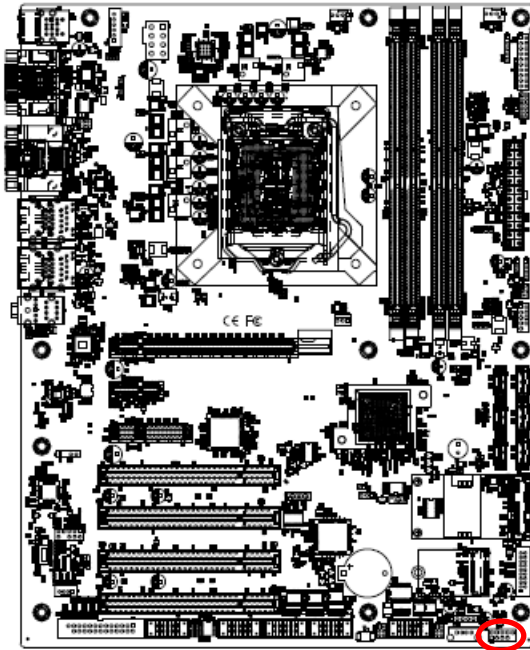
Signal	PIN	PIN	Signal
NC	10	11	USB_R_DP14
USB_R_DP13	9	12	USB_R_DN14
USB_R_DN13	8	13	GND
GND	7	14	SS_USB_TXP_C_6
SS_USB_TXP_C_5	6	15	SS_USB_TXN_C_6
SS_USB_TXN_C_5	5	16	GND
GND	4	17	SS_USB_RXP_C_6
SS_USB_RXP_C_5	3	18	SS_USB_RXN_C_6
SS_USB_RXN_C_5	2	19	USBVCC_DE
USBVCC_DE	1		

2.3.14 USB connector 2 (JUSB2)



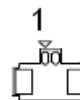
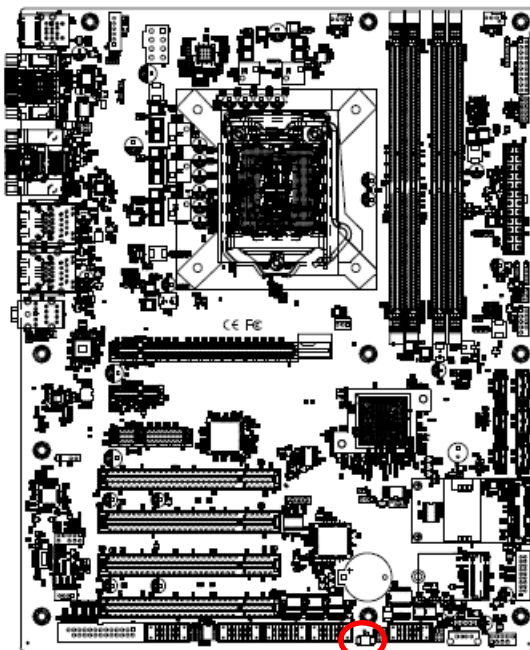
Signal	PIN
USBVCC56	1
USB_R_DN5	2
USB_R_DP5	3
GND	4
GND	5

### 2.3.15 USB connector 4 (JUSB4)



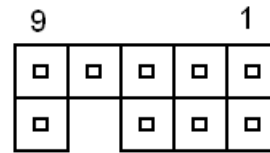
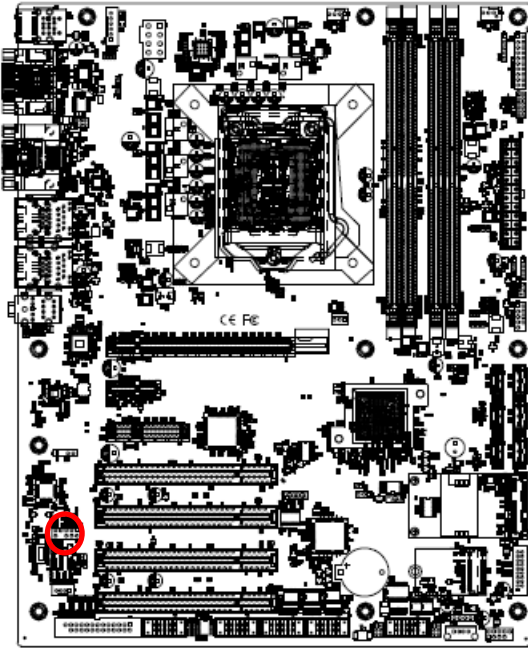
Signal	PIN	PIN	Signal
USBVCC_BC	1	2	USBVCC_BC
USB_R_DN12	3	4	USB_R_DN11
USB_R_DP12	5	6	USB_R_DP11
GND	7	8	GND
		10	NC

### 2.3.16 Battery connector (JBAT1)



Signal	PIN
RTC_VBAT_1	1
GND	2

2.3.17 Audio connector (JAUDIO1)

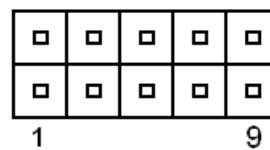
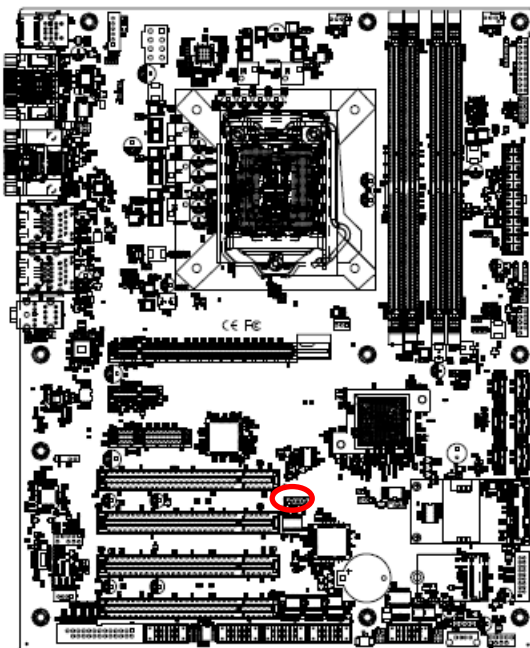


Signal	PIN	PIN	Signal
MIC2_L	1	2	GND
MIC2_R	3	4	ACZ_DET#_R
LNE2_RIN	5	6	MIC2_JD
GND	7		
LINE2_LIN	9	10	LINE2_JD

2.3.17.1 Signal Description –Audio connector (JAUDIO1)

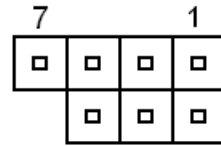
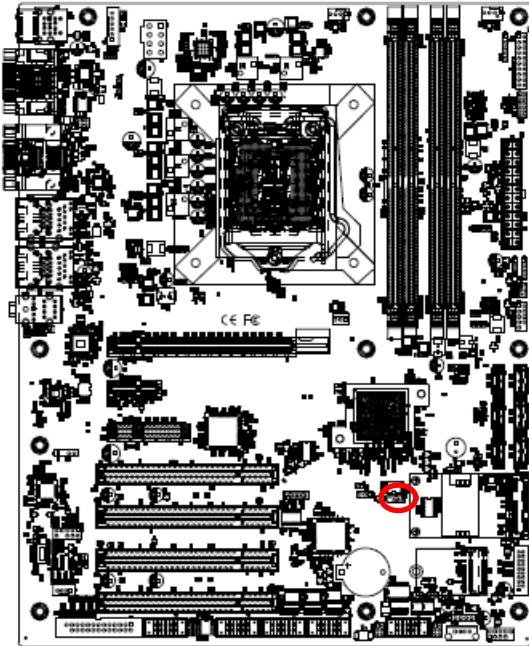
Signal	Signal Description
LINE2_JD	AUDIO IN (LINE_RIN/LIN)sense pin
MIC2_JD	MIC IN (MIC_RIN/LIN) sense pin

2.3.18 LPC connector (JLPC1)



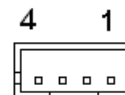
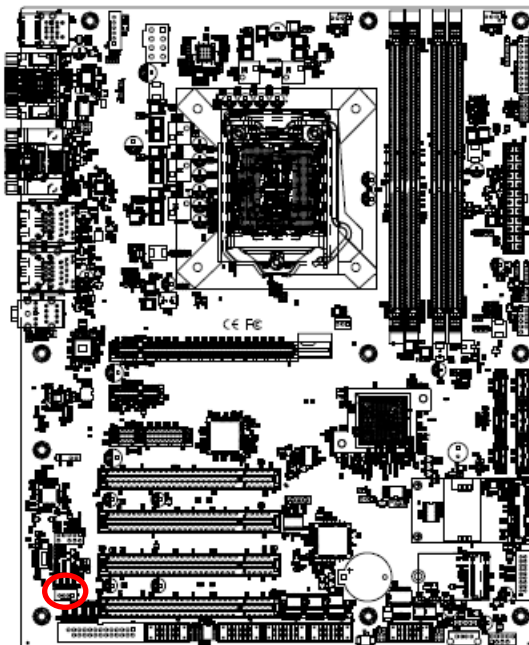
Signal	PIN	PIN	Signal
LPC_AD0_R	1	2	+3.3V
LPC_AD1_R	3	4	BUF_PLT_RST#
LPC_AD2_R	5	6	LPC_FRAME#_R
LPC_AD3_R	7	8	LPC_CLK
LPC_SERIRQ_R	9	10	GND

### 2.3.19 SPI connector (JSPI1)



Signal	PIN	PIN	Signal
+3.3V	1	2	GND
SSPI_CS0#_R	3	4	SSPI_SCLK_R
SSPI_SO_R	5	6	SSPI_SI_R
SSPI_HOLD#0	7		

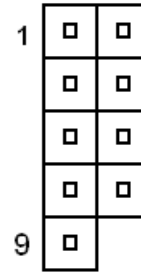
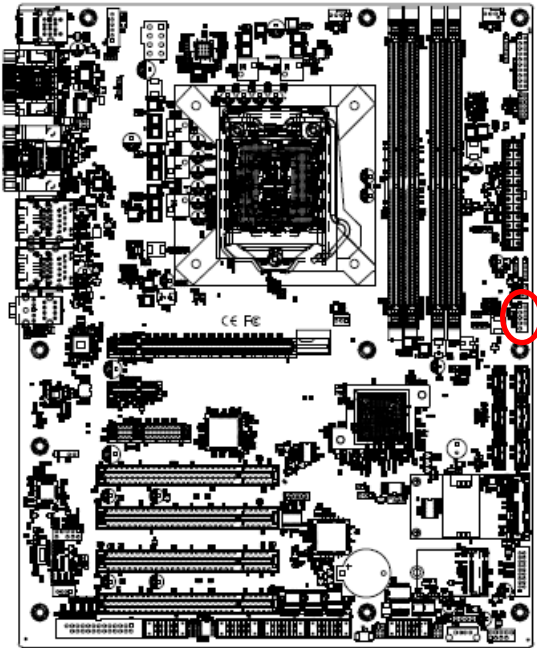
### 2.3.20 Speaker connector (JSPK1)



Signal	PIN
SPK_L+	1
SPK_L-	2
SPK_R+	3
SPK_R-	4

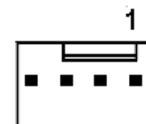
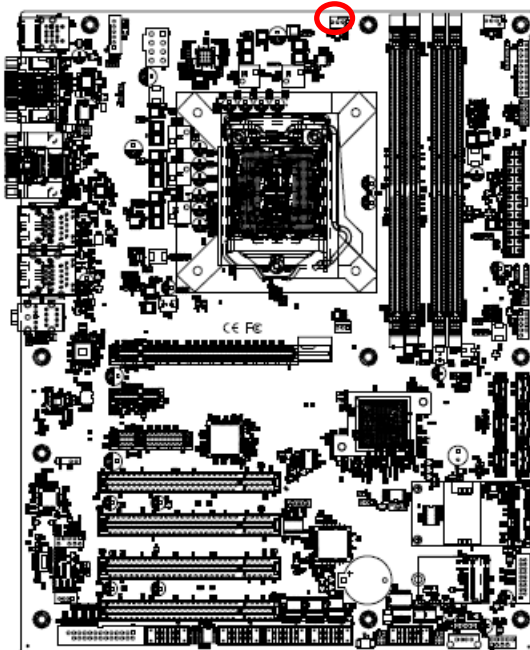


2.3.21 Miscellaneous setting connector (JFP1)



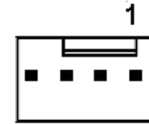
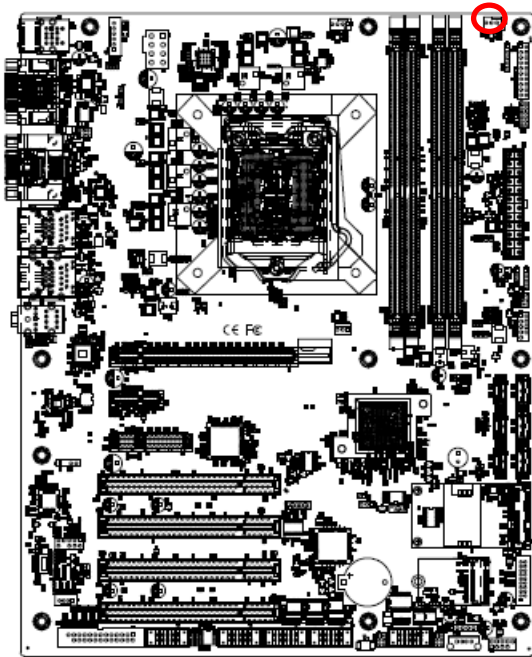
Signal	PIN	PIN	Signal
HDD_LED+	1	2	PWR_LED+
HDD_LED-	3	4	PWE_LED-
RSET_BTN#	5	6	PWRBTN#
GND	7	8	GND
NC	9		

2.3.22 CPU fan connector (CPUFAN1)



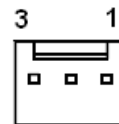
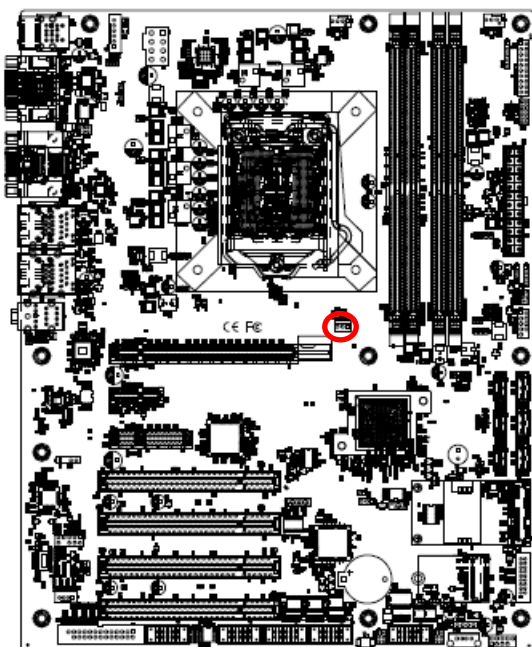
Signal	PIN
GND	1
+12V	2
CPUFANIN	3
CPUFANOUT	4

### 2.3.23 System fan connector 1 (SYSFAN1)



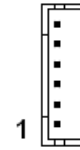
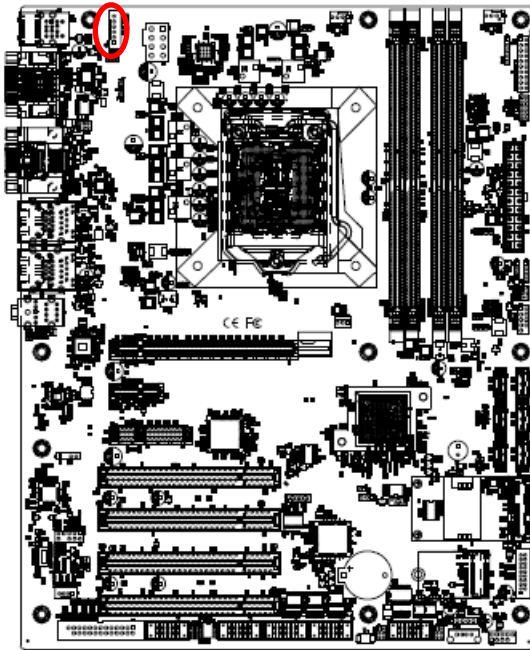
Signal	PIN
GND	1
+12V	2
SYSFANIN1	3
SYSFANOUT1	4

### 2.3.24 System fan connector 2 (SYSFAN3)



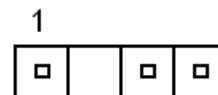
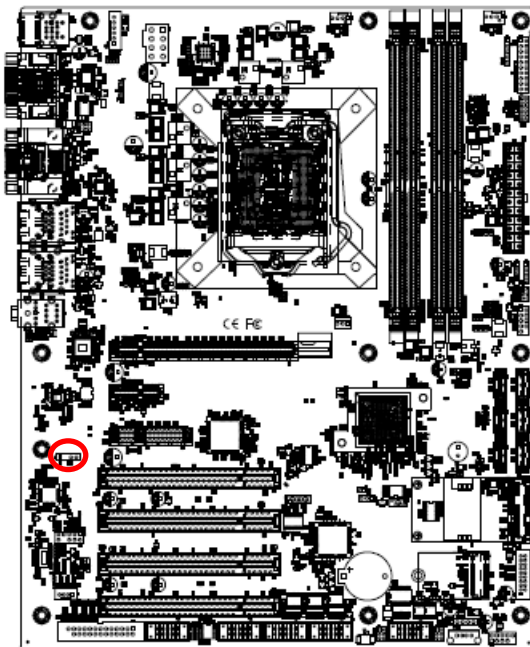
Signal	PIN
GND	1
+12V	2
SYS_FAN_IN_2	3

2.3.25 PS/2 keyboard & mouse connector (JKBMS1)



Signal	PIN
MSCK	6
+5V	5
GND	4
MSDT	3
KBDT	2
KBCK	1

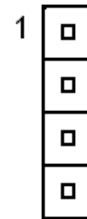
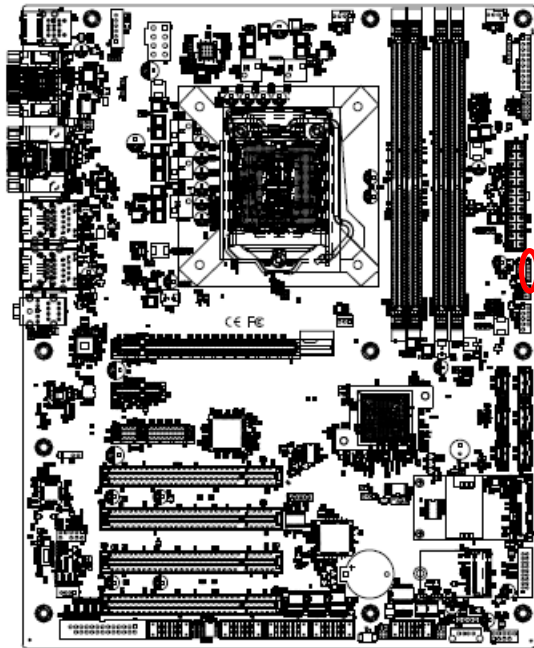
2.3.26 S/PDIF connector (JSPDIF1)



Signal	PIN
+5V	1
SPDIF_O	3
GND	4

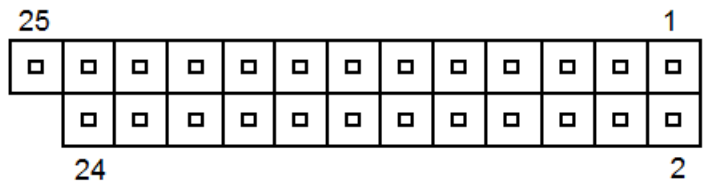
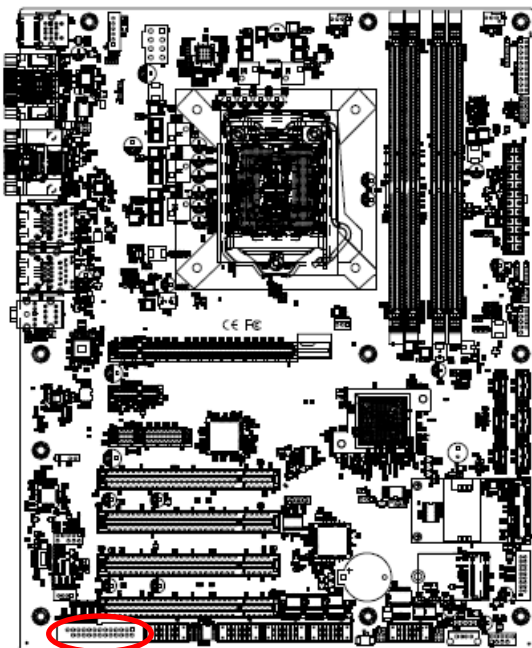


2.3.27 External Speaker connector (JBZ1)



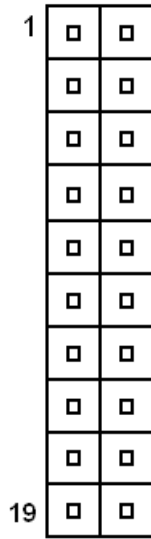
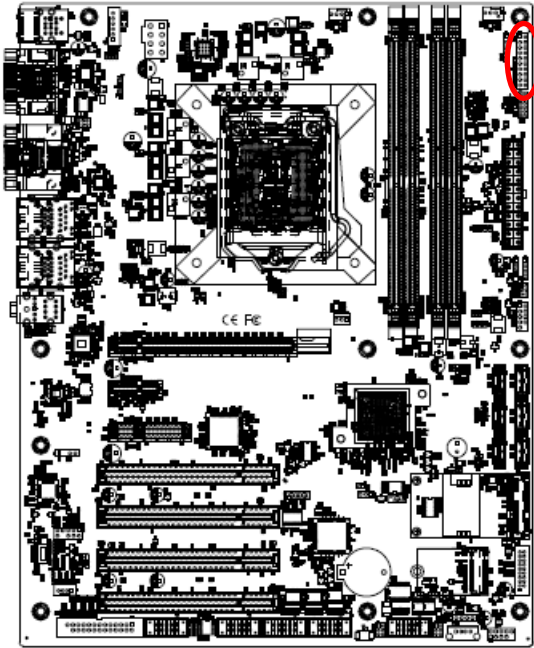
Signal	PIN
+5V	1
NC	2
NC	3
SIO_BEEP	4

2.3.28 LPT connector (LPT1)



Signal	PIN	PIN	Signal
PT-STB-	1	2	PT_AFD#
PTD0	3	4	ERR#
PTD1	5	6	PT_PAR_INIT#
PTD2	7	8	PT_SLIN#
PTD3	9	10	GND
PTD4	11	12	GND
PTD5	13	14	GND
PTD6	15	16	GND
PTD7	17	18	GND
ACK#	19	20	GND
BUSY	21	22	GND
PE	23	24	GND
SLCT	25		

2.3.29 Auxiliary Panel connector (JAUXP1)



Signal	PIN	PIN	Signal
+5V	1	2	NC
NC	3	4	SMB_CLK_MAIN
CASEOPEN#	5	6	NC
GND	7	8	GND
ERROR_LED	9	10	SMB_DATA_MAIN
ERROR_LED#	11	12	+5V
FRONT_LAN1_ACT	13	14	FRONT_LAN1_LINK100#
GND	15	16	FRONT_LAN1_LINK1000#
FRONT_LAN2_ACT	17	18	FRONT_LAN2_LINK100#
GND	19	20	FRONT_LAN2_LINK1000#

# 3. BIOS Setup

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### 3.1 Introduction

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

### 3.2 Starting Setup

The AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the NVRAM and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

By pressing <Del> or <F2> immediately after switching the system on, or

By pressing the <Del> or <F2> key when the following message appears briefly at the left-top of the screen during the POST (Power On Self Test).

**Press <Del> or <F2> to enter SETUP**

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to.

**Press F1 to Continue, DEL to enter SETUP**

### 3.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Button	Description
↑	Move to previous item
↓	Move to next item
←	Move to the item in the left hand
→	Move to the item in the right hand
Esc key	Main Menu -- Quit and not save changes into NVRAM Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
+ key	Increase the numeric value or make changes
- key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2 key	Previous Values.
F3 key	Optimized defaults
F4 key	Save & Exit Setup

- **Navigating Through The Menu Bar**

Use the left and right arrow keys to choose the menu you want to be in.



**Note:** Some of the navigation keys differ from one screen to another.

- **To Display a Sub Menu**

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A “>” pointer marks all sub menus.

### 3.4 Getting Help

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the F1 key again.

### 3.5 In Case of Problems

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the AMI BIOS supports an override to the NVRAM settings which resets your system to its defaults.

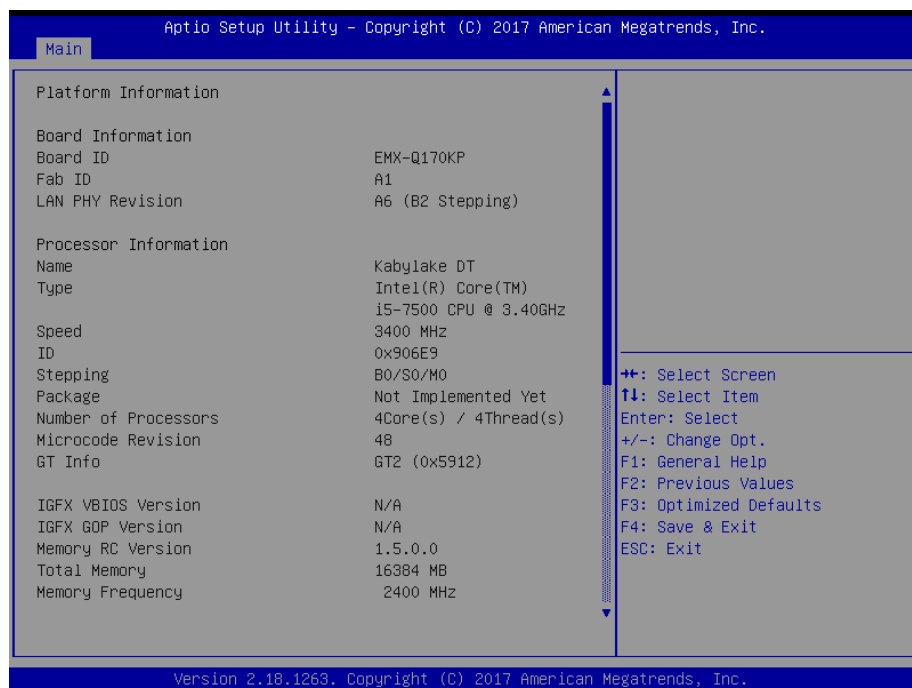
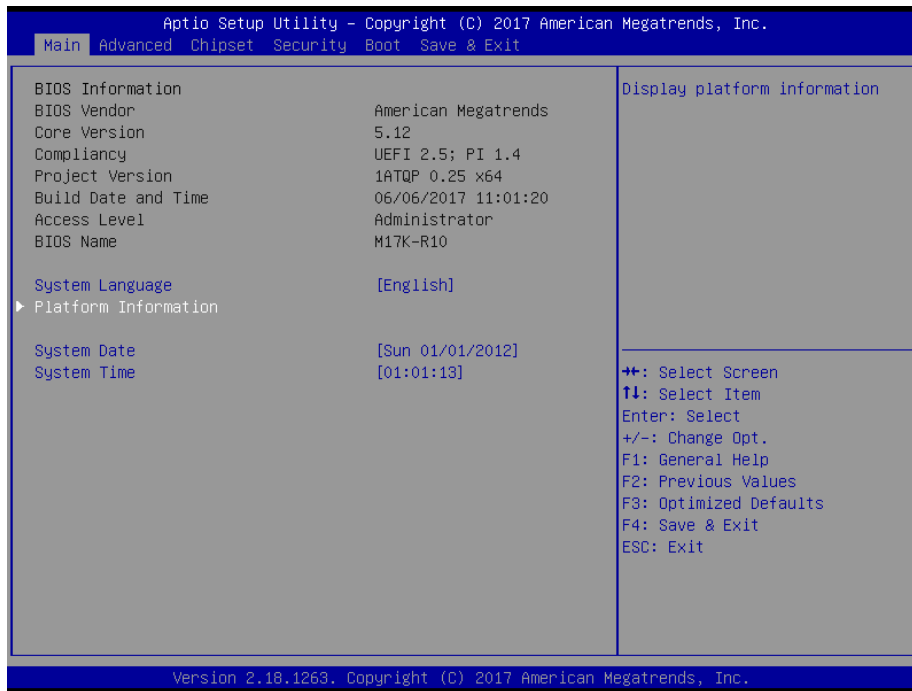
The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both BIOS Vendor and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

### 3.6 BIOS setup

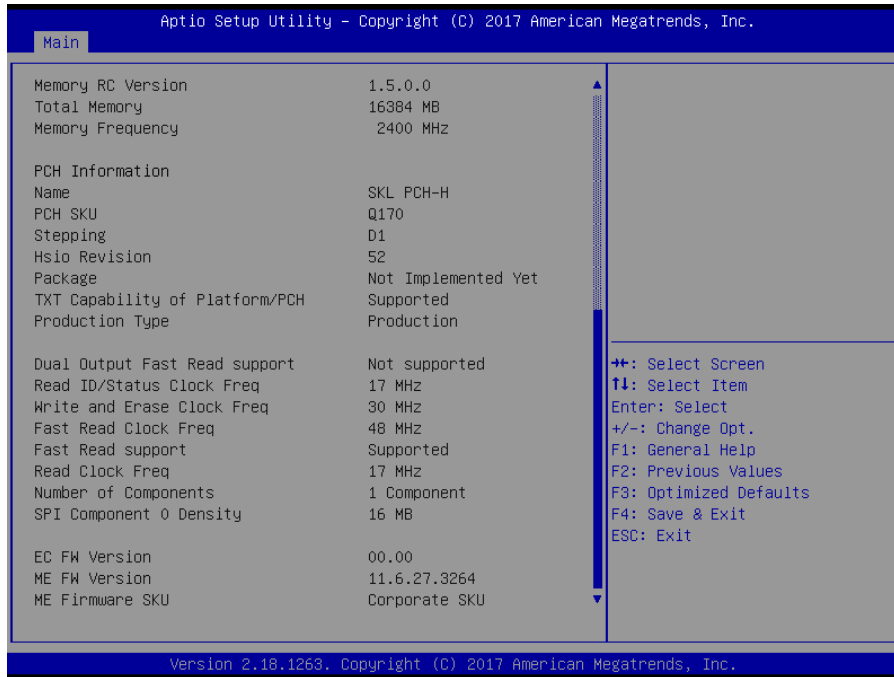
Once you enter the Aptio Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

#### 3.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.



## BC170Q User's Manual



### 3.6.1.1 System Language

This option allows choosing the system default language.

### 3.6.1.2 System Date

Use the system date option to set the system date. Manually enter the day, month and year.

### 3.6.1.3 System Time

Use the system time option to set the system time. Manually enter the hours, minutes and seconds.

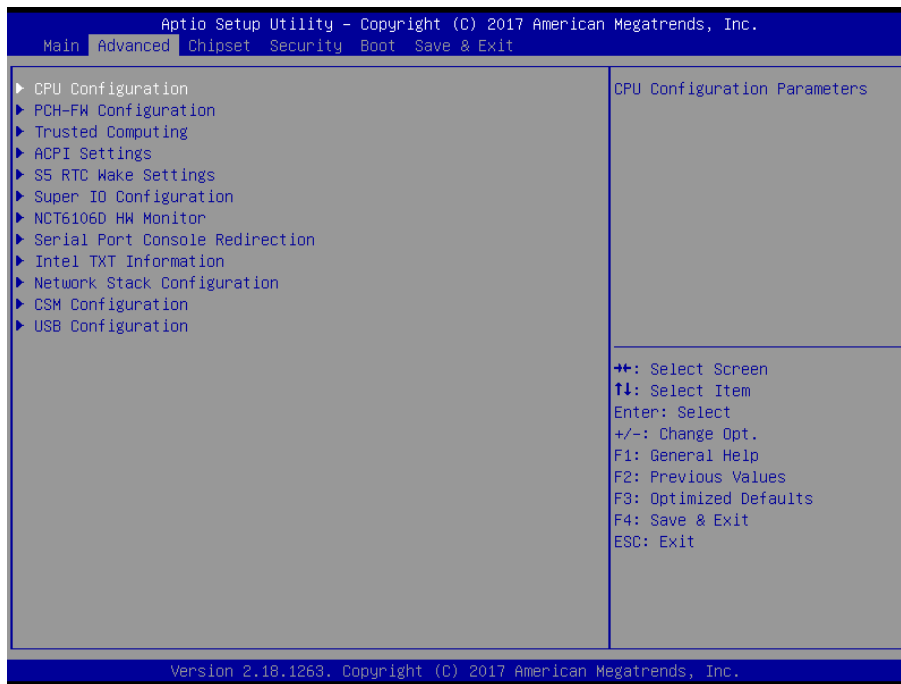


**Note:** The BIOS setup screens shown in this chapter are for reference purposes only, and may not exactly match what you see on your screen.



### 3.6.2 Advanced Menu

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.



#### 3.6.2.1 CPU Configuration

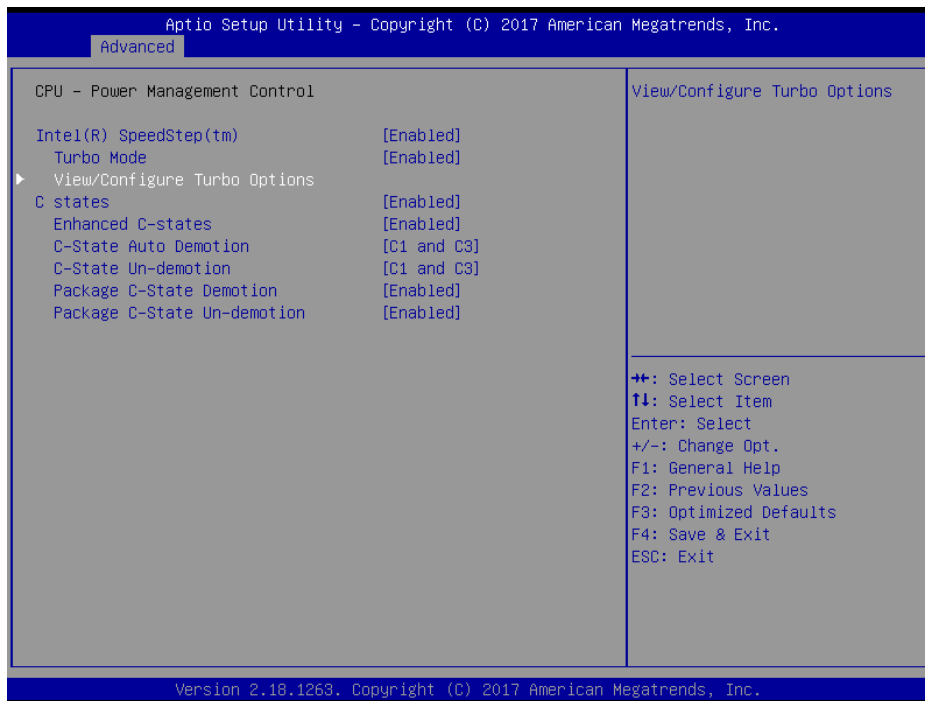


Item	Options	Description
<b>Intel (VMX) Virtualization Technology</b>	Disabled Enabled[Default],	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.

## BC170Q User's Manual

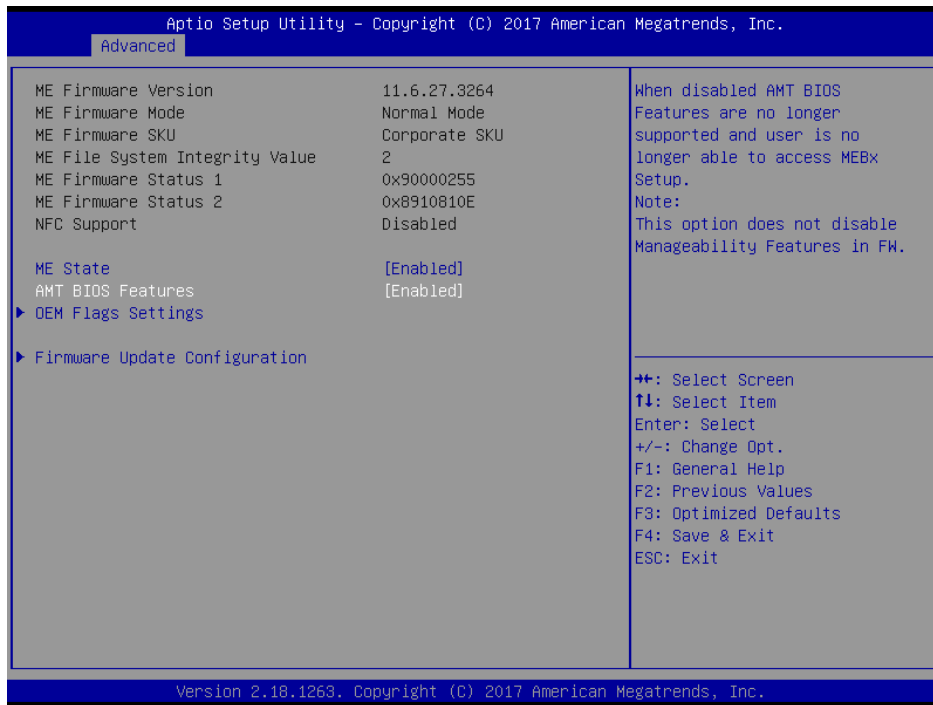
<b>Active Processor Cores</b>	<b>All[Default],</b>	Number of cores to enable in each processor package.
	1	
	2	
	3	

### 3.6.2.1.1 CPU – Power management Control



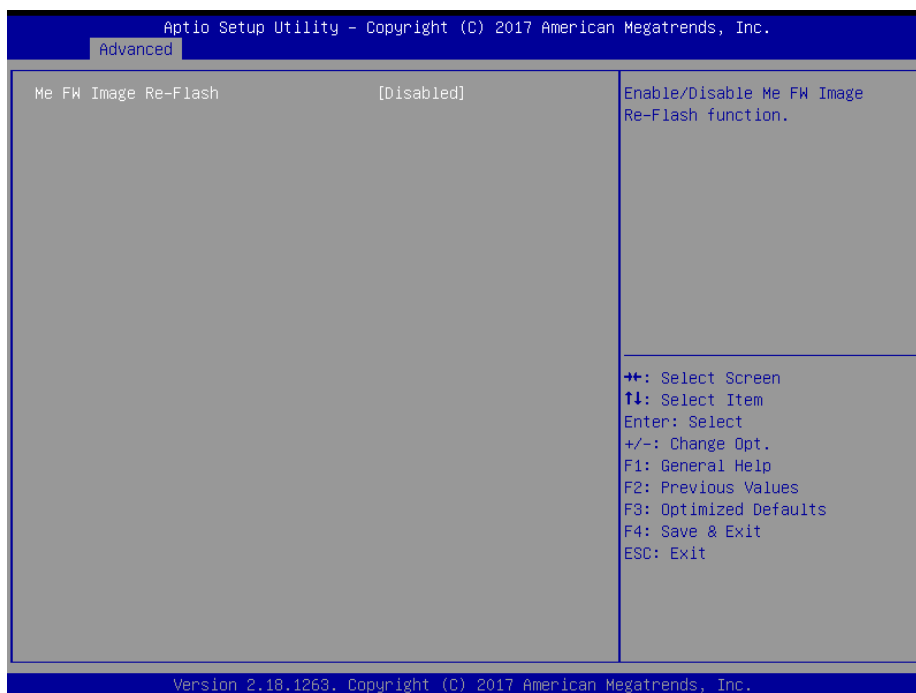
Item	Option	Description
<b>Intel® SpeedStep™</b>	Disabled, Enabled[Default]	Allows more than two frequency ranges to be supported.
<b>Turbo Mode</b>	Disabled, Enabled[Default]	Enable/Disable processor Turbo Mode (requires EMTTM enabled too). AUTO means enabled, unless max turbo ratio is bigger than 16 – SKL A0 W/A.
<b>C states</b>	Disabled, Enabled[Default]	Enable/Disable CPU Power Management. Allows CPU to go to C states when it's not utilized.
<b>Enhanced C-states</b>	Disabled, Enabled[Default]	Enable/Disable C1E. When enabled, CPU will switch to minimum speed when all cores enter C-State.
<b>C-State Auto Demotion</b>	Disabled C1 C3 C1 and C3[Default]	Configure C-State Auto Demotion.
<b>C-State Un-demotion</b>	Disabled C1 C3 C1 and C3[Default]	Configure C-State Un-demotion.
<b>Package C-State Demotion</b>	Disabled, Enabled[Default]	Package C-State Demotion.
<b>Package C-State Un-demotion</b>	Disabled, Enabled[Default]	Package C-State Un-demotion.

### 3.6.2.2 PCH-FW Configuration



Item	Options	Description
<b>ME State</b>	Disabled, Enabled[Default]	When Disabled ME will be put into ME Temporarily Disabled Mode.
<b>AMT BIOS Features</b>	Disabled, Enabled[Default]	When disabled AMT BIOS Features are no longer supported and user is no longer able to access MEBx Setup. Note: This option does not disable Manageability Features in FW.

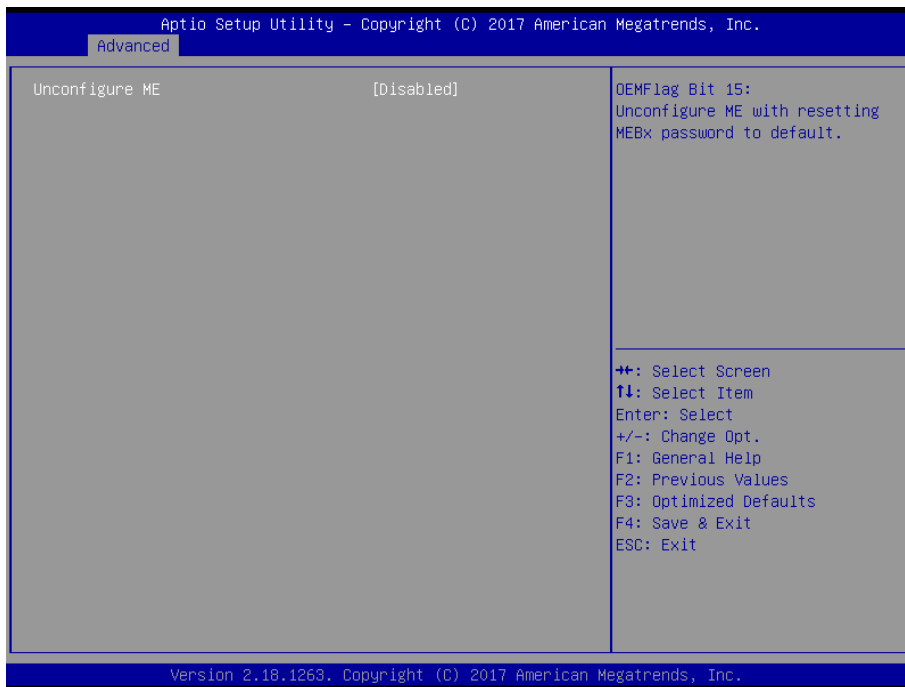
#### 3.6.2.2.1 Firmware Update Configuration



## BC170Q User's Manual

Item	Option	Description
Me FW Image Re-Flash	Disabled[Default], Enabled	Enable/Disable Me FW Image Re-Flash function.

### 3.6.2.2.2 OEM Flags Settings



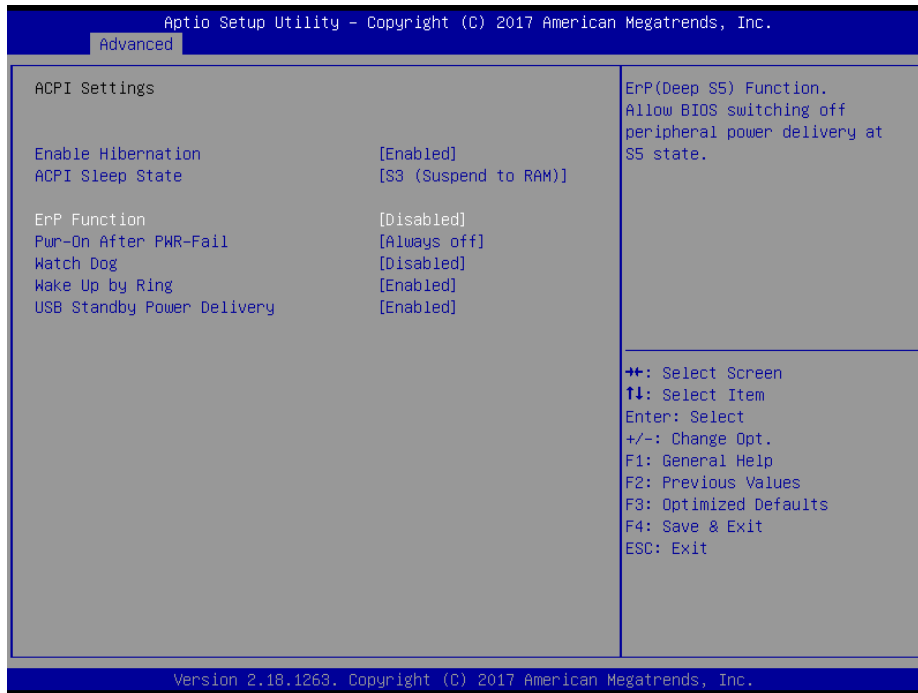
Item	Option	Description
Unconfigure ME	Disabled[Default], Enabled	OEMFlag Bit 15: Unconfigure ME with resetting MEBx password to default.

### 3.6.2.3 Trusted Computing



Item	Options	Description
<b>Security Device Support</b>	Disable, Enable[Default]	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1Ainterface will not be available.

### 3.6.2.4 APCI Settings

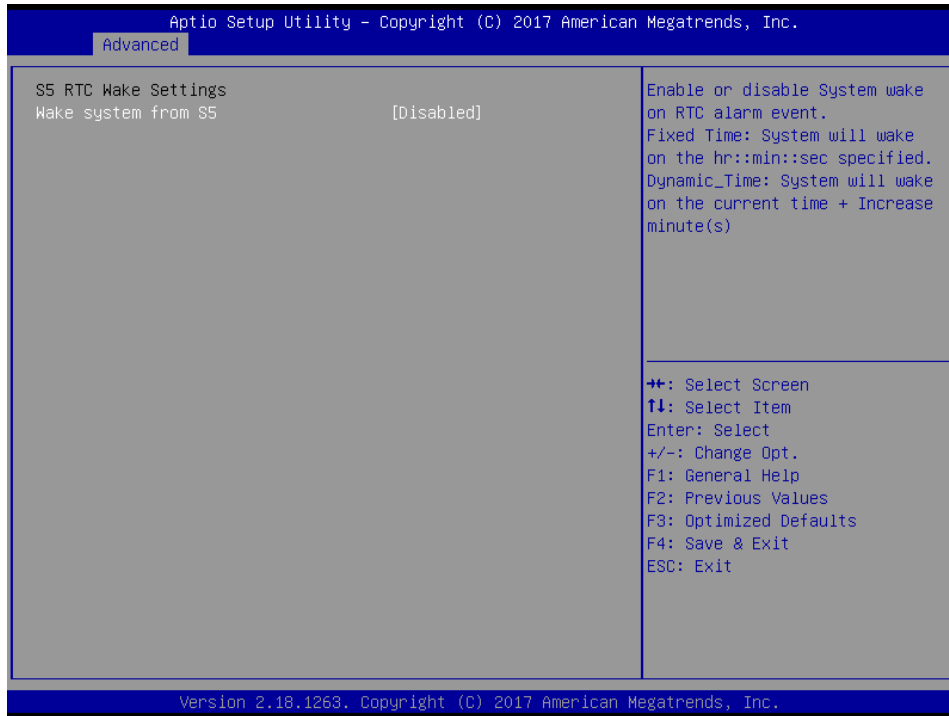


Item	Options	Description
<b>Enable Hibernation</b>	Disabled Enabled[Default],	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some operating systems.
<b>ACPI Sleep State</b>	Suspend Disabled, S3 (Suspend to RAM)[Default]	Select the highest ACPI sleep state the system will enter when the SUSPEDN button is pressed.
<b>ErP Function</b>	Disabled[Default], Enabled	ErP (Deep S5) Function. Allow BIOS switching off peripheral power delivery at S5 state.
<b>Pwr-On After PWR-Fail</b>	Always Off[Default] Always On Keep Last state	Specify what state to go to when power is re-applied after a power failure (G3 state).
<b>Watch Dog</b>	Disabled[Default], 30 sec 40 sec 50 sec 1 min 2 min 10 min 30 min	Select Watch Dog Timer (WDT) Mode.

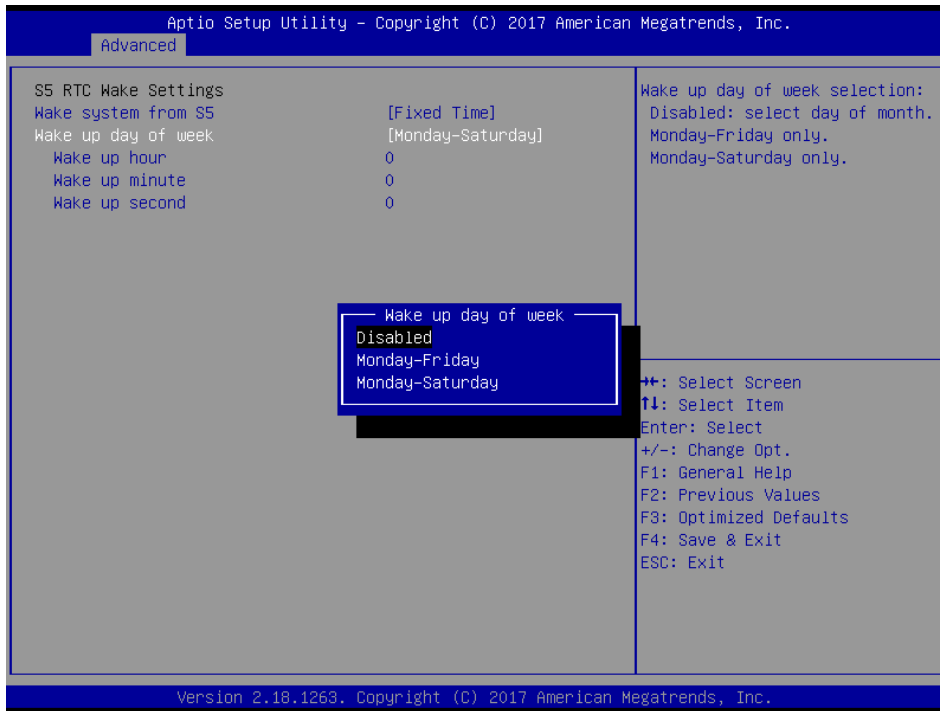
## BC170Q User's Manual

<b>Wake Up by Ring</b>	Disabled Enabled[ <b>Default</b> ],	Enable/Disable system waked up by Ring signal from S3(Sleep). S4(Hibernate) and S5(Soft Off) States.
<b>USB Standby Power Delivery</b>	Disabled Enabled[ <b>Default</b> ],	Enable/Disable USB Power delivery in S3 (Sleep), S4 (Hibernate) and S5 (Soft Off) States.

### 3.6.2.5 S5 RTC Wake Settings

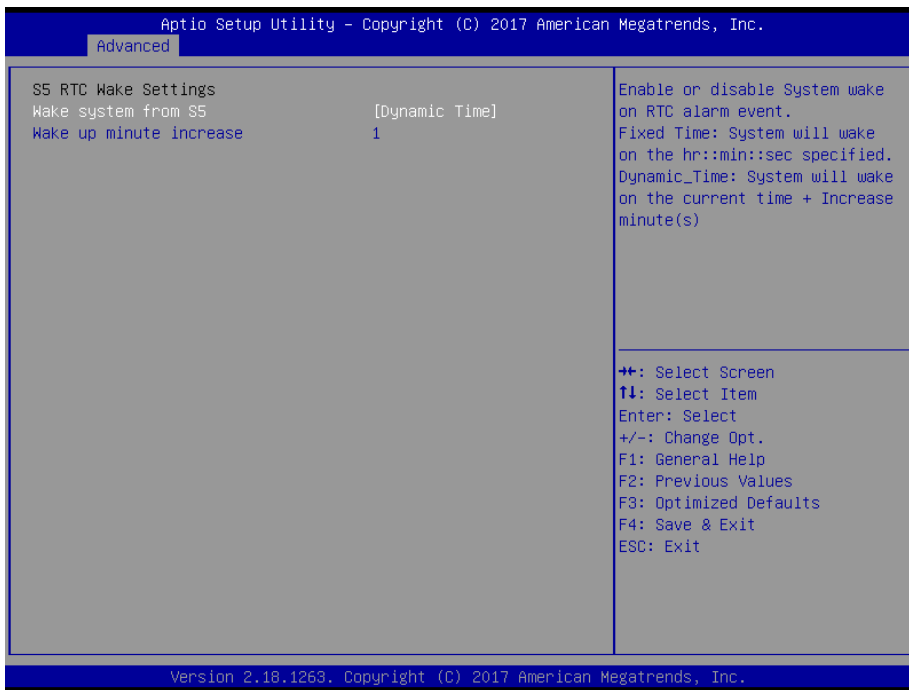


Item	Options	Description
<b>Wake system from S5</b>	Disabled[ <b>Default</b> ], Fixed Time Dynamic Time	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Time, System will wake on the current time + Increase minute(s).



Item	Options	Description
<b>Wake system from S5</b>	Disabled, Fixed Time <b>[Default]</b> Dynamic Time	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Time, System will wake on the current time + Increase minute(s).
<b>Wake up day of week</b>	Disabled <b>[Default]</b> , Monday-Friday Monday-Saturday	Wake up day of week selection: Disabled: select day of month. Monday-Friday only. Monday-Saturday only.
<b>Wake up hour</b>	0-23	Select 0-23 For example enter 3 for 3am and 15 for 3pm.
<b>Wake up minute</b>	0-59	0-59.
<b>Wake up second</b>	0-59	0-59.

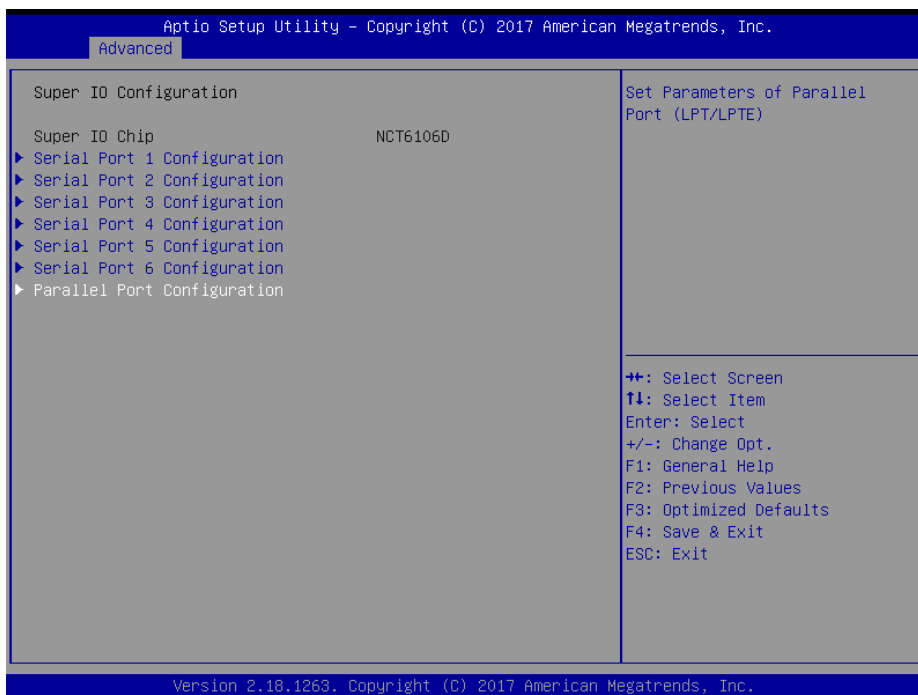
# BC170Q User's Manual



Item	Options	Description
Wake system from S5	Disabled, Fixed Time Dynamic Time[Default]	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Time, System will wake on the current time + Increase minute(s).
Wake up minute increase	1-5	1-5.

## 3.6.2.6 Super IO Configuration

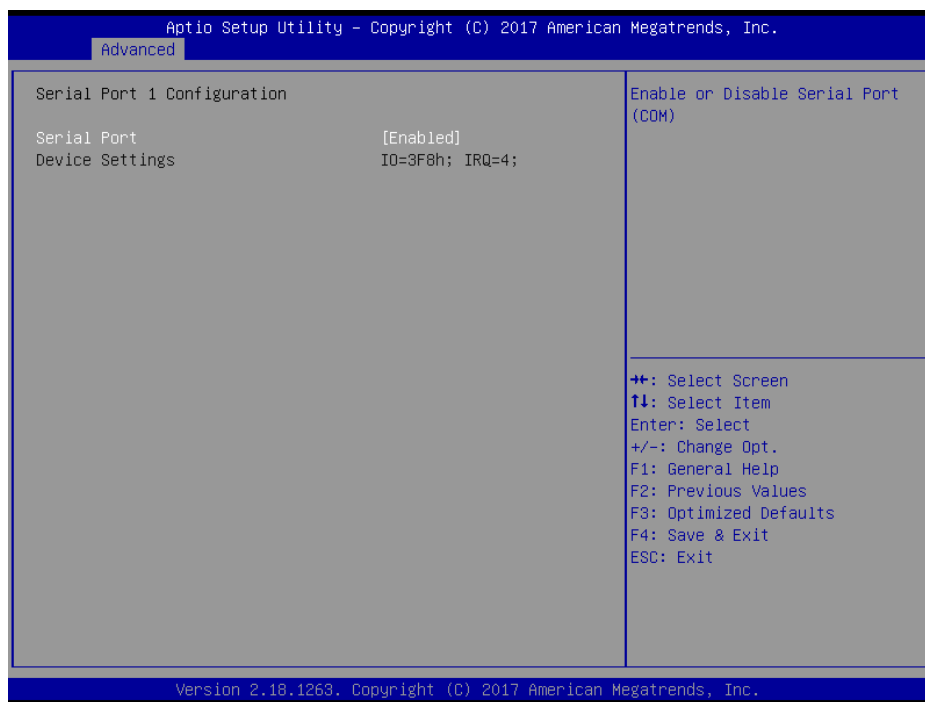
You can use this item to set up or change the Super IO configuration for serial ports. Please refer to 3.6.2.6.1~ 3.6.2.6.7 for more information.





Item	Description
<b>Serial Port 1 Configuration</b>	Set Parameters of Serial Port 1 (COMA).
<b>Serial Port 2 Configuration</b>	Set Parameters of Serial Port 2 (COMB).
<b>Serial Port 3 Configuration</b>	Set Parameters of Serial Port 3 (COMC).
<b>Serial Port 4 Configuration</b>	Set Parameters of Serial Port 4 (COMD).
<b>Serial Port 5 Configuration</b>	Set Parameters of Serial Port 5 (COME).
<b>Serial Port 6 Configuration</b>	Set Parameters of Serial Port 6 (COMF).
<b>Parallel Port Configuration</b>	Set Parameters of Parallel Port (LPT/LPTE).

### 3.6.2.6.1 Serial Port 1 Configuration



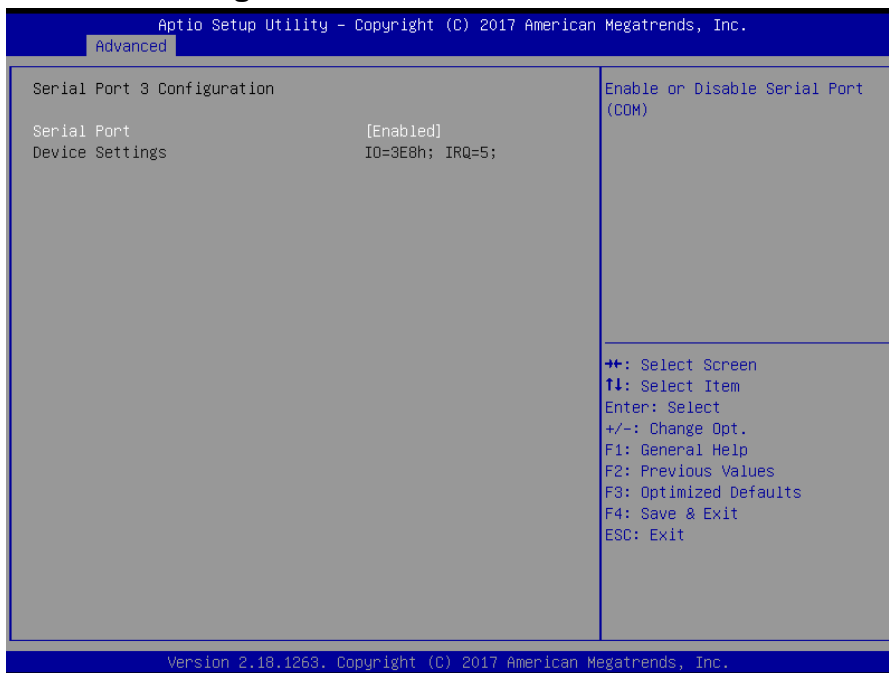
Item	Option	Description
<b>Serial Port</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable Serial Port (COM).

3.6.2.6.2 Serial Port 2 Configuration



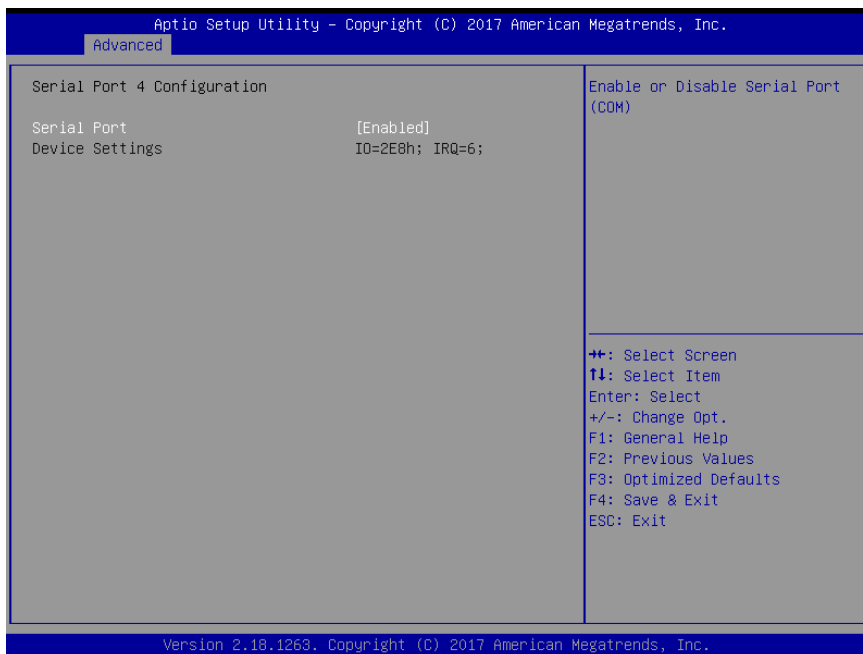
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
UART 232 422 485	RS232[Default] RS422 RS485	Set COM Port as RS232, RS422 or RS485 mode.

3.6.2.6.3 Serial Port 3 Configuration



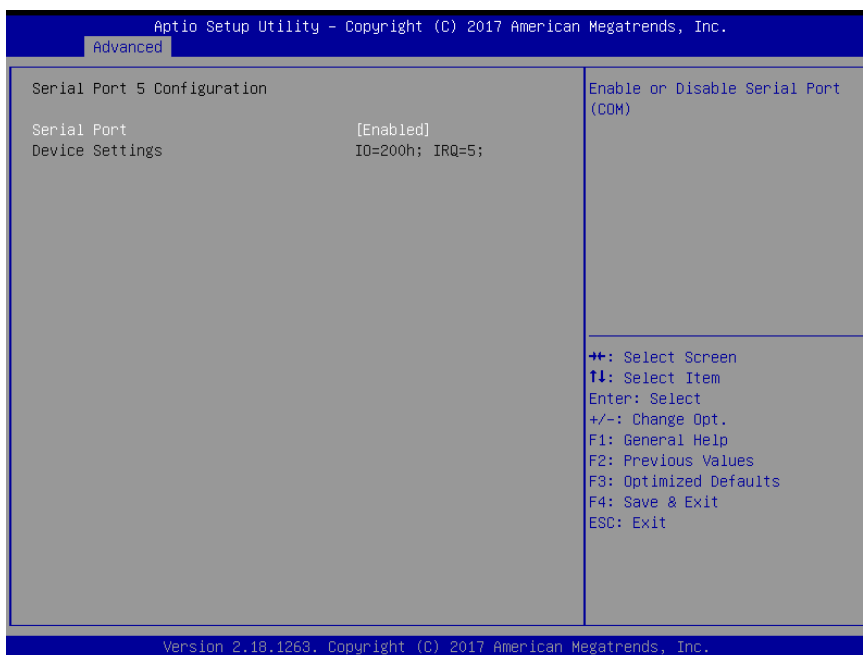
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

### 3.6.2.6.4 Serial Port 4 Configuration



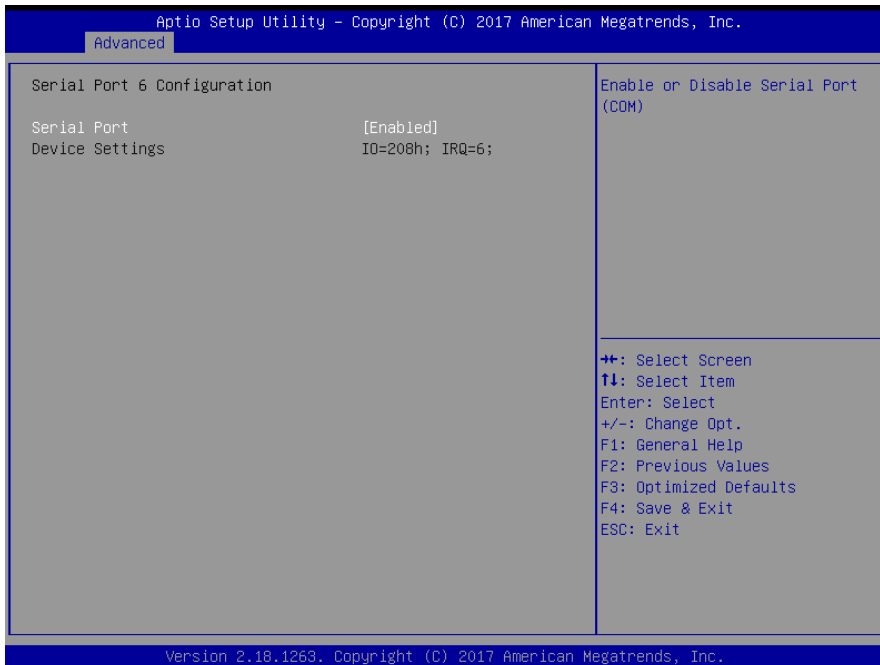
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

### 3.6.2.6.5 Serial Port 5 Configuration



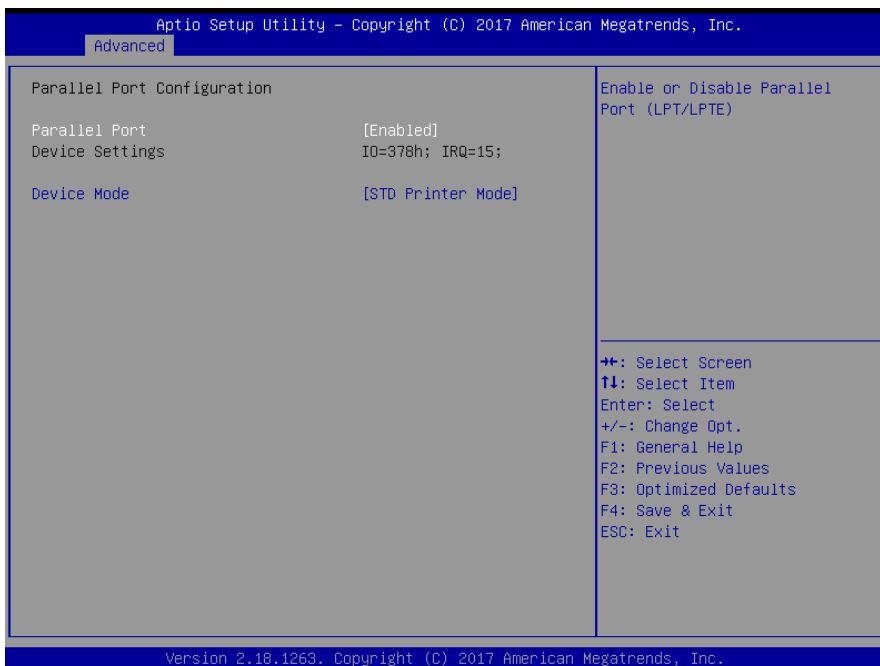
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

### 3.6.2.6.6 Serial Port 6 Configuration



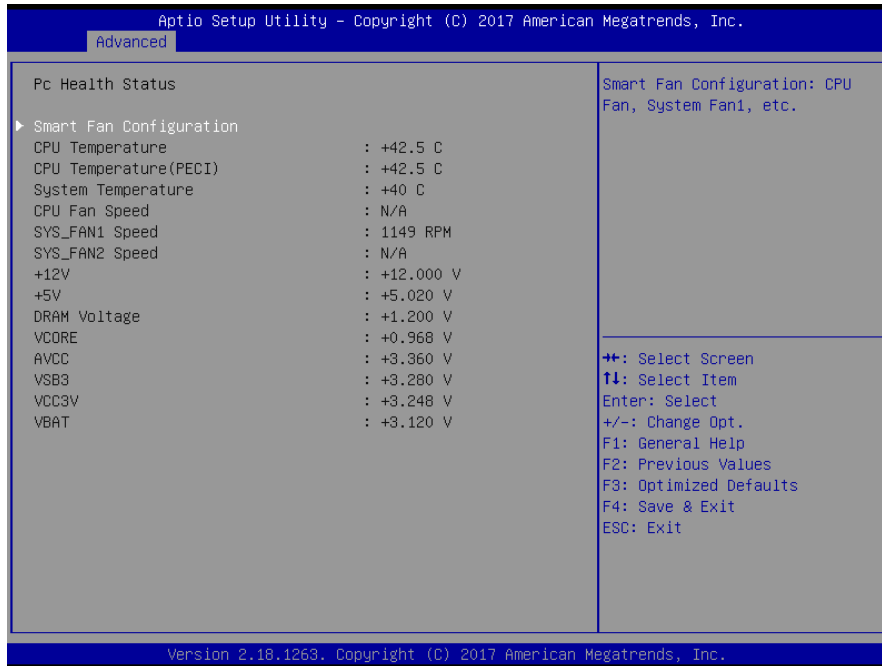
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

### 3.6.2.6.7 Parallel Port Configuration

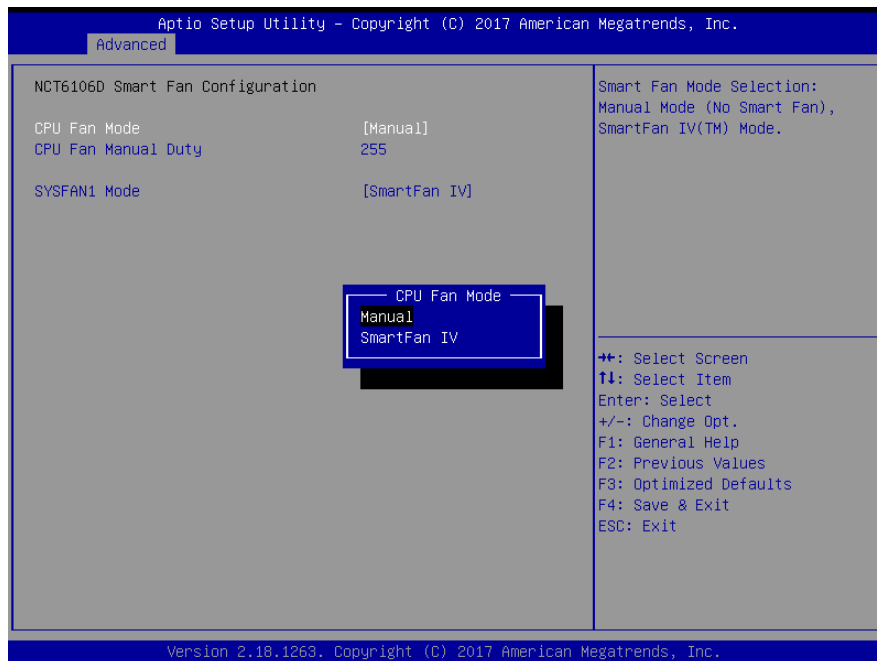


Item	Option	Description
Parallel Port	Enabled[Default], Disabled	Enable or Disable Parallel Port (LPT/LPTE).
Device Mode	STD Printer Mode[Default] SPP Mode EPP-1.9 and SPP Mode EPP-1.7 and SPP Mode	Change the Printer Port mode.

### 3.6.2.7 NCT6106D H/W Monitor



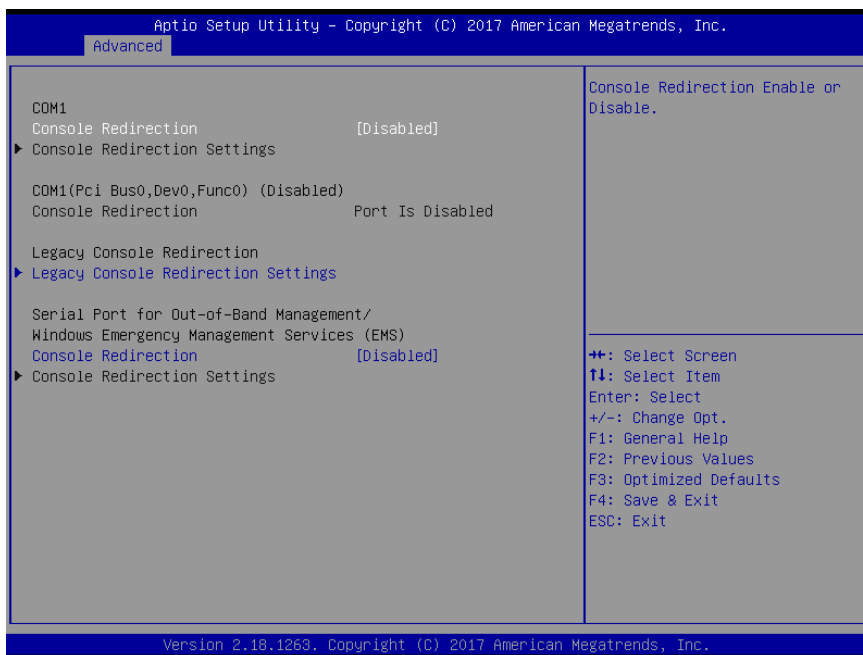
#### 3.6.2.7.1 Smart Fan Configuration



## BC170Q User's Manual

Item	Option	Description
CPU Fan Mode	Manual <b>[Default]</b> , SmartFan IV	Smart Fan Mode Selection: Manual Mode (No Smart Fan), SmartFan IV™ Mode.
CPU Fan Manual Duty	0-255	CPU Fan manual output duty: 0 to 255.
SYSFAN1 Mode	Manual SmartFan IV <b>[Default]</b> ,	Smart Fan Mode Selection: Manual Mode (No Smart Fan), SmartFan IV™ Mode.

### 3.6.2.8 Serial Port Console Redirection



Item	Options	Description
Console Redirection	Disabled <b>[Default]</b> , Enabled	Console Redirection Enable or Disable.

3.6.2.8.1 COM1



Item	Option	Description
<b>Terminal Type</b>	VT100 VT100+[Default] VT-UTF8 ANSI	Emulation: ANSI: Extended ASCII char set. VT100: ASCII char set. VT100+: Extends VT100 to support color, function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.
<b>Bits per second</b>	9600 19200 38400 57600 115200[Default]	Select serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
<b>Data Bits</b>	7 8[Default]	Data Bits.
<b>Parity</b>	None[Default] Even Odd Mark Space	A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: Parity bit is always. 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.
<b>Stop Bits</b>	1[Default] 2	Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.
<b>Flow Control</b>	None[Default] Hardware RTS/CTS	Flow control can prevent data loss from buffer overflow. When sending data, if the

## BC170Q User's Manual

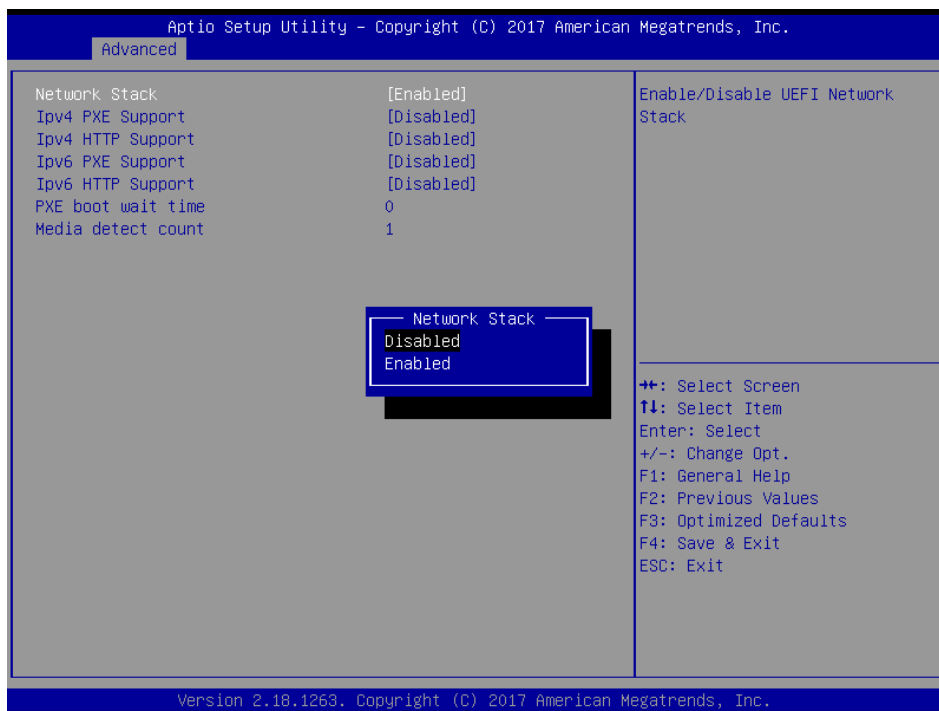
		receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
<b>VT-UTF8 Combo Key Support</b>	Disabled Enabled[ <b>Default</b> ]	Enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals.
<b>Recorder Mode</b>	Disabled[ <b>Default</b> ] Enabled	With this mode enabled only text will be sent. This is to capture Terminal data.
<b>Resolution 100x31</b>	Disabled Enabled[ <b>Default</b> ]	Enables or disables extended terminal resolution.
<b>Legacy OS Redirection Resolution</b>	80x24[ <b>Default</b> ] 80x25	On Legacy OS, the Number of Rows and Columns supported redirection.
<b>Putty KeyPad</b>	VT100[ <b>Default</b> ] LINUX XTERMR6 SCO ESCN VT400	Select FunctionKey and KeyPad on Putty.
<b>Redirection After BIOS POST</b>	Always Enable[ <b>Default</b> ] BootLoader	The Settings specify if BootLoader is selected then Legacy console redirection is disabled before booting to Legacy OS. Default value is Always Enabled which means Legacy console Redirection is enabled.

### 3.6.2.9 Intel TXT Configuration



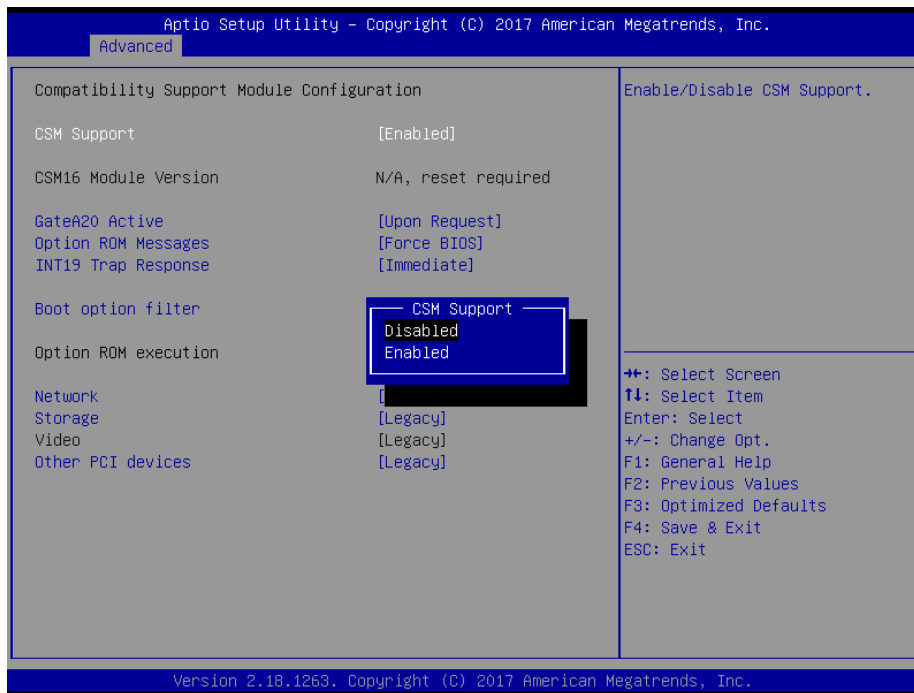


### 3.6.2.10 Network Stack Configuration



Item	Options	Description
<b>Network Stack</b>	Enabled[Default] Disabled	Enable/Disable UEFI Network Stack.
<b>Ipv4 PXE Support</b>	Enabled Disabled[Default]	Enable Ipv4 PXE Boot Support. If disabled IPV4 PXE boot option will not be created.
<b>Ipv4 HTTP Support</b>	Enabled Disabled[Default]	Enable Ipv4 HTTP Boot Support. If disabled IPV4 HTTP boot option will not be created.
<b>Ipv6 PXE Support</b>	Enabled Disabled[Default]	Enable Ipv6 PXE Boot Support. If disabled IPV4 PXE boot option will not be created.
<b>Ipv6 HTTP Support</b>	Enabled Disabled[Default]	Enable Ipv6 HTTP Boot Support. If disabled IPV4 HTTP boot option will not be created.
<b>PXE boot wait time</b>	0	Wait time to press ESC key to abort the PXE boot.
<b>Media detect count</b>	1	Number of times presence of media will be checked.

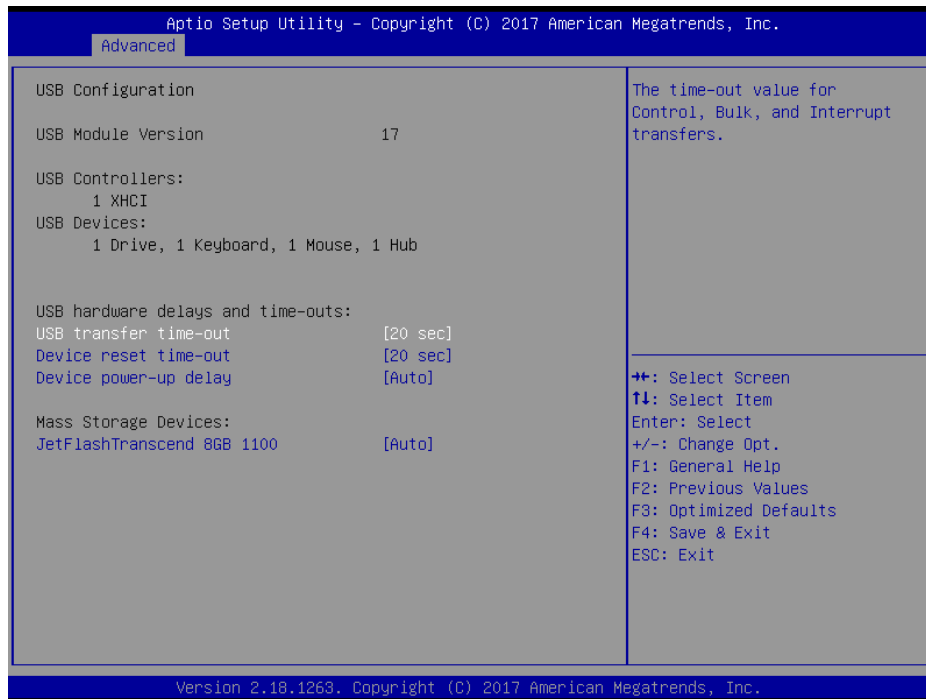
3.6.2.11 CSM Configuration



Item	Options	Description
<b>CSM Support</b>	Enabled[Default] Disabled	Enable/Disable CSM Support.
<b>GateA20 Active</b>	Upon Request[Default] Always	UPON REQUEST – GA20 can be disabled using BIOS services. ALWAYS – do not allow disabling GA20; this option is useful when any RT code is executed above 1MB.
<b>Option ROM Messages</b>	Force BIOS[Default] Keep Current	Set display mode for Option ROM.
<b>INT19 Trap Response</b>	Immediate[Default] Postponed	BIOS reaction on INT19 trapping by Option ROM: IMMEDIATE – execute the trap right away; POSTPONED – execute the trap during legacy boot.
<b>Boot option filter</b>	Legacy only[Default] UEFI only	This option controls Legacy/UEFI ROMs priority.
<b>Network</b>	Do not launch[Default] UEFI Legacy	Controls the execution of UEFI and Legacy PXE OpROM.
<b>Storage</b>	Do not launch UEFI Legacy[Default]	Controls the execution of UEFI and Legacy Storage OpROM.
<b>Other PCI devices</b>	Do not launch UEFI Legacy[Default]	Determines OpROM execution policy for devices other than Network, Storage, or Video.

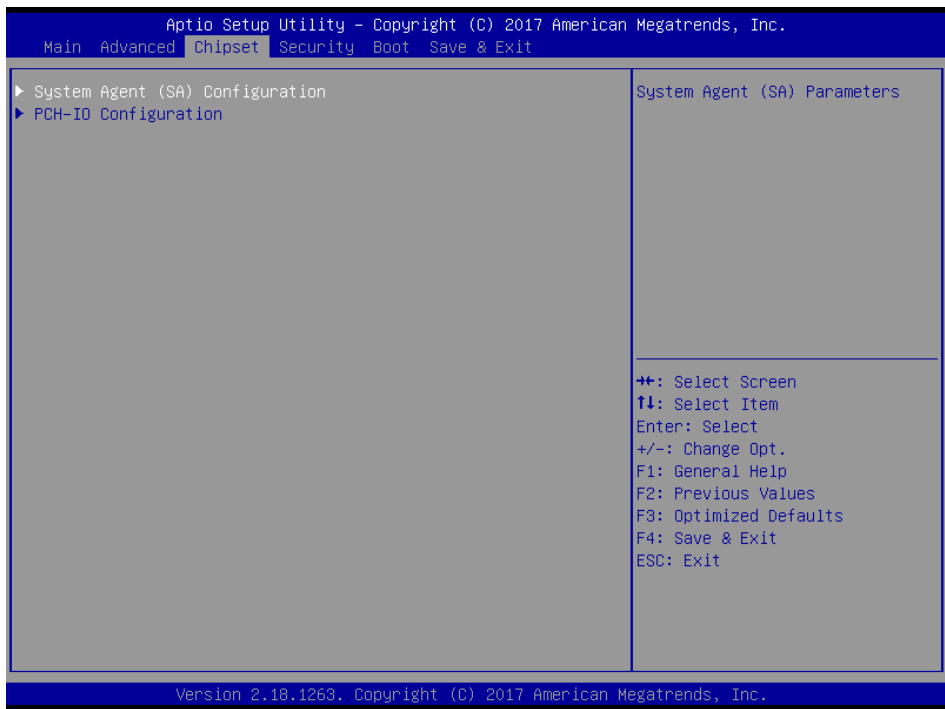
### 3.6.2.12 USB Configuration

The USB Configuration menu helps read USB information and configures USB settings.

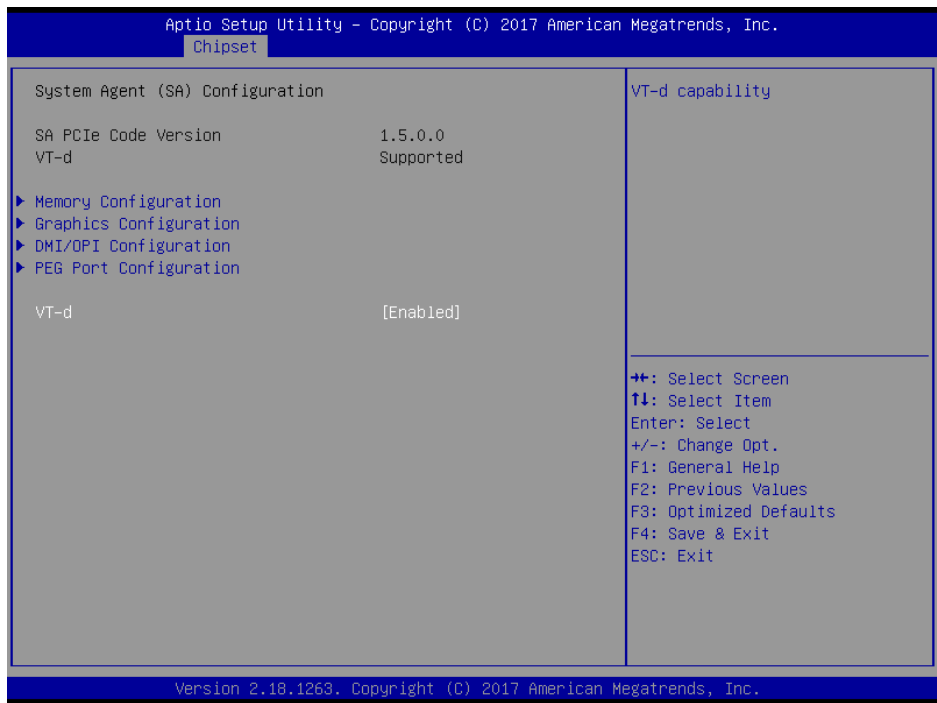


Item	Options	Description
<b>USB transfer time-out</b>	1 sec 5 sec 10 sec 20 sec <b>[Default]</b>	The time-out value for Control, Bulk, and Interrupt transfers.
<b>Device reset time-out</b>	10 sec 20 sec <b>[Default]</b> 30 sec 40 sec	USB mass storage device Start Unit command time-out.
<b>Device power-up delay</b>	Auto <b>[Default]</b> Manual	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100ms, for a Hub port the delay is taken from Hub descriptor.
<b>Mass Storage Devices</b>	Auto <b>[Default]</b> Floppy Forced FDD Hard Disk CD-ROM	Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type.

### 3.6.3 Chipset

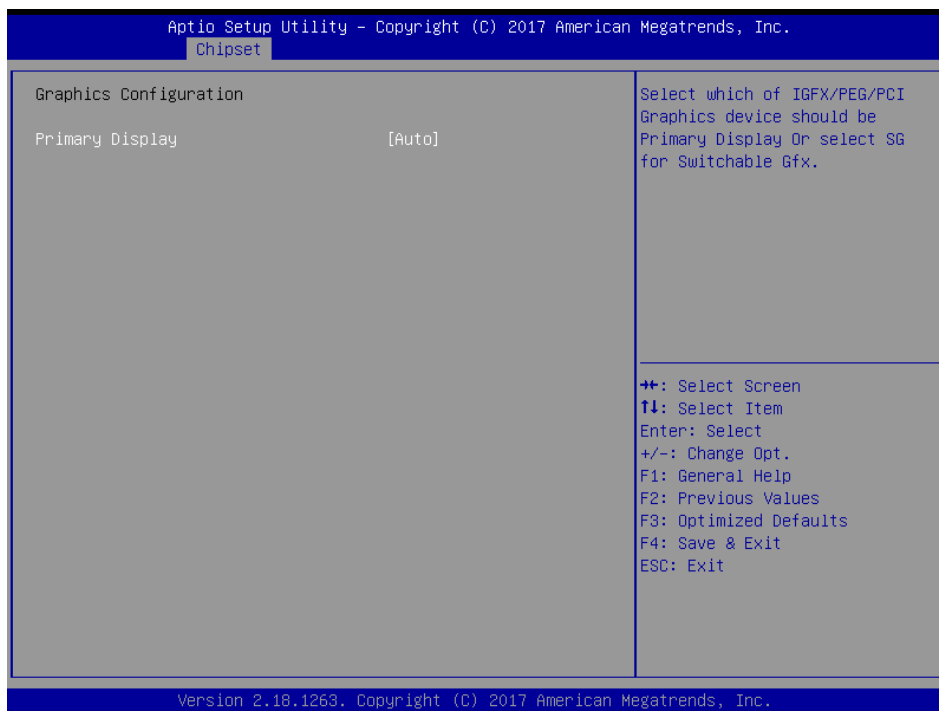


#### 3.6.3.1 System Agent (SA) Configuration



Item	Option	Description
VT-d	Enabled[Default] Disabled	VT-d capability.

### 3.6.3.1.1 Graphics Configuration

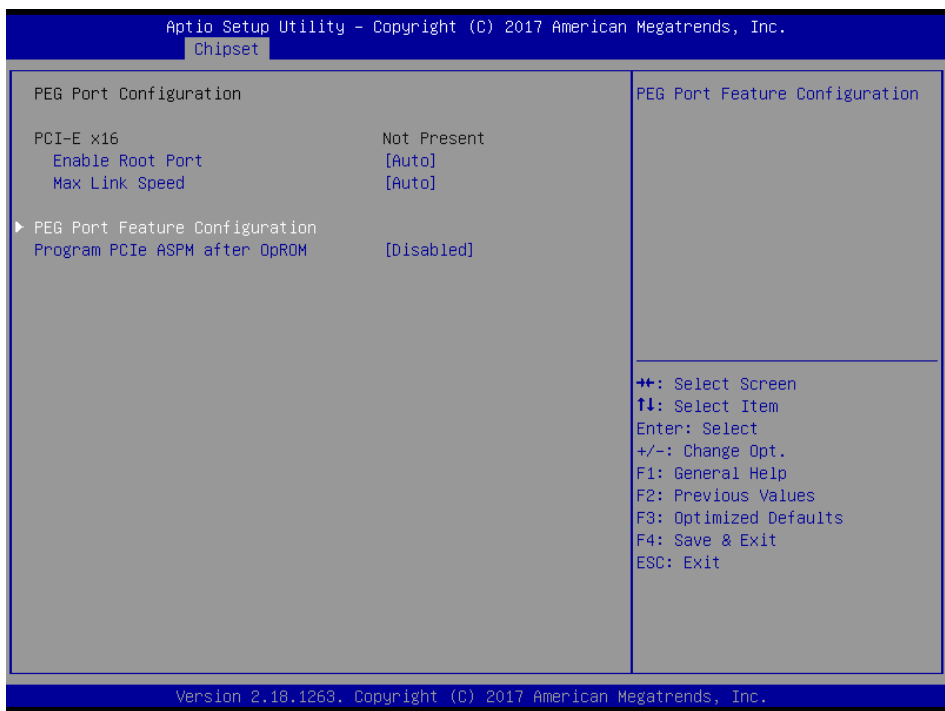


Item	Option	Description
Primary Display	Auto[Default]	Select which of IGFX/PEG/PCI Graphics device should be Primary Display Or select SG for Switchable Gfx.
	IGFX	
	PEG	
	PCIE	

### 3.6.3.1.2 DMI/OPI Configuration

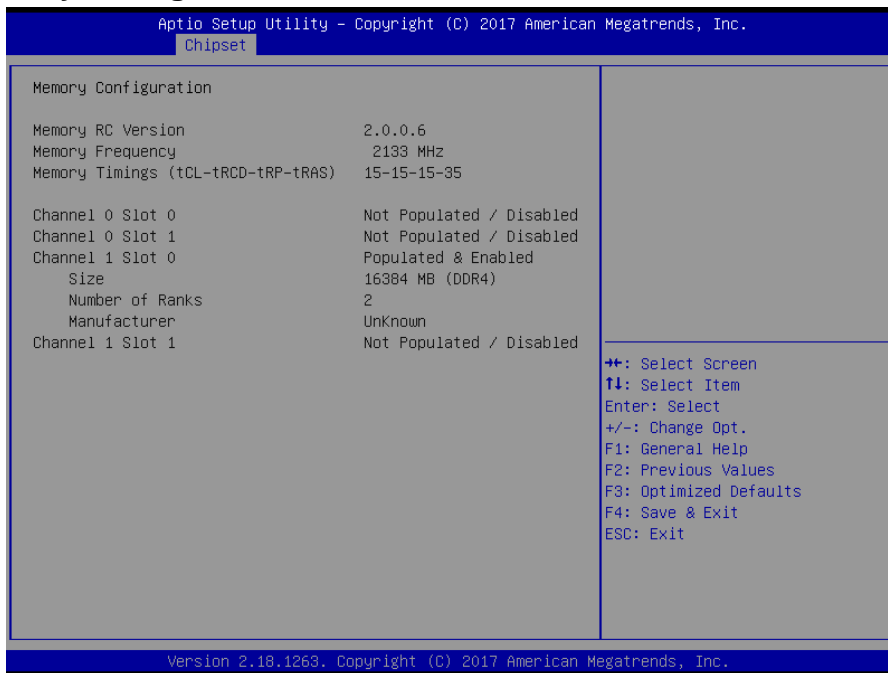


3.6.3.1.3 PEG Port Configuration

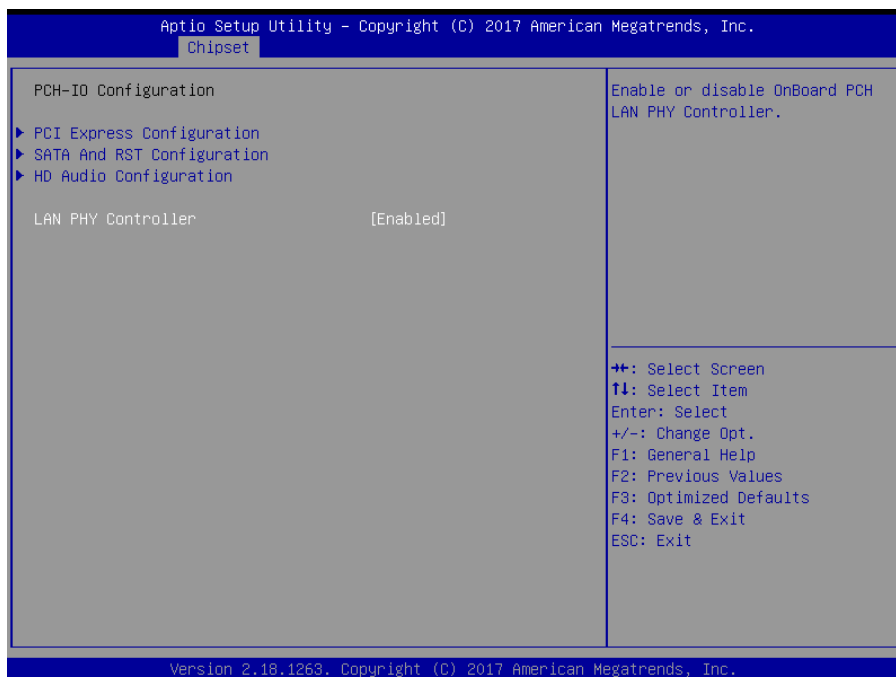


Item	Option	Description
<b>Enable Root Port</b>	Disabled Enabled Auto[Default]	Enable or Disable the Root Port.
<b>Max Link Speed</b>	Auto[Default] Gen1 Gen2 Gen3	Configure PEG 0:1:0 Max Speed.
<b>Program PCIe ASPM after OpROM</b>	Disabled[Default] Enabled	Enabled: PCIe ASPM will be programmed after OpROM. Disabled: PCIe ASPM will be programmed before OpROM.

### 3.6.3.1.4 Memory Configuration

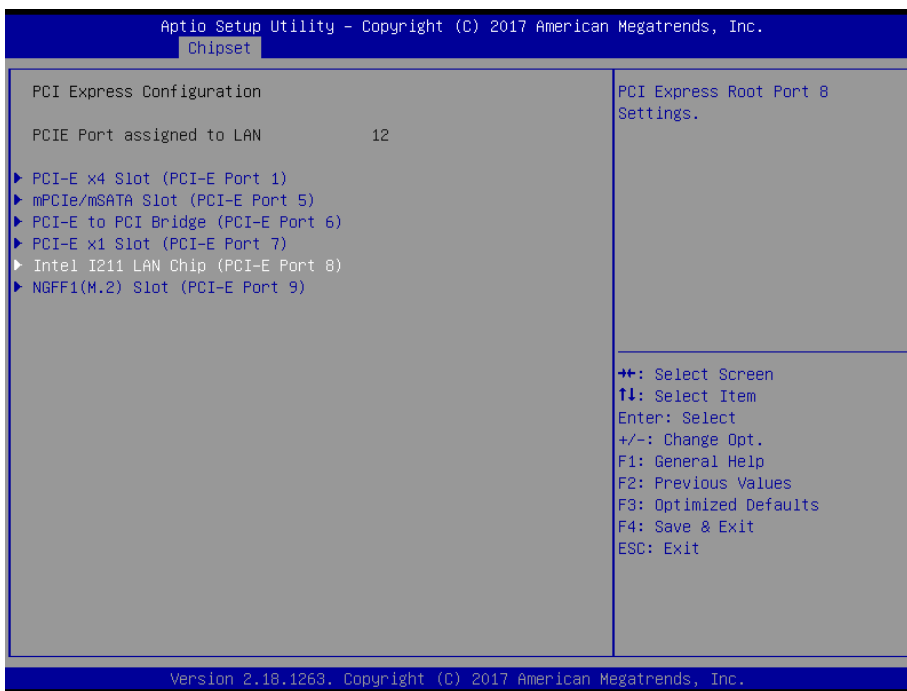


### 3.6.3.2 PCH-IO Configuration

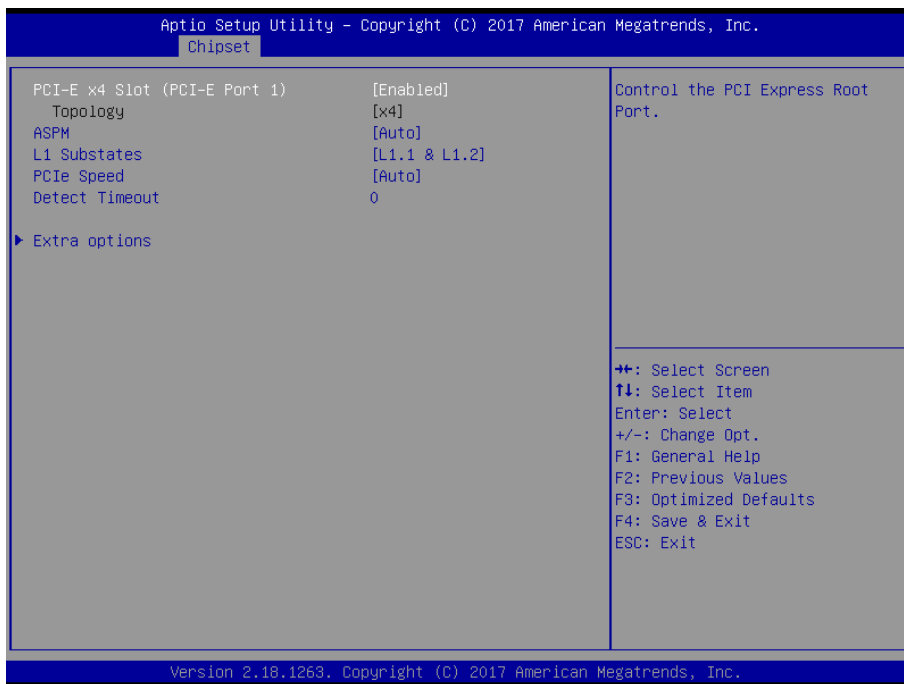


Item	Option	Description
LAN PHY Controller	Disabled Enabled[Default]	Enable or disable OnBoard PCH LAN PHY Controller.

3.6.3.2.1 PCI Express Configuration



3.6.3.2.1.1 PCI-E x4 Slot (PCI-E Port 1)

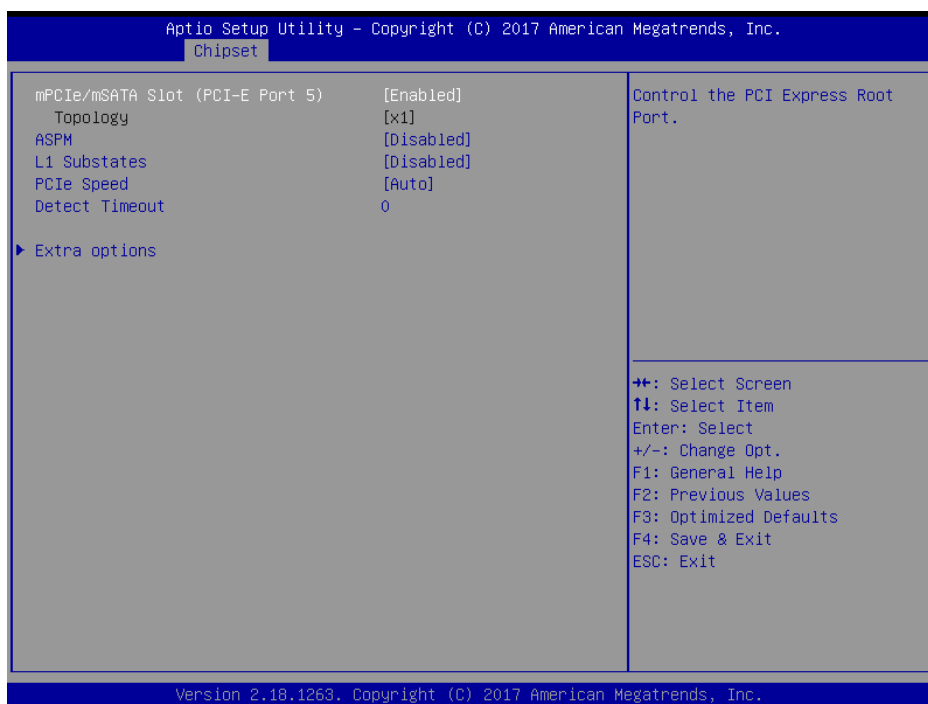


Item	Option	Description
PCIe x4 Slot (PCI-E Port 1)	Enabled[Default], Disabled	Control the PCI Express Root Port.
ASPM	Auto[Default] L0sL1	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto



	L1 L0s Disabled	configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled[Default] L1.1 L1.2 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe Speed.
<b>Detect Timeout</b>	0	The number of milliseconds reference code will wait for link to exit Detect state for enabled ports before assuming there is no device and potentially disabling the port.

### 3.6.3.2.1.2 mPCIe/mSATA Slot (PCI-E Port 5)

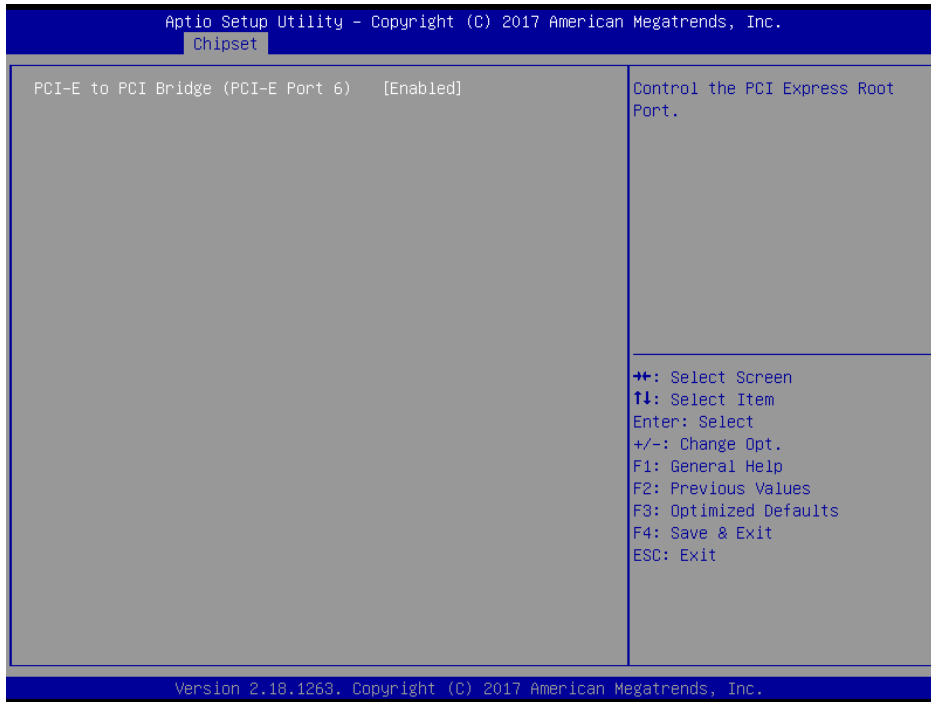


Item	Option	Description
<b>mPCIe/mSATA Slot (PCI-E Port 5)</b>	Enabled[Default], Disabled	Control the PCI Express Root Port.
<b>ASPM</b>	Auto L0sL1 L1 L0s Disabled[Default]	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled[Default] L1.1 L1.2 L1.1 & L1.2	PCI Express L1 Substates settings.

## BC170Q User's Manual

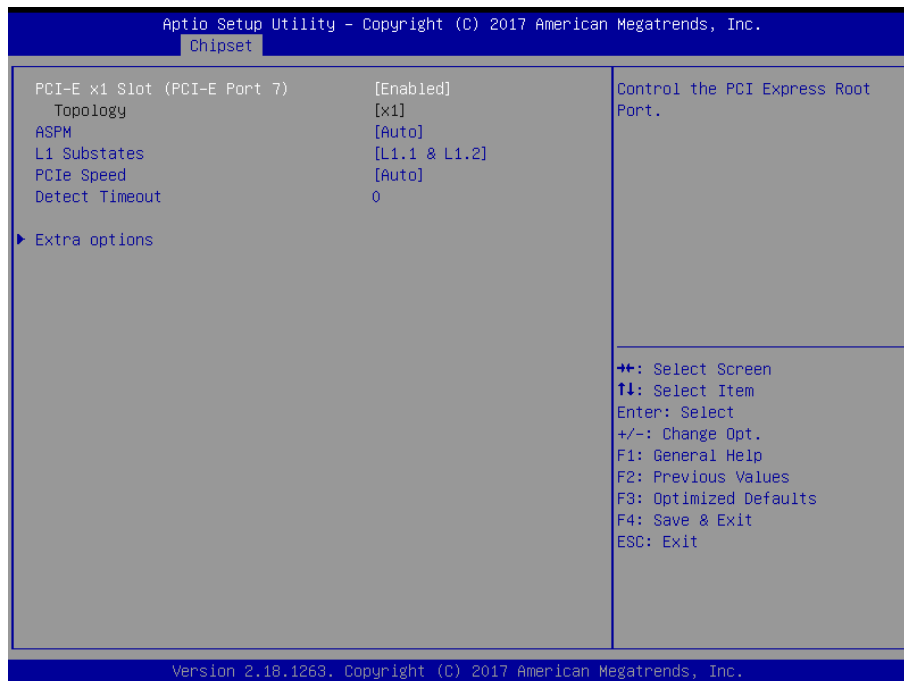
<b>PCIe Speed</b>	Auto[Default] Gen1 Gen2	Configure PCIe Speed.
<b>Detect Timeout</b>	0	The number of milliseconds reference code will wait for link to exit Detect state for enabled ports before assuming there is no device and potentially disabling the port.

### 3.6.3.2.1.3 PCI-E to PCI Bridge (PCI-E Port 6)



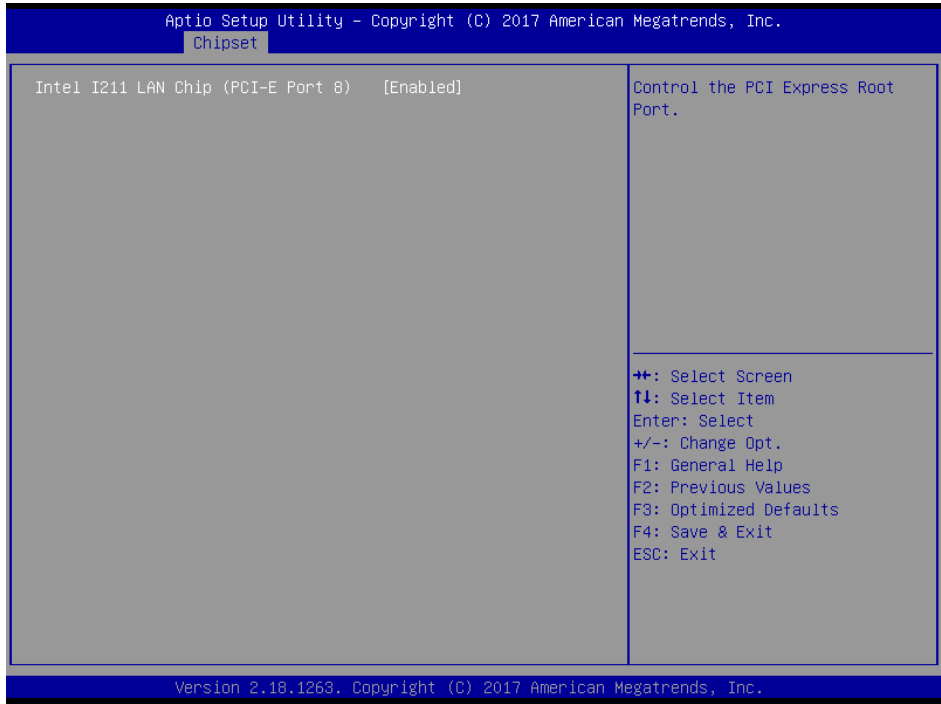
Item	Option	Description
<b>PCI-E to PCI Bridge (PCI-E Port 6)</b>	Enabled[Default], Disabled	Control the PCI Express Root Port.

### 3.6.3.2.1.4 PCI-E x1 Slot (PCI-E Port 7)



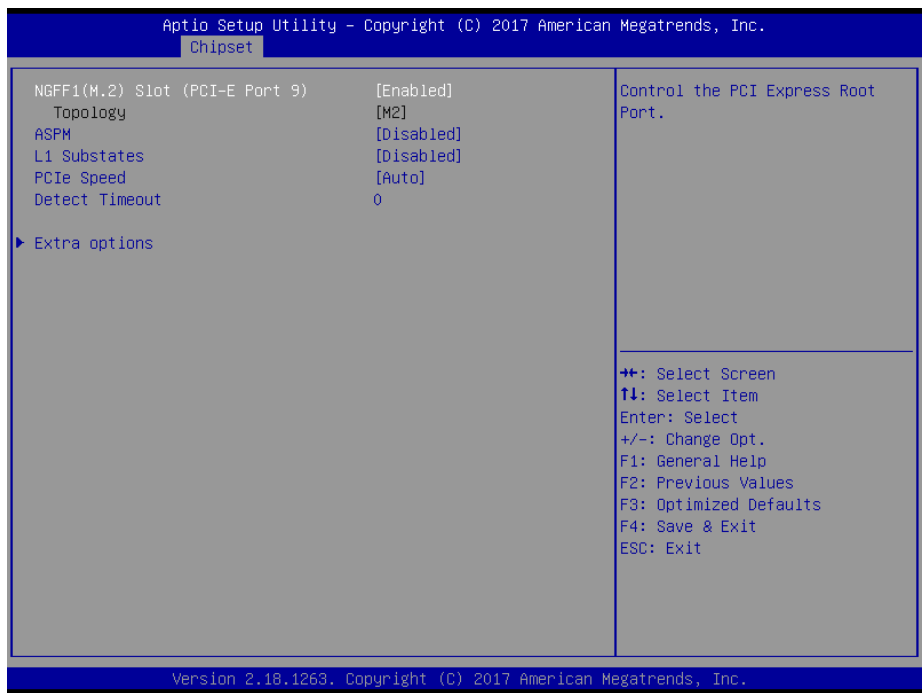
Item	Option	Description
<b>PCI-E x1 Slot (PCI-E Port 7)</b>	Enabled[Default], Disabled	Control the PCI Express Root Port.
<b>ASPM</b>	Auto[Default] L0sL1 L1 L0s Disabled	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled L1.1 L1.2 L1.1 & L1.2[Default]	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe Speed.
<b>Detect Timeout</b>	0	The number of milliseconds reference code will wait for link to exit Detect state for enabled ports before assuming there is no device and potentially disabling the port.

3.6.3.2.1.5 Intel I211 LAN Chip (PCI-E Port 8)



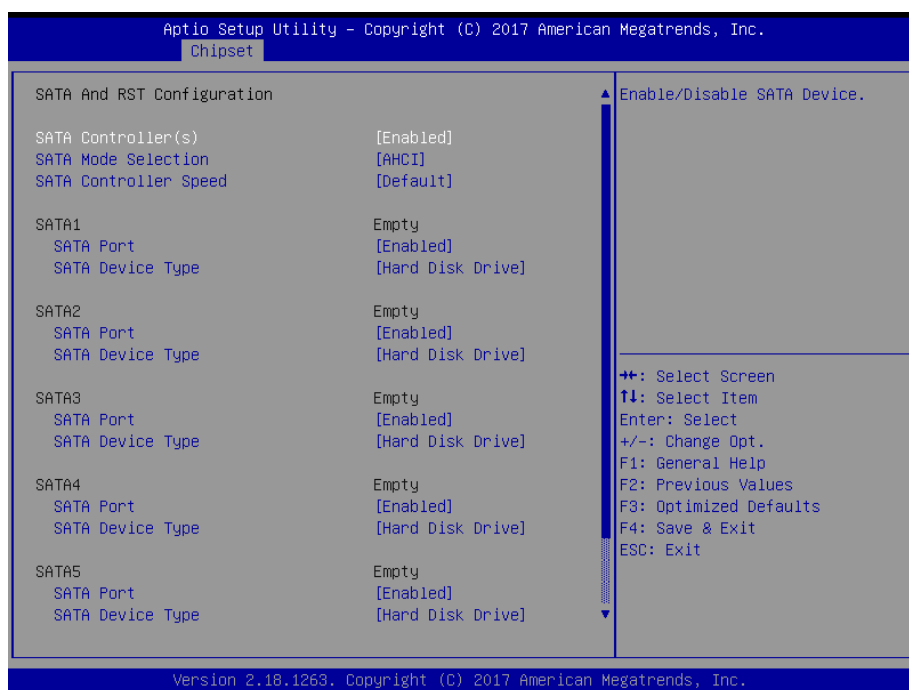
Item	Option	Description
Intel I211 LAN Chip (PCI-E Port 8)	Enabled[Default], Disabled	Control the PCI Express Root Port.

3.6.3.2.1.6 NGFF1(M.2) Slot (PCI-E Port 9)

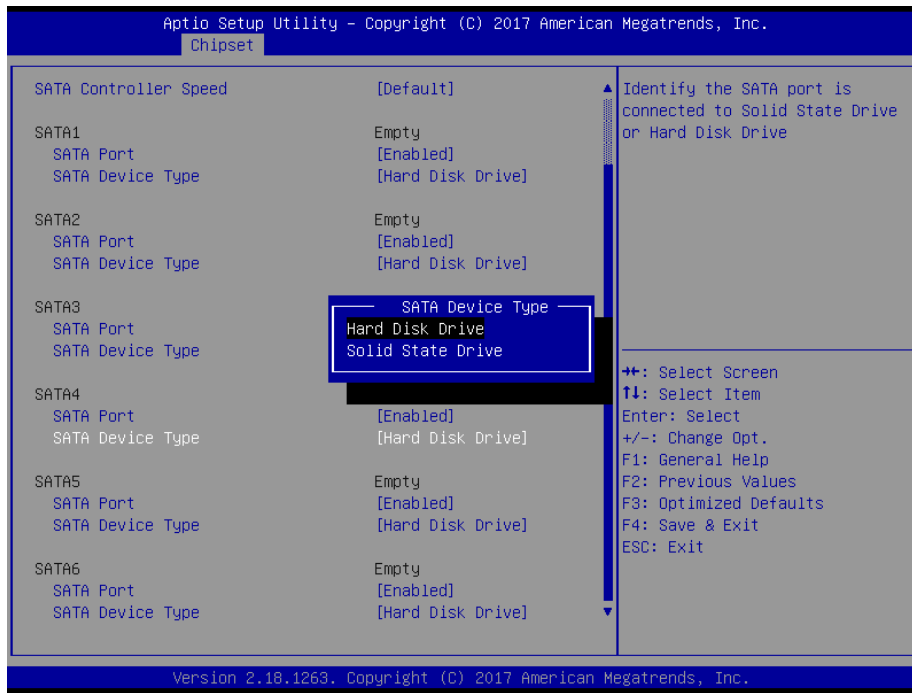


Item	Option	Description
<b>NGFF1(M.2) Slot (PCI-E Port 9)</b>	Enabled[Default], Disabled	Control the PCI Express Root Port.
<b>ASPM</b>	Auto L0sL1 L1 L0s Disabled[Default]	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled[Default] L1.1 L1.2 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto[Default] Gen1 Gen2	Configure PCIe Speed.
<b>Detect Timeout</b>	0	The number of milliseconds reference code will wait for link to exit Detect state for enabled ports before assuming there is no device and potentially disabling the port.

### 3.6.3.2.2 SATA And RST Configuration

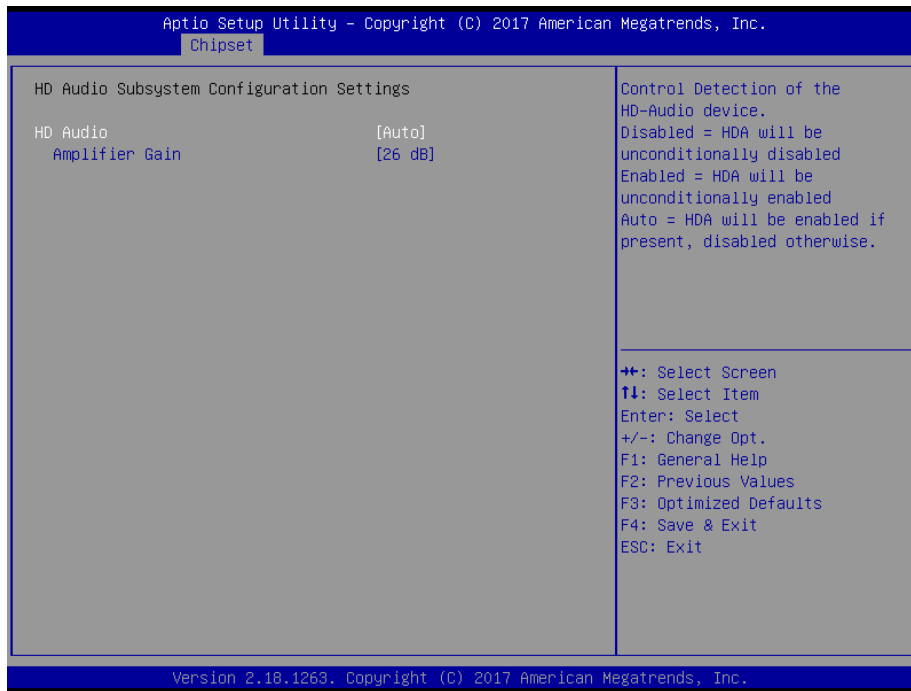


# BC170Q User's Manual



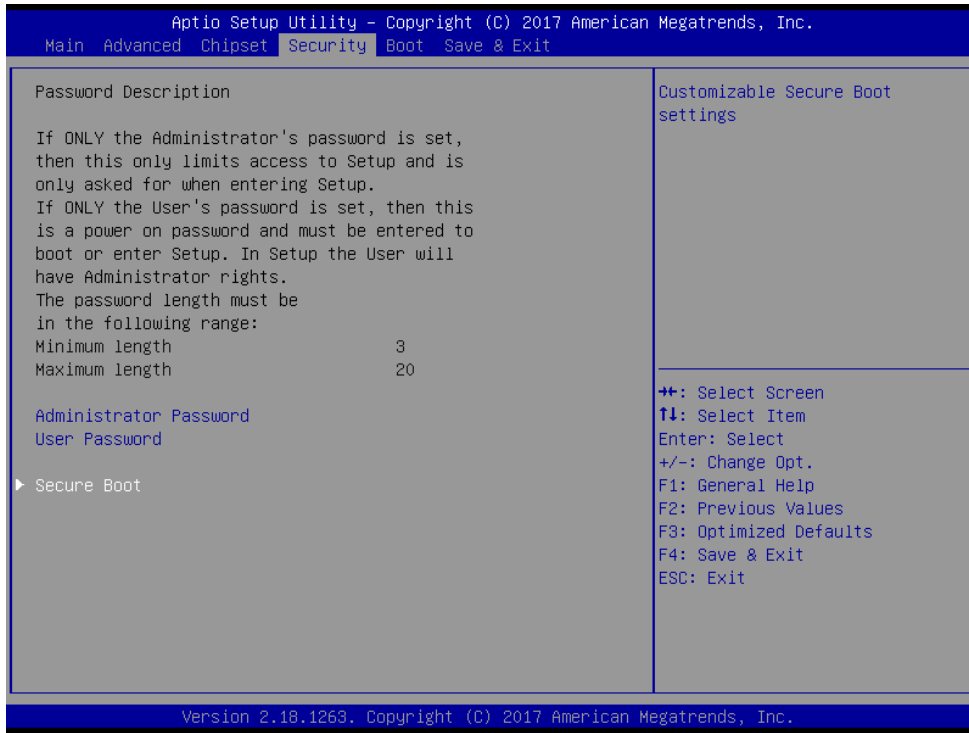
Item	Option	Description
<b>SATA Controller(s)</b>	Enabled[ <b>Default</b> ], Disabled	Enable/Disable SATA Device.
<b>SATA Mode Selection</b>	AHCI[ <b>Default</b> ], RAID	Determines how SATA controller(s) operate.
<b>SATA Controller Speed</b>	Default[ <b>Default</b> ], Gen1 Gen2 Gen3	Indicates the maximum speed the SATA controller can support.
<b>SATA Port</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable SATA Port.
<b>SATA Device Type</b>	Hard Disk Drive[ <b>Default</b> ] Solid State Drive	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive.

### 3.6.3.2.3 HD Audio Configuration



Item	Option	Description
<b>HD Audio</b>	Disabled Enabled, Auto[Default]	Control Detection of the HD-Audio device. Disabled = HAD will be unconditionally disabled Enabled = HAD will be unconditionally enabled Auto = HAD will be enabled if present, disabled otherwise.
<b>Amplifier Gain</b>	20 dB 26 dB[Default], 32 dB 36 dB	Select Amplifier Gain(dB).

## 3.6.4 Security



- **Administrator Password**

Set setup Administrator Password

- **User Password**

Set User Password

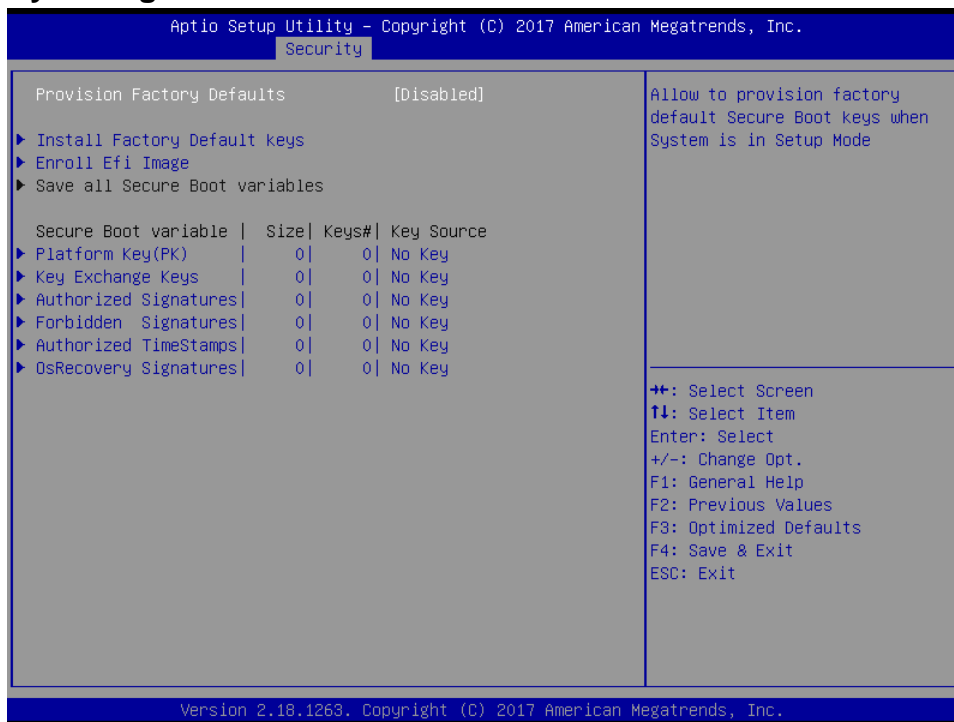


### 3.6.4.1 Secure Boot menu



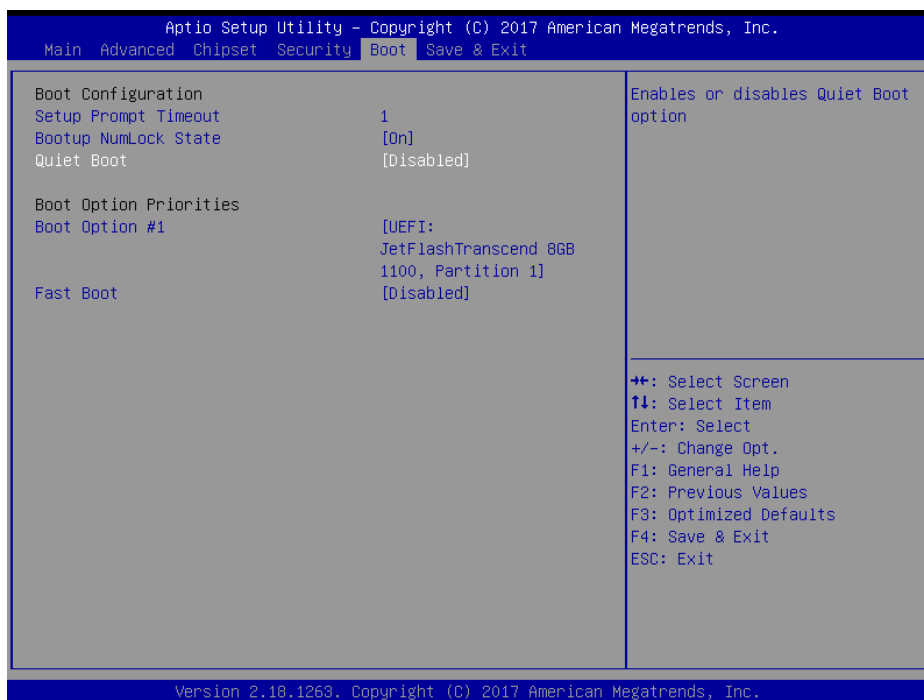
Item	Option	Description
<b>Attempt Secure Boot</b>	Disabled[Default] Enabled	Secure Boot activated when Platform Key(PK) is enrolled, System mode is User/Deployed, and CSM function is disabled.
<b>Secure Boot Mode</b>	Standard Custom[Default]	Secure Boot mode selector. 'Custom' Mode enables users to change Image Execution policy and manage Secure Boot Keys.

3.6.4.1.1 Key Management



Item	Option	Description
Provision Factory Defaults	Enabled, Disabled[Default]	Allow to provision factory default Secure Boot keys when System is in Setup Mode.

3.6.5 Boot



Item	Option	Description
Setup Prompt Timeout	1~ 65535	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Bootup NumLock State	On[Default] Off	Select the keyboard NumLock state
Quiet Boot	Disabled[Default] Enabled	Enables or disables Quiet Boot option
Fast Boot	Disabled[Default] Enabled	Enables or disables boot with initialization of a minimal set of devices required to launch active boot option. Has no effect for BBS boot options.
Boot Option #1	Set the system boot order.	

### 3.6.6 Save and exit



#### 3.6.6.1 Save Changes and Reset

Reset the system after saving the changes.

#### 3.6.6.2 Discard Changes and Reset

Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and reboots the controller.

## **BC170Q User's Manual**

### **3.6.6.3 *Restore Defaults***

This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting.

### **3.6.6.4 *Launch EFI Shell from filesystem device***

Attempts to Launch EFI Shell application (Shellx64.efi) from one of the available filesystem devices.

# 4. Drivers Installation

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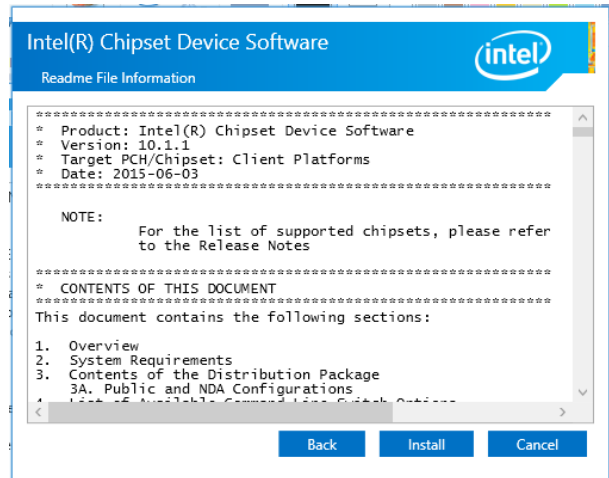
**Note:** Installation procedures and screen shots in this section are for your reference and may not be exactly the same as shown on your screen.

## 4.1 Install Chipset Driver

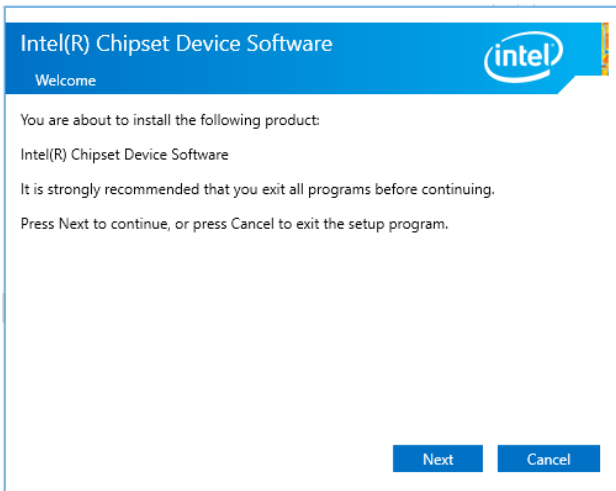
Insert the supporting DVD-ROM to DVD-ROM drive, and it should show the index page of the products automatically. If not, locate Index.htm and choose the product from the menu left.



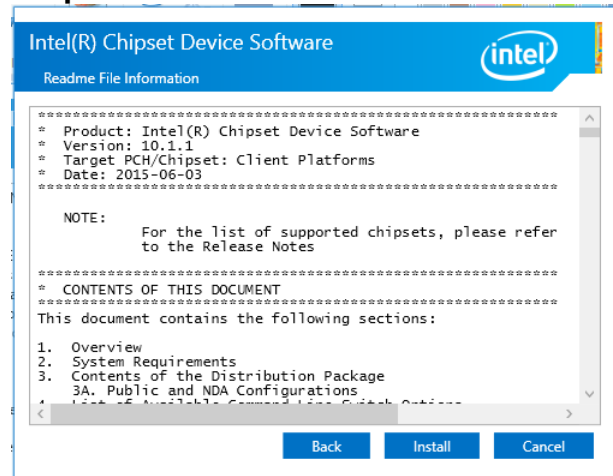
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system. If the warning message appears while the installation process, click Continue to go on.



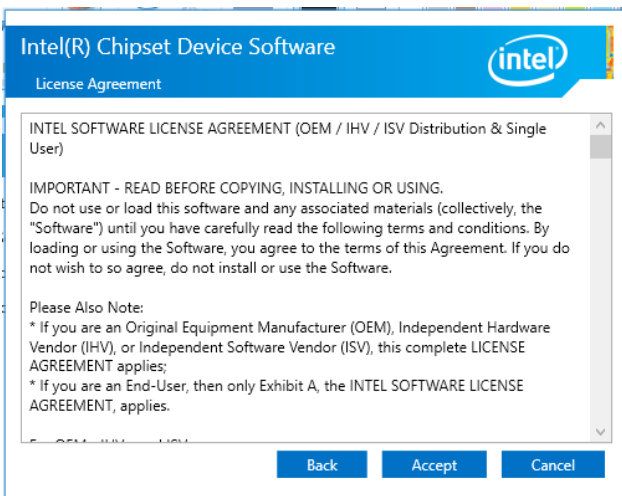
**Step 3. Click Install.**



**Step1. Click Next.**



**Step 4. Complete setup.**



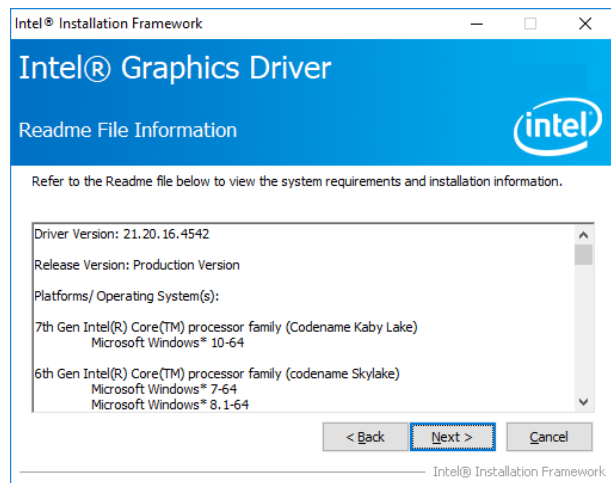
**Step 2. Click Accept.**

## 4.2 Install VGA Driver

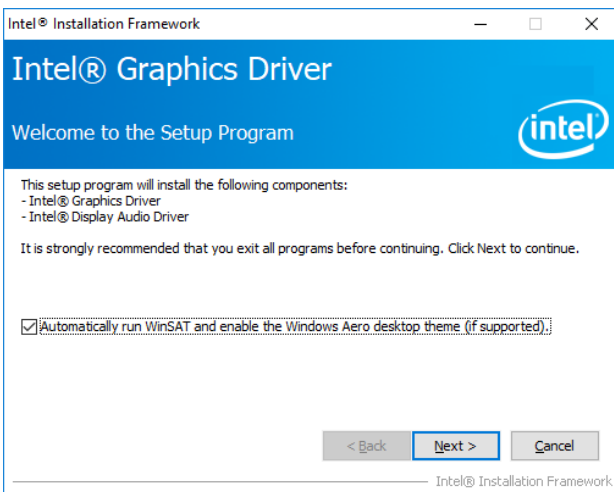
Insert the Supporting DVD-ROM to DVD-ROM drive, and it should show the index page of the products automatically. If not, locate Index.htm and choose the product from the menu left.



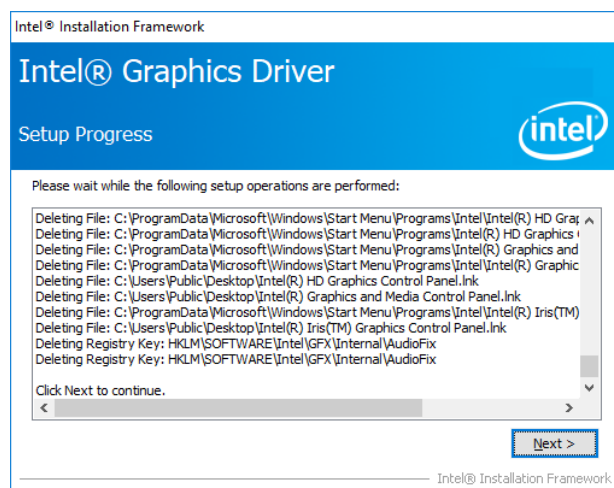
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



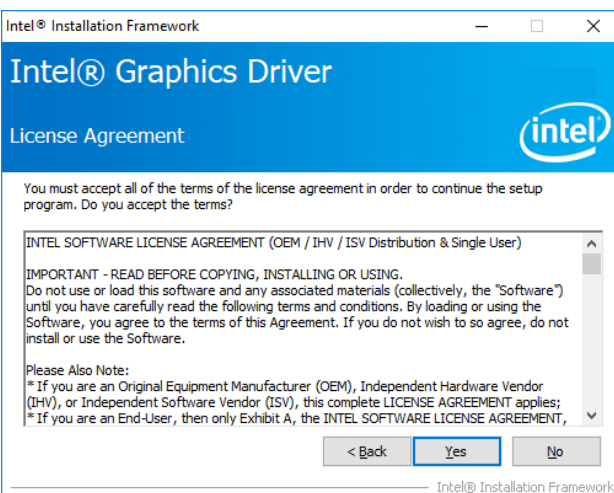
**Step 3. Click Next.**



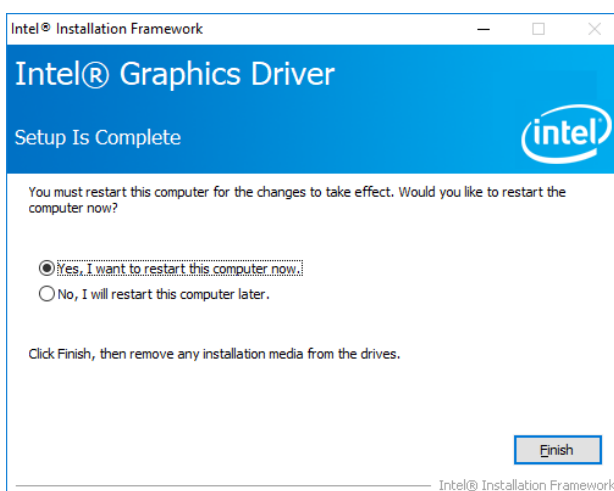
**Step 1. Click Next** to continue installation.



**Step 4. Click Next.**



**Step 2.**  
Click **Yes** to accept license agreement.



**Step 5. Click Finish** to complete setup.

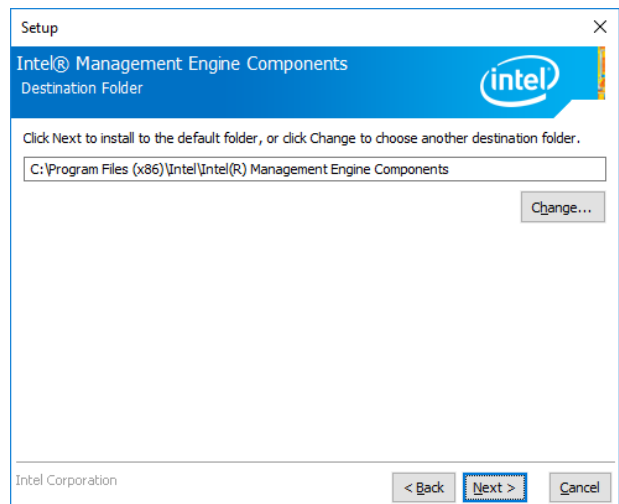
# BC170Q User's Manual

## 4.3 Install SOL Driver

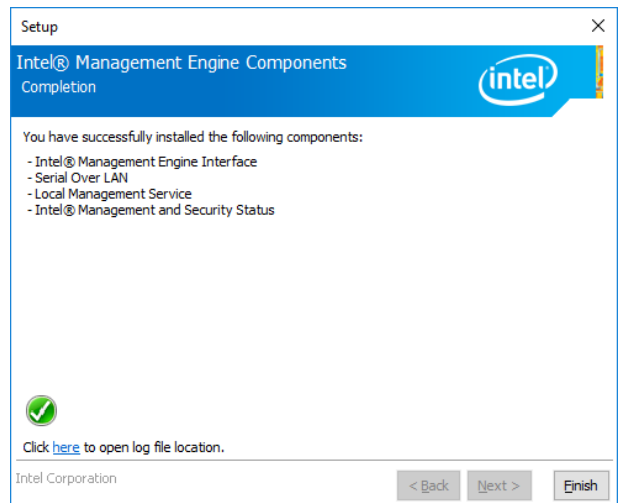
Insert the Supporting CD-ROM to CD-ROM drive, and it should show the index page of the products automatically. If not, locate Index.htm and choose the product from the menu left.



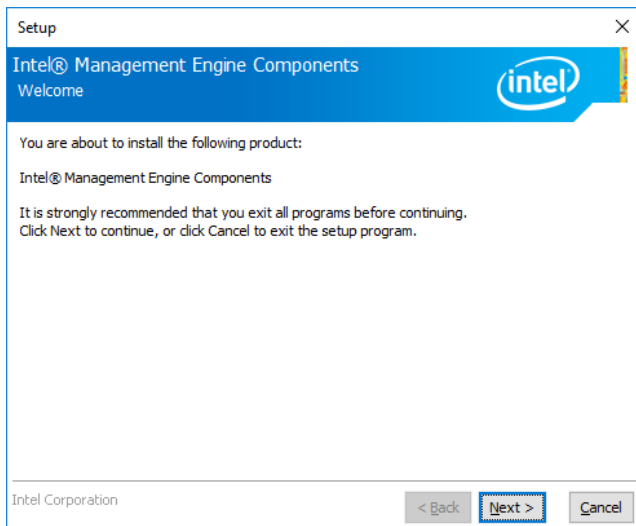
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



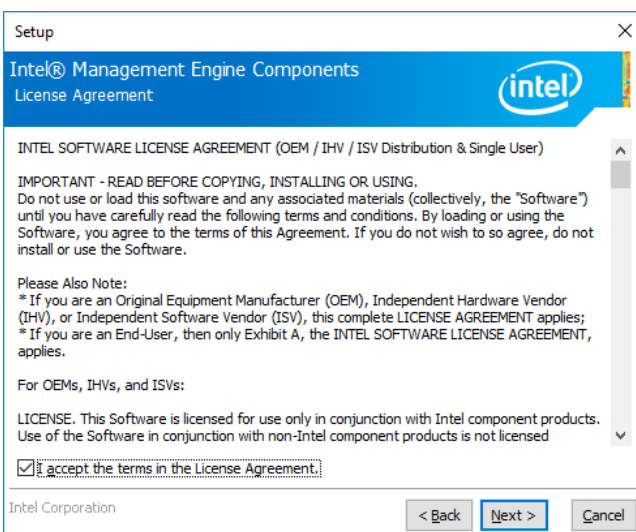
### Step 3. Click Next



### Step 4. Click Finish to complete the setup



### Step 1. Click Next to continue setup.



### Step 2. Click Next.

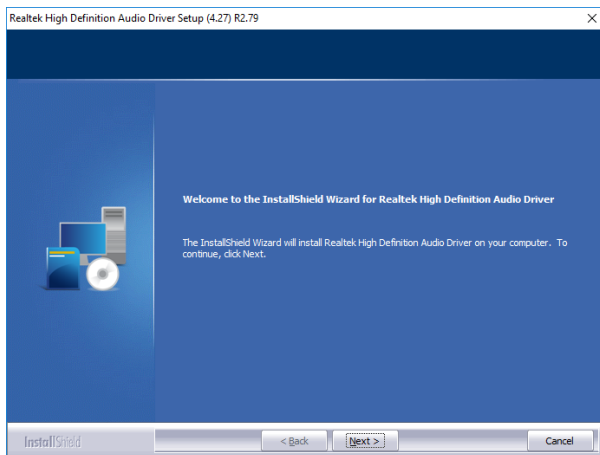


## 4.4 Install Audio Driver (For Realtek ALC892 HD Audio)

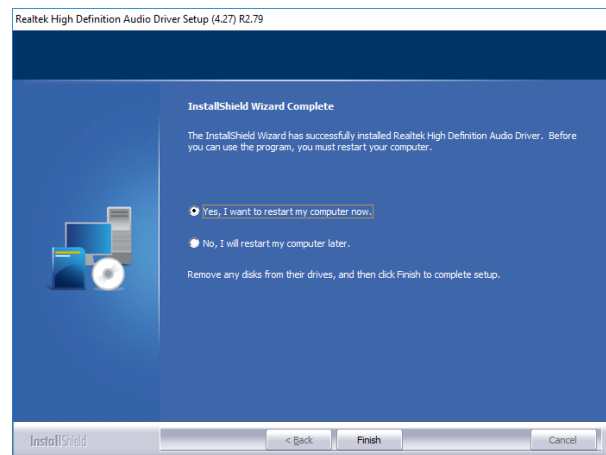
Insert the Supporting DVD-ROM to DVD-ROM drive, and it should show the index page of the products automatically. If not, locate Index.htm and choose the product from the menu left.



**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system. If the warning message appears while the installation process, click Continue to go on.



**Step1.** Click **Next** to Install.



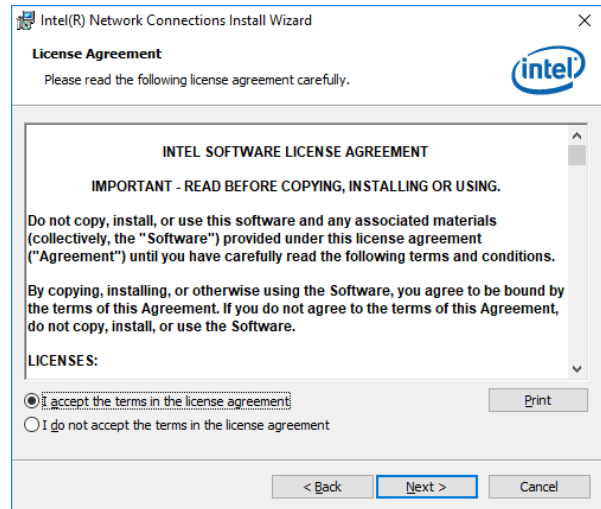
**Step 2.** Select **Finish** to complete Installation.

## 4.5 Install LAN Driver

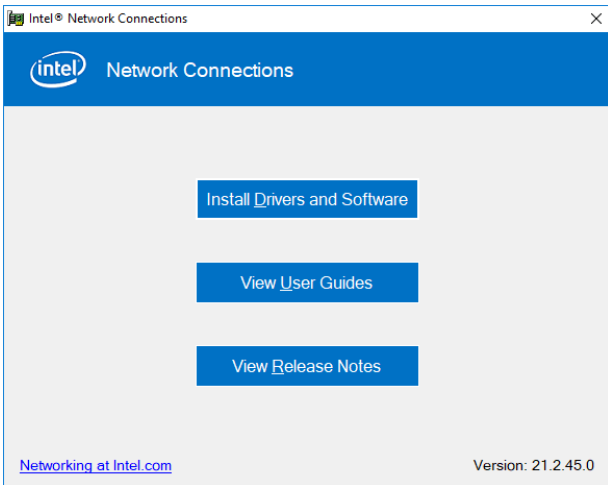
Insert the Supporting DVD-ROM to DVD-ROM drive, and it should show the index page of the products automatically. If not, locate Index.htm and choose the product from the menu left.



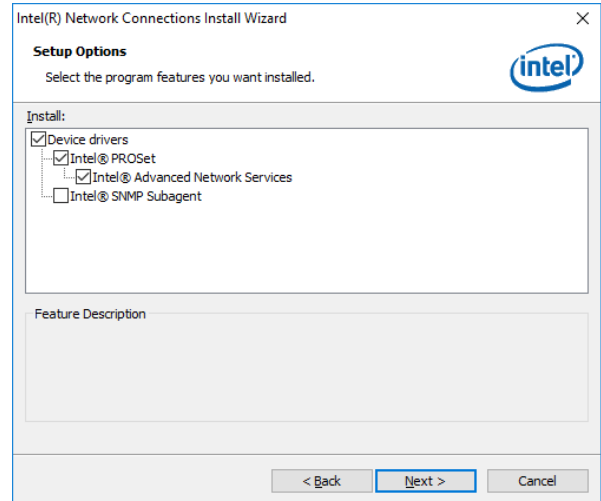
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



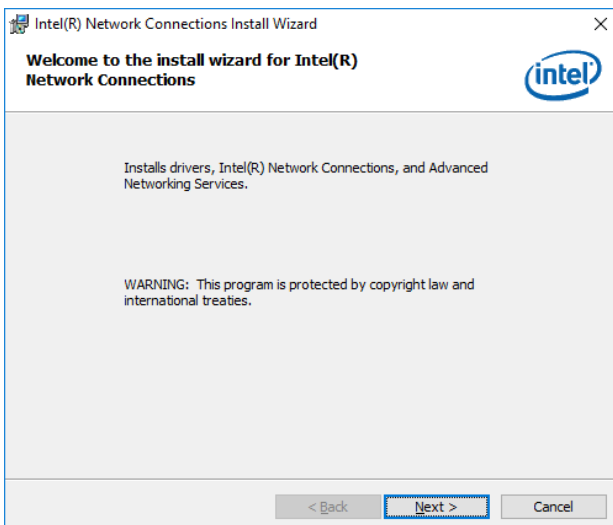
**Step 3. Click Next.**



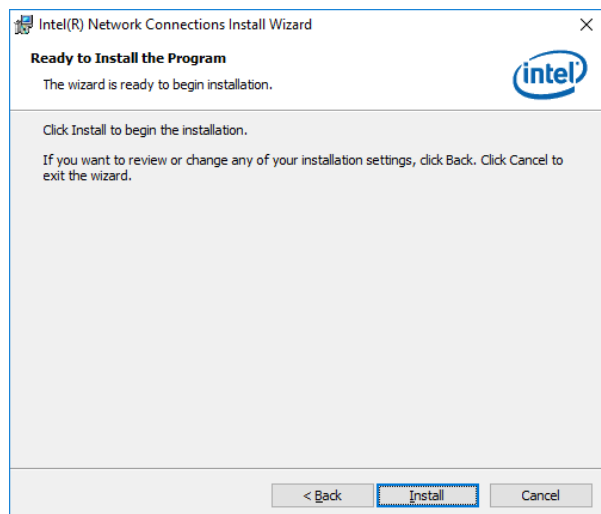
**Step 1. Click Install Drivers and Software.**



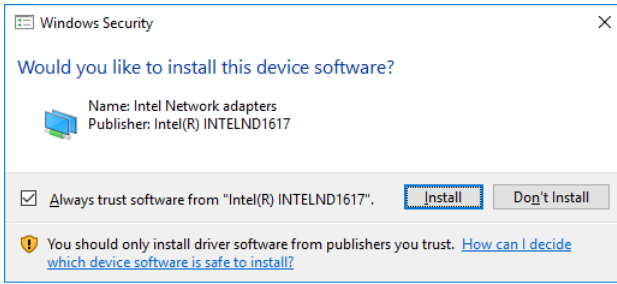
**Step 4. Click Next.**



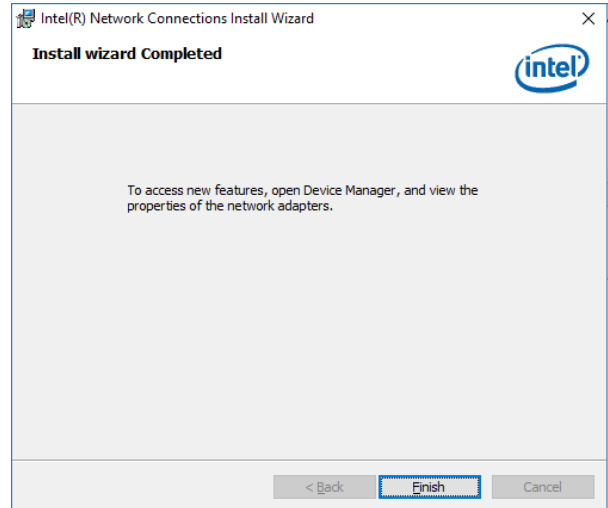
**Step 2. Click Next.**



**Step 5. Click Install.**



**Step 6. Click Install.**



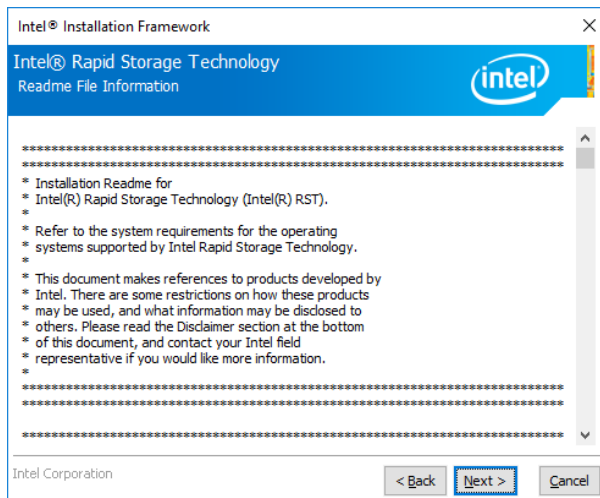
**Step 7. Click Finish to complete setup.**

## 4.6 Install RST Driver

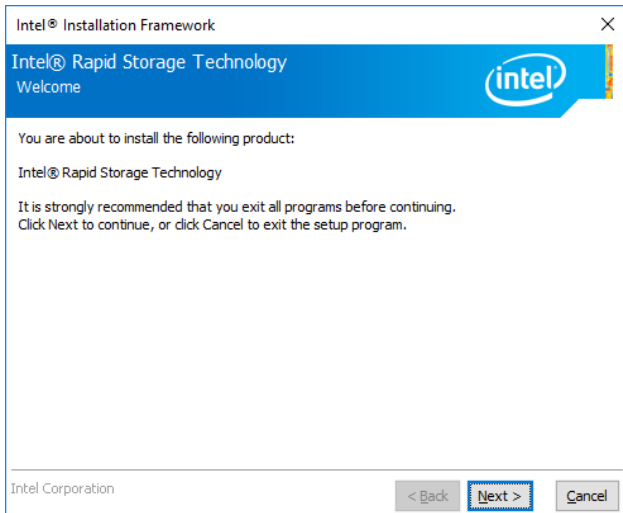
Insert the Supporting DVD-ROM to DVD-ROM drive, and it should show the index page of the products automatically. If not, locate Index.htm and choose the product from the menu left.



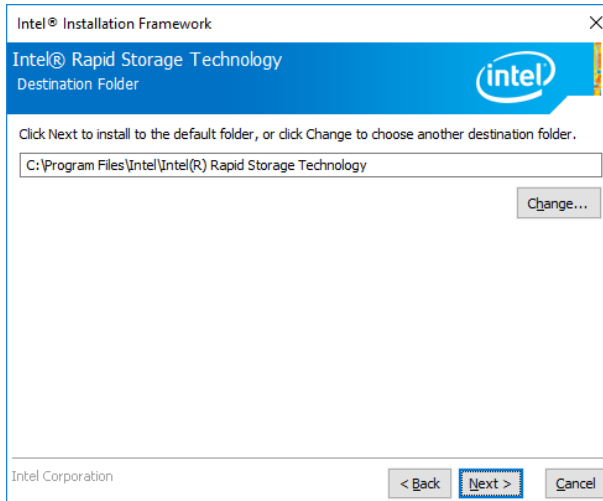
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



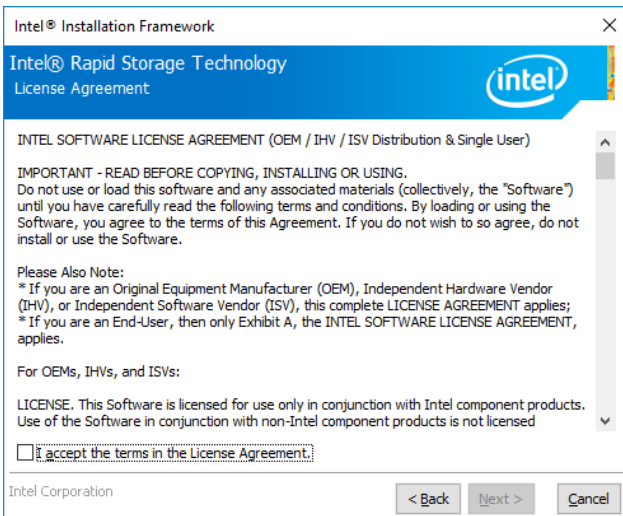
**Step 3. Click Next.**



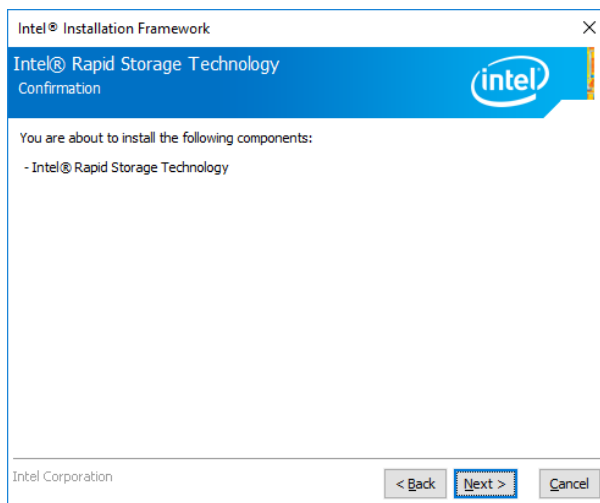
**Step 1. Click Next** to continue installation.



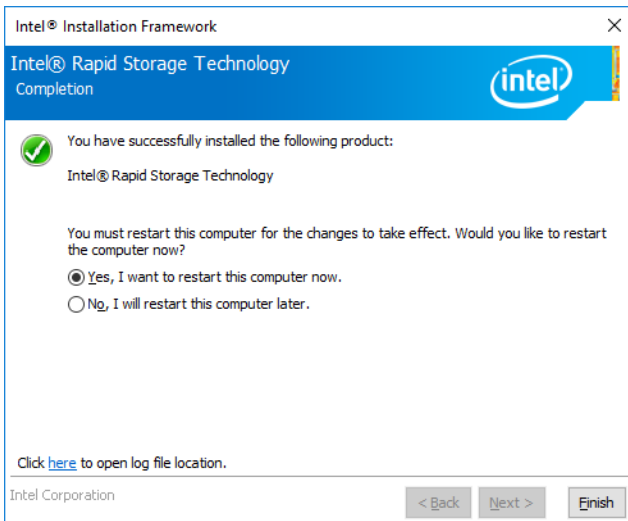
**Step 4. Click Next.**



**Step 2. Click Next.**



**Step 5. Click Next.**



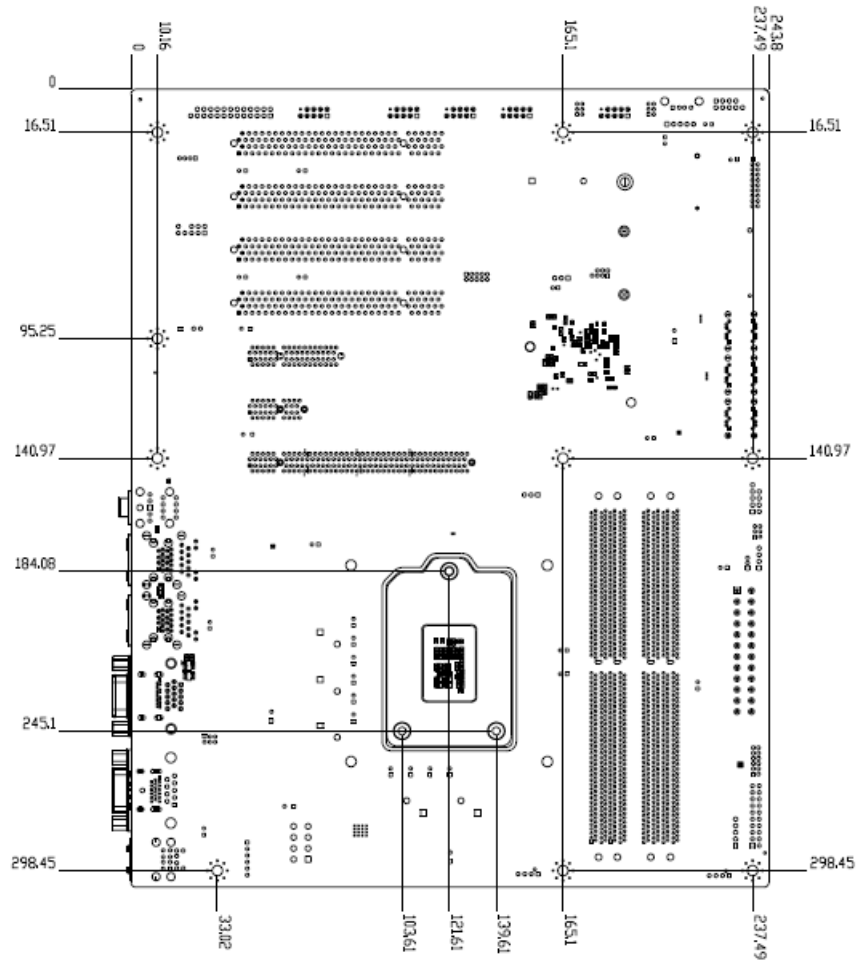
**Step 6.** Click **Finish** to complete setup.

# 5. Mechanical Drawing

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# BC170Q User's Manual



Unit: mm

