

# **EMP2-X404**

**mPCIe to four  
RS232/422/485**

**Customer:**

**Customer**

**Part Number:**

**Innodisk**

**Part Number:**

**Innodisk**

**Model Name:**

**Date:**

<b>Innodisk</b>	<b>Customer</b>
<b>Approver</b>	<b>Approver</b>

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## REVISION HISTORY

Revision	Description	Date
1.0	First Released	Sep, 2018
1.1	Update MTBF value	Jul, 2019
1.2	mPCIe Pin Define 3.3V => 3.3V AUX	Mar, 2020
1.3	Modify CLKREQ pin to GND	Dec, 2021

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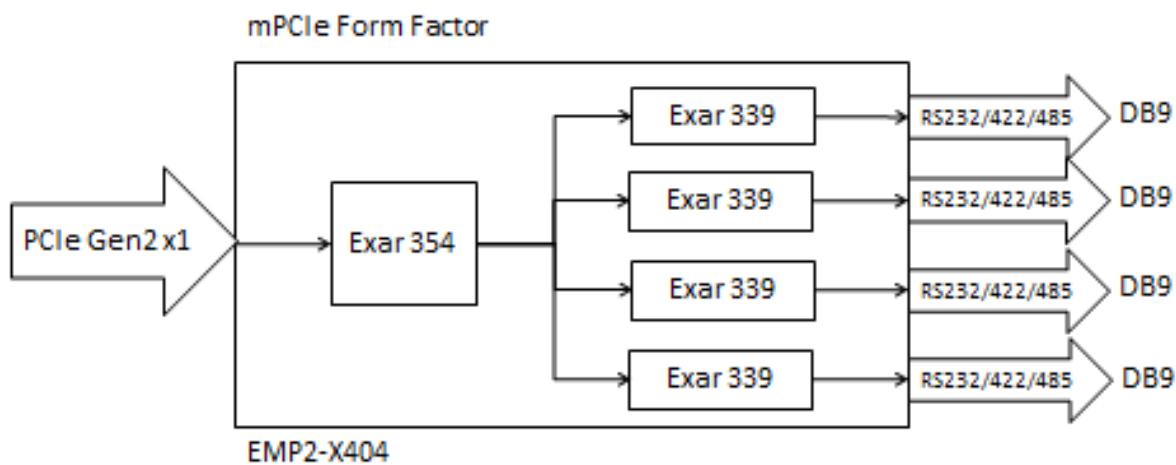
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# 1. Product Introduction

## 1.1. Overview

Innodisk EMP2-X404 is designed with standard Mini PCI Express form factor, EMP2-X404 supports PCIe Gen 2.0 with a single lane to four independent UARTs RS232/422/485, optimized for higher performance and lower power, which brings you a flexible expansion solution for embedded systems.



**Figure 1: Block Diagram**

## 1.2. Features

- PCIe 2.0 compliant. RS-232/422/485 mode configurable by software
- Up to 1 Mbps RS232 and 15Mbps RS422/485 serial data rate. 16C550 compatible. 256-byte FIFOs
- Industrial temperature (-40 °C to 85 °C) operation
- Full RS-232 functions with DB9 connector
- Minimize possible mechanical intervention without daughterboard
- Termination resistor enabled/disabled by DIP switch
- RI/5V/12V output switched by Jumper
- ESD up to 15KV(Electrostatic Discharge) protection circuit to prevent system damage

**Figure 2: Picture**

## 2. Product Specifications

### 2.1. Device Parameters

**Table 1: Device Parameters**

<b>Form Factor</b>	mPCIe
<b>Input I/F</b>	PCI Express 2.0 x 1
<b>Output I/F</b>	RS-232/422/485
<b>Output Connector</b>	DB-9 x 4
<b>Dimension (WxLxH)</b>	30 x 50.9 x 8 mm

### 2.2. Performance

**Table 2: RS232/422/485 Performance (unit: second)**

<b>Baud Rate</b>	<b>Transmission Data Size</b>	<b>RS232</b>	<b>RS485 Half Duplex</b>		<b>RS485 Full Duplex (RS422)</b>	
		<b>10m</b>	<b>100m</b>	<b>200m</b>	<b>100m</b>	<b>200m</b>
9600	5Kbyte	5	5	5	5	5
	1Mbyte	1,266	1,267	1,267	1,267	1,267
115200	1Mbyte	105	106	106	106	106
460800	1Mbyte	26	26	26	28	27
921600	1Mbyte	13	13	13	13	14

## 2.3. Electrical Specifications

### 2.3.1. Power Requirement

**Table 3: Power Requirement**

Item	Connector	Rating
Input voltage	mPCIe Golden Finger	+3.3 DC +-5%

### 2.3.2. Power Consumption

**Table 4: Power Consumption**

Full Load (mA)	Voltage (V)
150	3.3

## 2.4. Environmental Specifications

### 2.4.1. Temperature Ranges

**Table 5: Temperature Ranges**

Temperature	Range
Operating	Industrial Grade: -40°C to +85°
Storage	-55°C to +95°

### 2.4.2. Humidity

Relative Humidity: 10-95%, non-condensing

### 2.4.3. Shock and Vibration

**Table 6: Shock and Vibration**

Reliability	Test Conditions	Reference Standards
Vibration	7 Hz to 2K Hz, 20G, 3 axes	IEC 68-2-6
Mechanical Shock	Duration: 0.5ms, 1500 G, 3 axes	IEC 68-2-27

#### 2.4.4. Mean Time between Failure (MTBF)

Reliability prediction methodology provides the basis for reliability evaluation and analysis. The purpose of the prediction is to predict the life time of the product in units of failure rate and MTBF.

**Table 7: Mean Time between Failure (MTBF)**

Product	Condition	MTBF (Hours)
EMP2-X404	The analysis is at 25°C ambient temperature by Telcordia SR-332, Issues 4, Method I, Case 3 under Ground Benign, Controlled environment, 50% operation stress	19,167,421

#### 2.5. CE and FCC Compatibility

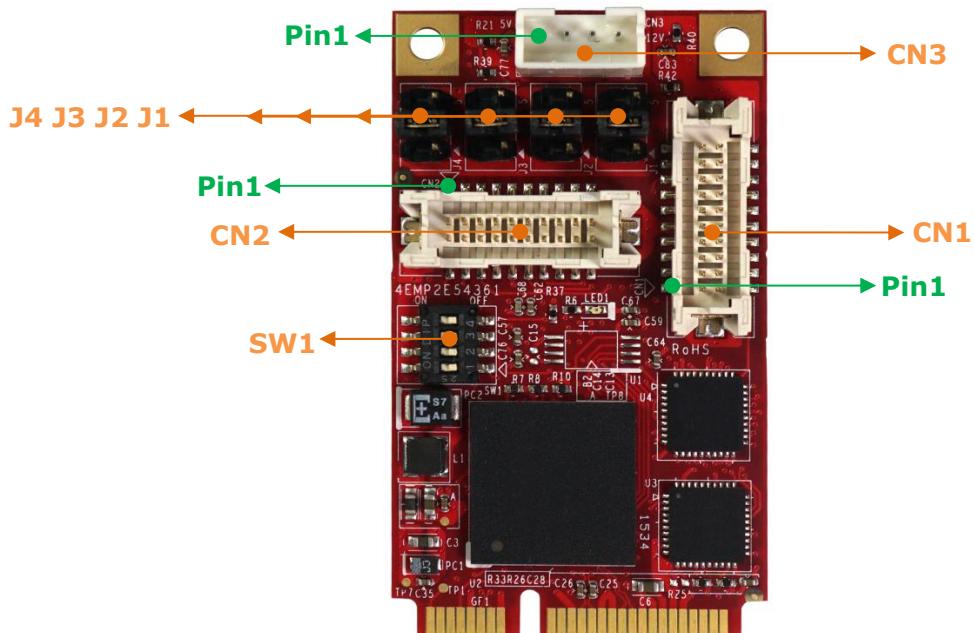
EMP2-X404 conforms to CE and FCC requirements.

#### 2.6. RoHS Compliance

EMP2-X404 is fully compliant with RoHS directive.

#### 2.7. Hardware

##### 2.7.1. Layout



**Table 8: PCB Layout Legend**

<b>Label</b>	<b>Connector Type</b>	<b>Function</b>
<b>CN1</b>	Wire to board SMD 2*10P 180° P:1.25mm H:4.8mm	Port 1(red edge cable), 2
<b>CN2</b>	Wire to board SMD 2*10P 180° P:1.25mm H:4.8mm	Port 3(red edge cable), 4
<b>CN3</b>	Wafer DIP 1*4P 180° P:2.0mm	Power input for DB9 pin9 (5V/12V)
<b>J1</b>	Pin Header DIP 2*3P(w/o pin2,6) 180° P:2.0mm	Port 1 DB9 pin9 mode setting (RI/5V/12V)
<b>J2</b>	Pin Header DIP 2*3P(w/o pin2,6) 180° P:2.0mm	Port 2 DB9 pin9 mode setting (RI/5V/12V)
<b>J3</b>	Pin Header DIP 2*3P(w/o pin2,6) 180° P:2.0mm	Port 3 DB9 pin9 mode setting (RI/5V/12V)
<b>J4</b>	Pin Header DIP 2*3P(w/o pin2,6) 180° P:2.0mm	Port 4 DB9 pin9 mode setting (RI/5V/12V)
<b>SW1</b>	DIP Switch, 8pin SMD 180°	RS422/485 Terminal resistor on/off

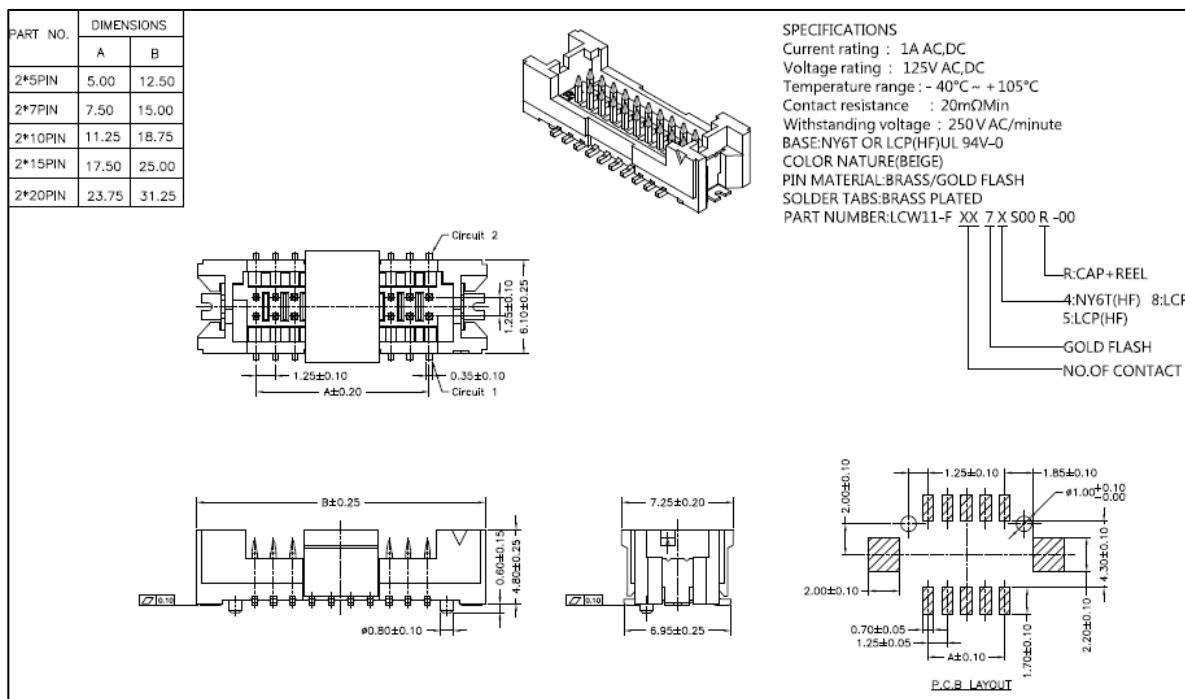
### 2.7.2. Pin Define

**Table 9: mPCIe Pin Define**

<b>Signal Name</b>	<b>Pin #</b>	<b>Pin #</b>	<b>Signal Name</b>
NC	<b>51</b>	<b>52</b>	3.3V AUX
NC	<b>49</b>	<b>50</b>	GND
NC	<b>47</b>	<b>48</b>	NC
NC	<b>45</b>	<b>46</b>	NC
GND	<b>43</b>	<b>44</b>	NC
3.3V AUX	<b>41</b>	<b>42</b>	NC
3.3V AUX	<b>39</b>	<b>40</b>	GND
GND	<b>37</b>	<b>38</b>	NC

GND	<b>35</b>	<b>36</b>	NC
PERXP	<b>33</b>	<b>34</b>	GND
PERXN	<b>31</b>	<b>32</b>	NC
GND	<b>29</b>	<b>30</b>	NC
GND	<b>27</b>	<b>28</b>	NC
PETXP	<b>25</b>	<b>26</b>	GND
PETXN	<b>23</b>	<b>24</b>	3.3V AUX
GND	<b>21</b>	<b>22</b>	PERST#
NC	<b>19</b>	<b>20</b>	NC
NC	<b>17</b>	<b>18</b>	GND
GND	<b>15</b>	<b>16</b>	NC
REFCLK+	<b>13</b>	<b>14</b>	NC
REFCLK-	<b>11</b>	<b>12</b>	NC
GND	<b>9</b>	<b>10</b>	NC
GND	<b>7</b>	<b>8</b>	NC
NC	<b>5</b>	<b>6</b>	NC
NC	<b>3</b>	<b>4</b>	GND
WAKE #	<b>1</b>	<b>2</b>	3.3V AUX

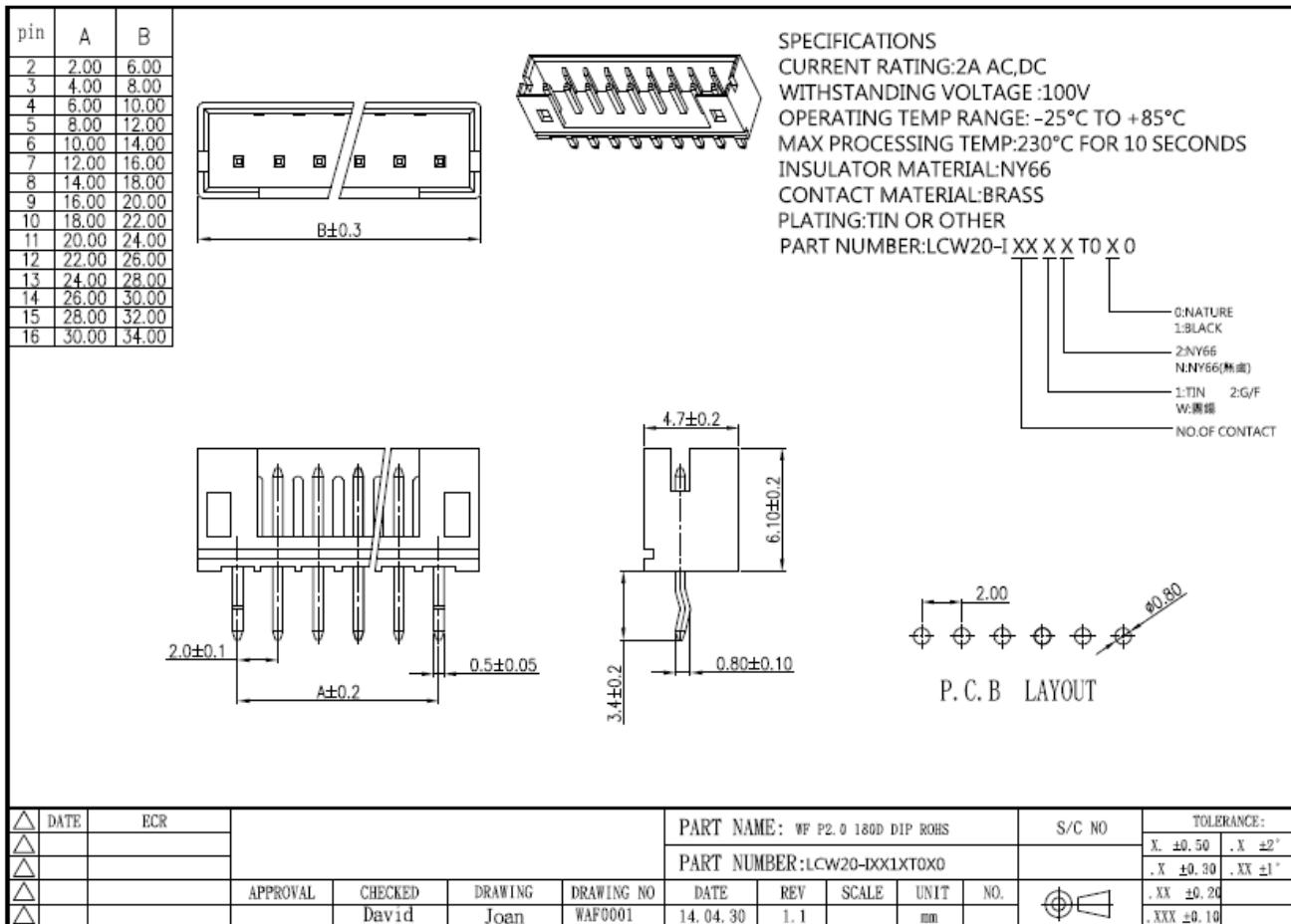
### 2.7.3. I/O Connector Mechanical Drawing & Pin Defines



**Figure 3: Wire to Board SMD 2\*10P Connector Drawing (CN1/CN2)**

**Table 10: Wire to Board SMD 2\*10P Connector Pin Define (CN1/CN2)**

Signal Name	Pin #	Pin #	Signal Name
CD_P2	<b>2</b>	<b>1</b>	CD_P1
RX_P2	<b>4</b>	<b>3</b>	RX_P1
TX_P2	<b>6</b>	<b>5</b>	TX_P1
DTR_21	<b>8</b>	<b>7</b>	DTR_P1
GND	<b>10</b>	<b>9</b>	GND
DSR_P2	<b>12</b>	<b>11</b>	DSR_P1
RTS_P2	<b>14</b>	<b>13</b>	RTS_P1
CTS_P2	<b>16</b>	<b>15</b>	CTS_P1
RI_P2	<b>18</b>	<b>17</b>	RI_P1
NC	<b>20</b>	<b>19</b>	NC

**Figure 4: Wafer DIP 1\*4P Connector Drawing (CN3)****Table 11 Wafer DIP 1\*4P Pin Define (CN3)**

Pin #	1	2	3	4
Signal Name	5V	GND	GND	12V

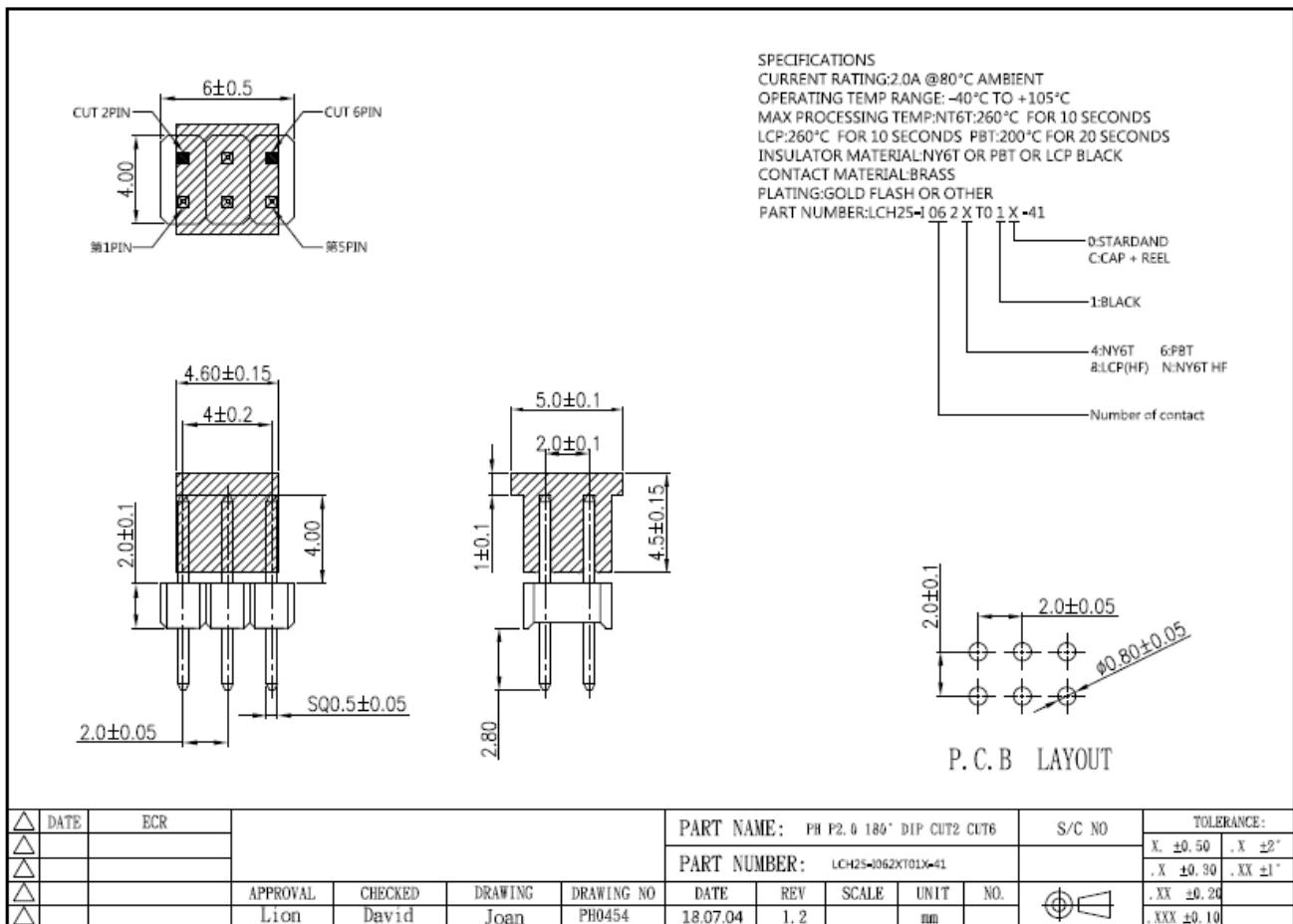
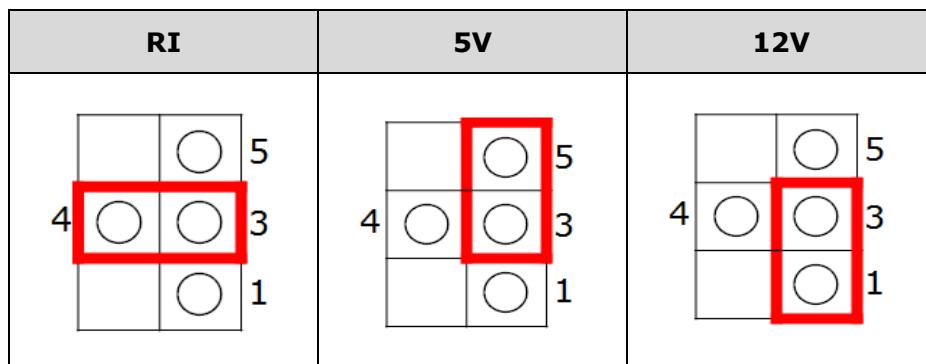
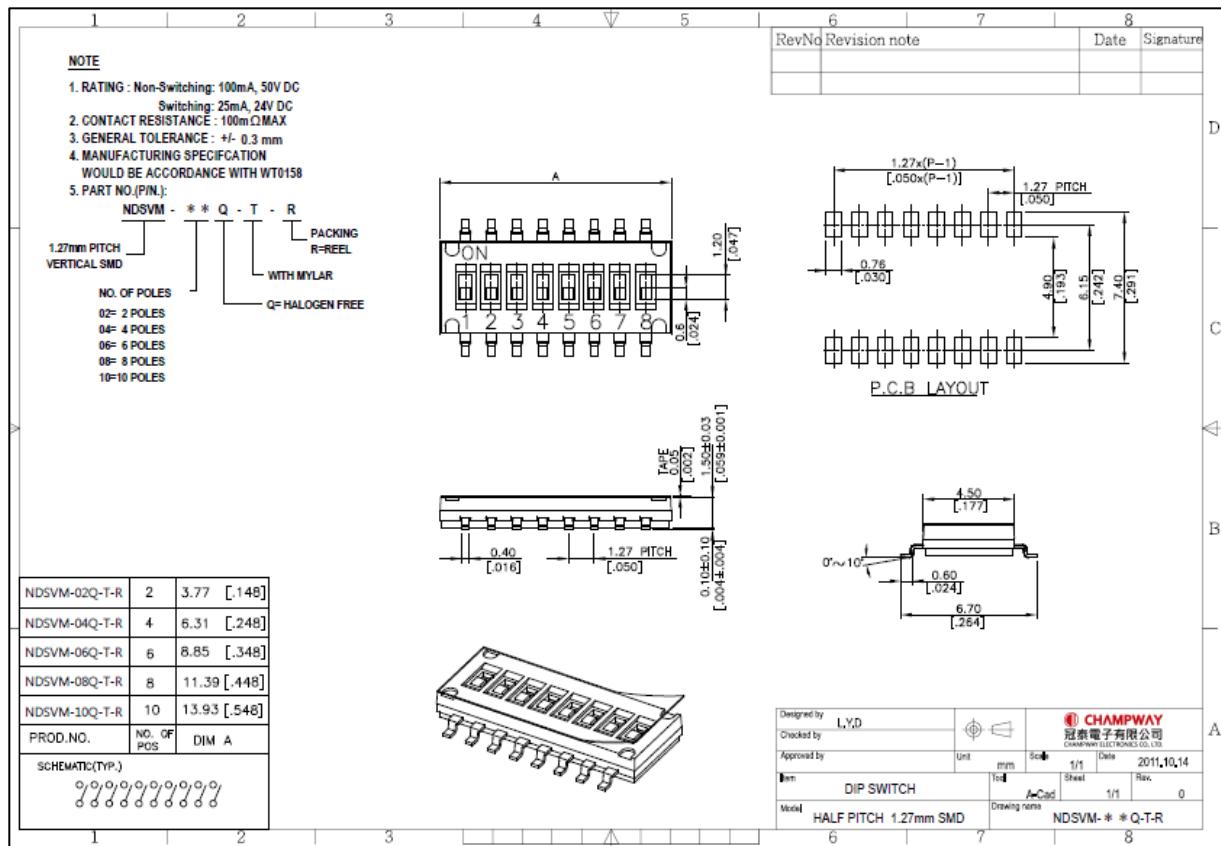


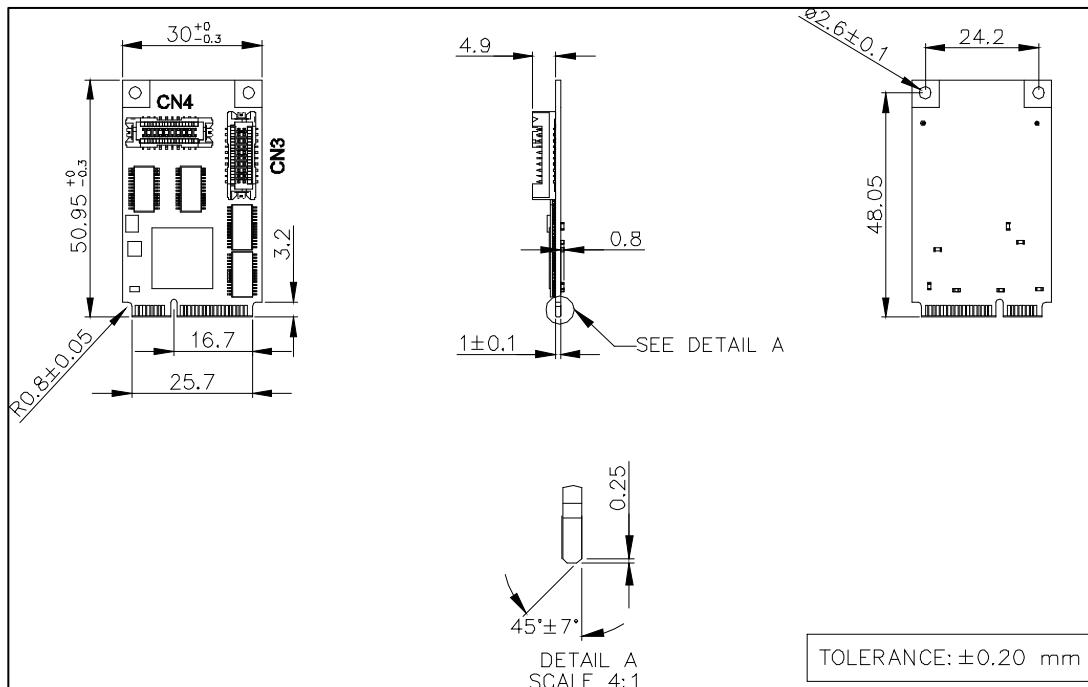
Figure 5: Pin Header DIP 2\*3P(w/o pin2,6) Drawing (J1/J2/J3/J4)

Table 12: Pin Header DIP 2\*3P(w/o pin2,6) Jumper Setting (J1/J2/J4/J4)



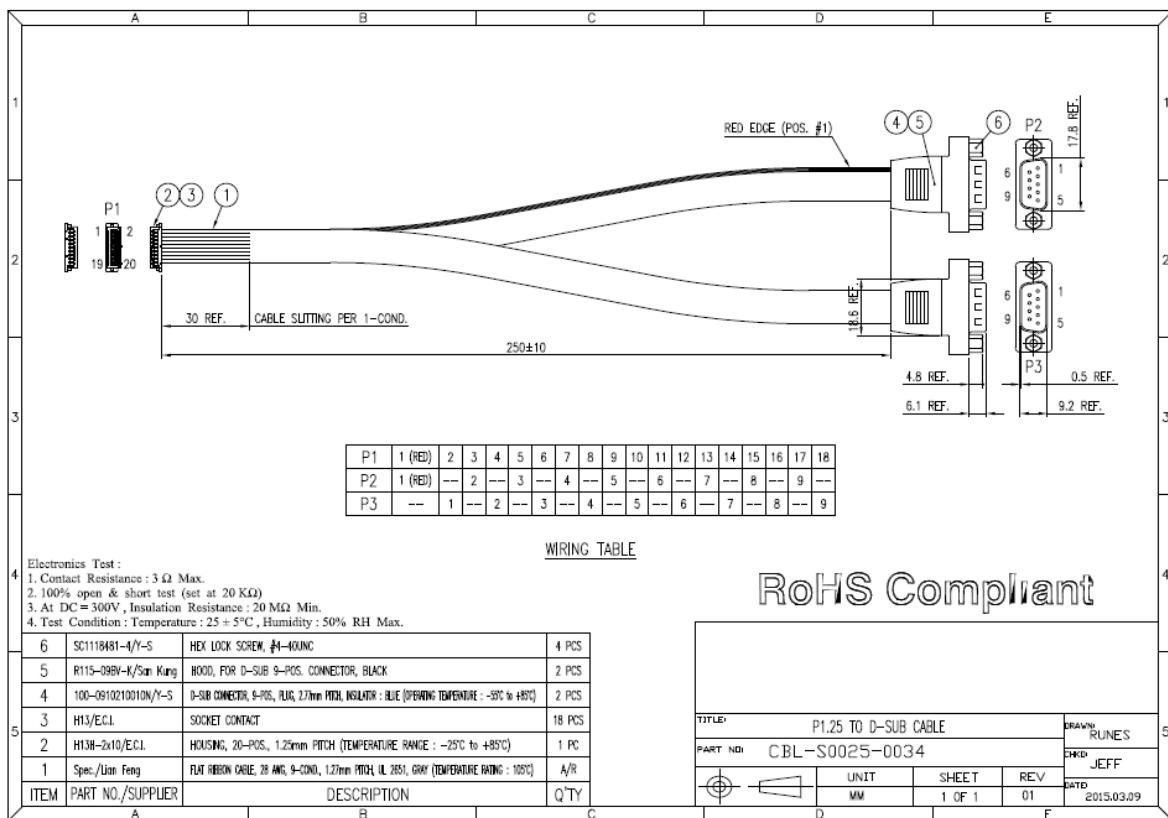


### 2.7.4. EMP2-X404 Mechanical Drawing



**Figure 7: EMP2-X404 Drawing**

### 2.7.5. Cable Mechanical Drawing & Pin defines



**Figure 8: DB9 Cable Drawing**

**Table 13: DB9 Cable Pin Define**

Pin #	1	2	3	4	5	6	7	8	9
<b>RS-232</b>	CD	RXD	TXD	DTR	GND	DSR	RTS	CTS	RI/5V/12V
<b>RS-422</b>	TX-	TX+	RX+	RX-	GND				RI/5V/12V
<b>RS-485</b>	D-	D+			GND				RI/5V/12V

#### 2.7.6. Packing List

- EMP2-X404 x1
- DB9 Cable x2

#### 2.8. Software Support

- Windows: XP, Windows 7, 8 , 8.1, 10, WES7, CE7.0
- Linux: Kernel 2.6.x and above.(Linux source code for modification)

## 3. Installation Guide

Please download driver and user manual from Myinnodisk web site.

<https://myinnodisk.innodisk.com/myinnodisk/Login.aspx>

## 4. Appendix



宜鼎國際股份有限公司  
Innodisk Corporation

Tel:(02)7703-3000 Fax:(02) 7703-3555 Internet: <http://www.innodisk.com/>

### REACH Declaration of Conformity

#### Manufacturer Product: All Innodisk EP products

1. 宜鼎國際股份有限公司（以下稱本公司）特此保證此售予貴公司之產品，皆符合歐盟化學品法案(Registration , Evaluation and Authorization of Chemicals ; REACH)之規定  
(<http://www.echa.europa.eu/de/candidate-list-table> last updated: 15/01/2018)。所提供之產品包含：(1) 產品或產品所使用到的所有原物料；(2)包裝材料；(3)設計、生產及重工過程中所使用到的所有原物料。

We Innodisk Corporation hereby declare that our products are in compliance with the requirements according to the REACH Regulation  
(<http://www.echa.europa.eu/de/candidate-list-table> last updated: 15/01/2018).  
Products include : 1) Product and raw material used by the product ; 2) Packaging material ; 3) Raw material used in the process of design, production and rework

2. 本公司同意因本保證書或與本保證書相關事宜有所爭議時，雙方宜友好協商，達成協議。  
InnoDisk Corporation agrees that both parties shall settle any dispute arising from or in connection with this Declaration of Conformity by friendly negotiations.

#### 立 保 證 書 人 (Guarantor)

Company name 公司名稱 : InnoDisk Corporation 宜鼎國際股份有限公司

Company Representative 公司代表人 : Randy Chien 簡川勝

Company Representative Title 公司代表人職稱 : Chairman 董事長

Date 日期 : 2018 / 02 / 08



**宜鼎國際股份有限公司  
Innodisk Corporation**

Page 1/1

Tel:(02)7703-3000 Fax:(02) 7703-3555 Internet: <http://www.innodisk.com/>

**RoHS 自我宣告書 (RoHS Declaration of Conformity)**

**Manufacturer Product: All Innodisk EP products**

**一、** 宜鼎國際股份有限公司（以下稱本公司）特此保證售予貴公司之所有產品，皆符合歐盟 2011/65/EU 及 (EU) 2015/863 關於 RoHS 之規範要求。

Innodisk Corporation declares that all products sold to the company, are complied with European Union RoHS Directive (2011/65/EU) and (EU) 2015/863 requirement.

**二、** 本公司同意因本保證書或與本保證書相關事宜有所爭議時，雙方宜友好協商，達成協議。

Innodisk Corporation agrees that both parties shall settle any dispute arising from or in connection with this Declaration of Conformity by friendly negotiations.

Name of hazardous substance	Limited of RoHS ppm (mg/kg)
鉛 (Pb)	< 1000 ppm
汞 (Hg)	< 1000 ppm
鎘 (Cd)	< 100 ppm
六價鉻 (Cr 6+)	< 1000 ppm
多溴聯苯 (PBBs)	< 1000 ppm
多溴二苯醚 (PBDEs)	< 1000 ppm
鄰苯二甲酸二(2-乙基己基)酯 (DEHP)	< 1000 ppm
鄰苯二甲酸丁酯苯甲酯 (BBP)	< 1000 ppm
鄰苯二甲酸二丁酯 (DBP)	< 1000 ppm
鄰苯二甲酸二異丁酯 (DIBP)	< 1000 ppm

**立 保 證 書 人 (Guarantor)**

Company name 公司名稱 : Innodisk Corporation 宜鼎國際股份有限公司

Company Representative 公司代表人 : Randy Chien 簡川勝

Company Representative Title 公司代表人職稱 : Chairman 董事長

Date 日期 : 2018 / 02 / 08



# Certificate

Issue Date: November 18, 2015  
 Ref. Report No. ISL-15LE550CE

Product Name : mPCIe to RS232/422/485 Module  
 Model(s) : E%P2-X#04  
               ( %: Form factor: ( 2: 2.5"SSD, 3:DDR3 DIMM, D:Dongle, G:NGFF\_M.2,  
               H:mPCIe Half, L:PCIe Low profile, M:mPCIe, S:SATA, X:Multi, Z:Others )  
               #: Output items: ( 1:1Port, 2:2Ports, 3:3Ports, 4:4Ports, A~Z:TBD,X:Multi ) )  
 Responsible Party : Innodisk Corporation  
 Address : 5F.,No.237, Sec. 1, Datong Rd., Xizhi Dist., New Taipei City 221, Taiwan  
               (R.O.C.)

We, International Standards Laboratory, hereby certify that:

The device bearing the trade name and model specified above has been shown to comply with the applicable technical standards as indicated in the measurement report and was tested in accordance with the measurement procedures specified in European Council Directive- EMC Directive 2004/108/EC. The device was passed the test performed according to :



**Standards:**

EN 55022: 2010+AC2011 and CISPR 22: 2008 (modified)  
 EN 61000-3-2:2014 and IEC 61000-3-2:2014  
 EN 61000-3-3: 2013 and IEC 61000-3-3: 2013  
 EN 55024: 2010 and CISPR 24: 2010  
 EN 61000-4-2: 2009 and IEC 61000-4-2: 2008  
 EN 61000-4-3: 2006+A1: 2008 +A2: 2010 and  
 IEC 61000-4-3:2006+A1: 2007+A2: 2010  
 EN 61000-4-4:2012 and IEC 61000-4-4:2012

I attest to the accuracy of data and all measurements reported herein were performed by me or were made under my supervision and are correct to the best of my knowledge and belief. I assume full responsibility for the completeness of these measurements and vouch for the qualifications of all persons taking them.

International Standards Laboratory

W.H. Chang / Director

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# Certificate

Issue Date: November 18, 2015  
 Ref. Report No. ISL-15LE550FB

Product Name : mPCIe to RS232/422/485 Module  
 Model(s) : EMP2-X#04  
 ( %: Form factor: ( 2: 2.5"SSD, 3:DDR3 DIMM, D:Dongle, G:NGFF\_M.2,  
 H:mPCIe\_Half, L:PCIe\_Low profile, M:mPCIe, S:SATA, X:Multi, Z:Others )  
 #: Output items: ( 1:1Port, 2:2Ports, 3:3Ports, 4:4Ports, A~Z:TBD,X:Multi ))  
 Applicant : Innodisk Corporation  
 Address : 5F, No.237, Sec. 1, Datong Rd., Xizhi Dist., New Taipei City 221, Taiwan  
 (R.O.C.)

We, International Standards Laboratory, hereby certify that:

The device bearing the trade name and model specified above has been shown to comply with the applicable technical standards as indicated in the measurement report and was tested in accordance with the measurement procedures specified. (refer to Test Report if any modifications were made for compliance).

**Standards:**



FCC CFR Title 47 Part 15 Subpart B: 2014- Section 15.107 and 15.109

ANSI C63.4-2009

Industry Canada Interference-Causing Equipment Standard ICES-003 Issue 5: 2012

**Class B**

I attest to the accuracy of data and all measurements reported herein were performed by me or were made under my supervision and are correct to the best of my knowledge and belief. I assume full responsibility for the completeness of these measurements and vouch for the qualifications of all persons taking them.

**International Standards Laboratory**

W.H. Chang / Director

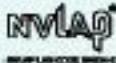
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December 15, 2021