

AIMB-233

Intel® Core™ 8th Gen ULT
Mini-ITX with Type C Alt./HDMI/
LVDS(eDP), 6 COM, Dual LAN,
miniPCle,
M.2, PCIe x1, DDR4, DC Input

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Memory Compatibility

Test Item	Description							Result	Remark
Brand	Size	Speed	Type	ECC	Vendor PN	Memory	Advantech PN		
Advantech	16 GB	2666	DDR4	N	AQD-SD4U16N2 6-SE	SEC 813 K4A8G08 5WB BCTD	AQD-SD4U16N2 6-SE	PASS	
Advantech	4 GB	2666	DDR4	N	SQR-SD4N4G2K 6SNEEB	SEC 807 K4A4G08 5WE BCTD	SQR-SD4N4G2K 6SNEEB	PASS	
Advantech	4 GB	2400	DDR4	N	SQR-SD4N4G2K 4SNEEB	SEC 807 K4A4G08 5WE BCRC	SQR-SD4N4G2K 4SNEEB	PASS	
Advantech	8 GB	2133	DDR4	N	AQD-SD4U8GN2 1-SG	SEC 552 BCPB K4A4G085 WD	AQD-SD4U8GN 21-SG	PASS	
Advantech	16 GB	2400	DDR4	N	AQD-SD4U16N2 4-HE	H5AN8G8N AFRUHC	AQD-SD4U16N2 4-HE	PASS	
Advantech	16 GB	3200	DDR4	N	96SD4-16G 3200NN-MI	9CJ45 D9WSM	96SD4-16G 3200NN-MI	PASS	

Ordering Information

Order Number	CPU	Display	GbE	TPM	Storage	USB	PCIe/PCI
AIMB-233G2-U7A1E	i7-8665UE						
AIMB-233G2-U5A1E	i5-8365UE	HDMI, Type C Alt, LVDS/eDP (optional)	2	Yes (optional)	2 x SATA III 1 x mSATA	4 x USB 3.1, 2 x USB 3.0,	1 x Full-size Mini-PCIe
AIMB-233G2-U3A1E	i3-8145UE						
AIMB-233G2-U0A1E	Celeron 4305UE					2 x USB 2.0,	1 x PCIe1

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2. Call your dealer and describe the problem. Please have your manual, product, and any helpful information readily available.
3. If your product is diagnosed as defective, obtain an RMA (return merchandise authorization) number from your dealer. This allows us to process your return more quickly.
4. Carefully pack the defective product, a fully-completed Repair and Replacement Order Card and a photocopy proof of purchase date (such as your sales receipt) in a shippable container. A product returned without proof of the purchase date is not eligible for warranty service.
5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

Initial Inspection

Before you begin installing your motherboard, please make sure that the following materials have been shipped:

- 1 x AIMB-233 Intel® Core™ 9th Gen ULT Mini-ITX
- 2 x SATA HDD cable
- 2 x SATA Power cable
- 3 x Com port cable
- 1 x CPU Fanless Heat Sink
- 1 x I/O port bracket
- 1 x Startup manual
- 1 x Warranty card

If any of these items are missing or damaged, contact your distributor or sales representative immediately. We have carefully inspected the AIMB-233 mechanically and electrically before shipment. It should be free of marks and scratches and in perfect working order upon receipt. As you unpack the AIMB-233, check it for signs of shipping damage. (For example, damaged box, scratches, dents, etc.) If it is damaged or it fails to meet specifications, notify our service department or your local sales representative immediately. Also, notify the carrier and retain the shipping carton and packing material for inspection by the carrier. After inspection, we will make arrangements to repair or replace the unit.

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Chapter 1

General Information

1.1 Introduction

AIMB-233 is designed with the Intel® Whiskeylake ULT Dual/Quard Core processors for industrial applications that require both performance computing and enhanced power management capabilities. The motherboard supports Intel whiskeylake ULT Dual Celereon 2.0 GHz / i3 2.2 GHz processors, Quard core i5 1.6 GHz / i7 1.7 GHz processors and DDR4 SO-DIMM 2400 up to 16GB. Multiple I/O connectivity includes 6 serial ports, 4 USB 3.1, 2 USB 3.0, 2 USB 2.0, dual GbE LAN and 2 SATA III ports.

1.2 Features

- **Rich I/O connectivity:** 6 serial ports, 4 USB 3.1, 2 USB 3.0, 2 USB 2.0, 2 SATA 3.0, 1 M.2 E key, 1 M.2 M key, 1 PCIe x1, Dual GbE LAN
- **Standard Mini-ITX form factor with industrial features:** The AIMB-233 is a full featured Mini-ITX motherboard with balanced expandability and performance.
- **Wide selection of storage devices:** SATA HDD, customers benefit from the flexibility of using the most suitable storage device for larger capacity.
- **Optimized integrated graphic solution:** With Intel® Graphics Flexible, it supports versatile display options and 32-bit 3D graphics engine.

1.3 Specifications

1.3.1 System

- **CPU:** BGA 1528 14nm Processor ULT Dual/Quard Core i7/ i5/ i3/ Celeron processor
- **BIOS:** AMI uEFI 32 Mbit SPI BIOS
- **System chipset:** Intel® Cannon Lake PCH-LP
- **SATA hard disk drive interface:**
 - Two on-board SATA connectors with data transmission rate up to 600 MB
- **M.2 Interface:** Supports M.2 E key (2230) and M key (2242 & 2280)

1.3.2 Memory

- **RAM:** Up to 32 GB in 2 slots 260-pin SODIMM sockets. Supports dual channel DDR4 2400 MHz SDRAM

1.3.3 Input/Output

- **Serial ports:** Six serial ports, support 5 x RS-232 and 1 x RS-232/422/485 (with hardware auto flow control)
- **USB port:** Supports up to eight USB ports with transmission rates of up to 10 gbps, including 4 x USB 3.1 & 2 x USB 3.0 and 2 x USB 2.0.
- **GPIO connector:** 8-bit general purpose Input/Output

1.3.4 Graphics

- **Controller:** Embedded in processor, Support DirectX 12, OpenGL 4.5, OpenCL 2.1, Full HW accelerated video decoding/encoding for AVC/MPEG2/HEVC/VP8/VP9/MJPEG. (VC1: decoding only)
- **LVDS:** Chrontel CH7511 single channel 24-bit/ dual channel 48-bit LVDS, Max 1920 x 1200 @ 60 Hz
- **HDMI:** Supports HDMI 2.0a for HD video playback max resolution up to 4096x2160@60Hz on HDMI
- **Type C Alt.:** Supports DP 1.2 from Whiskeylake-U Processor, max resolution up to 4096x2304@60Hz,24bpp.

Note! Triple independent displays need to use HDMI2.0a+Type C Alt.+LVDS (or eDP).



1.3.5 Ethernet LAN

- Supports dual 10/100/1000 Mbps Ethernet port (s) via PCI Express x1 bus which provides 500 MB/s data transmission rate
- **Controller:** LAN1: Intel I219LM GbE PHY; LAN2: Intel I211AT GbE (co-lay intel I210IT)

1.3.6 Industrial features

- **Watchdog timer:** Can generate a system reset. The watchdog timer is programmable, with each unit equal to one second or one minute (255 levels)

1.3.7 Mechanical and environmental specifications

- **Operating temperature:** 0 ~ 60° C (32 ~ 140° F, depending on CPU speed and cooler solution)
- **Storage temperature:** -40 ~ 85° C (-40 ~ 185° F)
- **Humidity:** 5 ~ 95% non-condensing
- **Power supply voltage:** 12 ~ 24V
- **Power consumption:** Typical: 26.74W @12V, 29.04W @24V
Boost: 63.28W @12V, 64.73W @24V
Measure the maximum current value which system under maximum load (CPU: Top speed, RAM & Graphic: Full loading)
- **Board size:** 170 mm x 170 mm (6.69" x 6.69")
- **Board weight:** 0.365 kg

1.4 Jumpers and Connectors

Connectors on the AIMB-233 motherboard link it to devices such as hard disk drives and a keyboard. In addition, the board has a number of jumpers used to configure your system for your application.

The tables below list the function of each of the board jumpers and connectors. Later sections in this chapter give instructions on setting jumpers. Chapter 2 gives instructions for connecting external devices to your motherboard.

Table 1.1: Jumpers

Label	Function
JFP1/JFP2	Front Panel Header
CMOS1	CMOS clear (Default 1-2)
PSON1	AT(1-2) / ATX(2-3) (Default 2-3)
JWDT1+JOBS1	Watchdog Reset and OBS Alarm
JLVDS1	Voltage 3.3V/5V/12V selector for LVDS1 connector (Default none, 3.3V)
COM1/COM2	RS-232
COM3456	COM3: RS-232/422/485, COM4/5/6: RS-232
CPUFAN1/SYSFAN	FAN control by DC or PWM mode selection

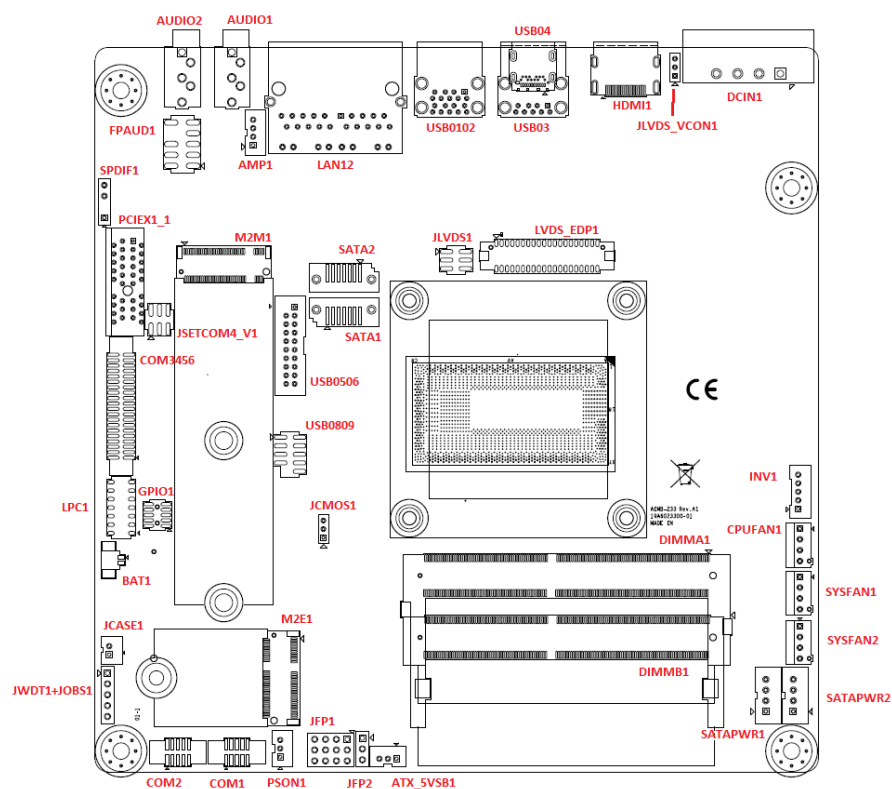
Table 1.2: Connectors

Label	Function
LVDS_EDP1	LVDS/eDP(Optional) connector
INV1	LVDS inverter connector
USB0102	USB port 1, 2 connector
USB03/USB04	USB port 3, 4 connector
USB0506	USB port 5, 6 (on board)
USB0809	USB port 8, 9 (on board)
HDMI	HDMI 2.0a connector
Type C Alt.	USB type C connector
COM1, 2	Serial port connector (RS232)
COM3456	COM3: RS-232/422/485, COM4/5/6: RS-232
CPUFAN1	CPU FAN 2.54mm connector (4-pin)
SYSFAN1/SYSFAN2	System FAN1 2.54mm connector (4-pin)
LAN12	LAN1 / LAN2
M2M1	M.2 M-Key connector_2242/2280
M2E1	M.2 E-Key connector_2230
MINI_PCIE1	MINI PCIE connector
PCIEX1_1	PCI Express x1 slot
AUDIO1, AUDIO2	Audio connector
SPDIF1	SPDIF Audio out pin header
FPAUD1	HD Audio Front Panel Pin Header
SATA1/SATA2	Serial ATA data connector 1/2
SATA_PWR1/2	Serial ATA power connector 1/2
DIMMA1/DIMMB1	Memory connector channel
LPC1	Low Pin Count Header
GPIO1	GPIO header

Table 1.2: Connectors

BAT1	Battery connector
DCIN1	DC jack 12-24V Main power connector
AMP1	Amplifier connector

1.5 Board layout: Jumper and Connector Locations

**Figure 1.1 Jumper and Connector Location**

1.6 AIMB-233 Board Diagram

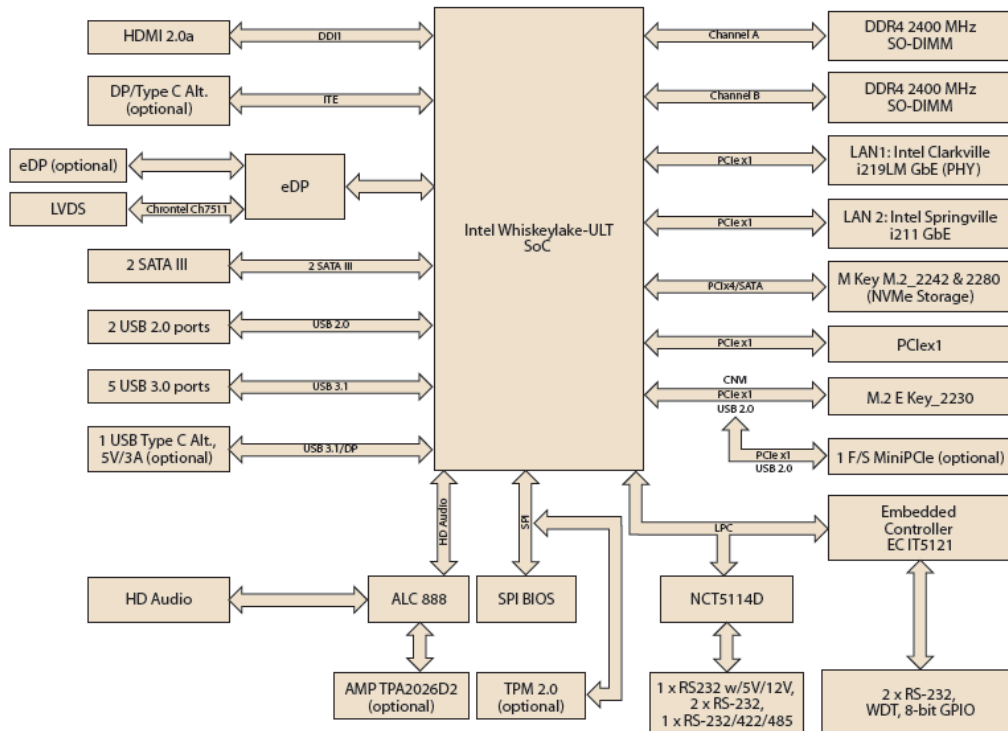


Figure 1.2 AIMB-233 Board Diagram

1.7 Safety Precautions

Warning! Always completely disconnect the power cord from chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.



Caution! Always ground yourself to remove any static charge before touching the motherboard. Modern electronic devices are very sensitive to electrostatic discharges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis.



Caution! The computer is provided with a battery-powered real-time clock circuit. There is a danger of explosion if battery is incorrectly replaced. Replace only with same or equivalent type recommended by the manufacturer. Discard used batteries according to manufacturer's instructions.



Caution! There is a danger of a new battery exploding if it is incorrectly installed. Do not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.



1.8 Jumper Settings

This section provides instructions on how to configure your motherboard by setting the jumpers. It also includes the motherboards's default settings and your options for each jumper.

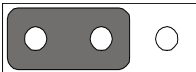

1.8.1 How to Set Jumpers

You can configure your motherboard to match the needs of your application by setting the jumpers. A jumper is a metal bridge that closes an electrical circuit. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” (or turn ON) a jumper, you connect the pins with the clip. To “open” (or turn OFF) a jumper, you remove the clip. Sometimes a jumper consists of a set of three pins, labeled 1, 2, and 3. In this case you connect either pins 1 and 2, or 2 and 3. A pair of needle-nose pliers may be useful when setting jumpers.

1.8.2 CMOS Clear (JCMOS1)

The AIMB-233 motherboard contains a jumper that can erase CMOS data and reset the system BIOS information. Normally this jumper should be set with pins 1-2 closed. If you want to reset the CMOS data, set CMOS1 to 2-3 closed for just a few seconds, and then move the jumper back to 1-2 closed. This procedure will reset the CMOS to its default setting.

Table 1.3: CMOS1

Function	Jumper Setting
*Keep CMOS data	 1-2 closed
Clear CMOS data	 2-3 closed

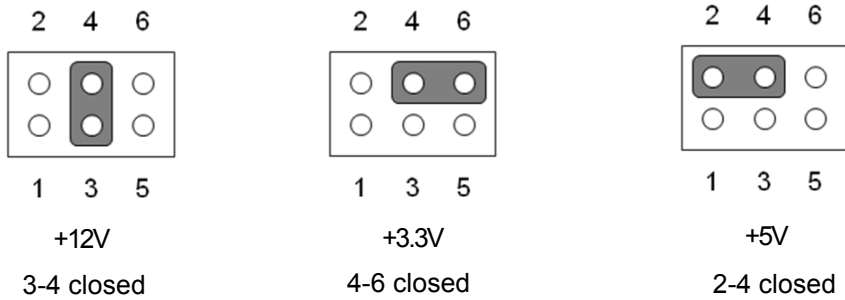
* Default

1.8.3 JLVDS1 : LCD Power 3.3 V/5 V/ 12 V Selector

Table 1.4: JLVDS1/JLVDS2: LCD Power 3.3 V/5 V/ 12 V Selector

Closed Pins	Result
JLVDS1	
3-4	Jumper for 12 V LVDS panel
4-6	Jumper for 3.3 V LVDS panel*
2-4	Jumper for 5 V LVDS panel

*Default

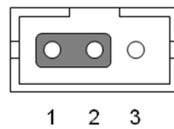


1.8.4 PSON1: ATX, AT Mode Selector

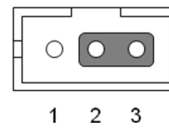
Table 1.5: PSON1: ATX, AT Mode Selector

Closed Pins	Result
1-2	AT Mode
2-3*	ATX Mode

*Default



AT Mode
1-2 closed



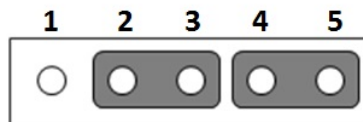
ATX Mode
2-3 closed

1.8.5 JWDT1+JOBS1: Watchdog Timer Output and OBS Alarm Option

Table 1.6: JWDT1+JOBS1: Watchdog Timer Output and OBS Alarm Option

Closed Pins	Result
1-2	NC
2-3*	Watchdog Timer Output OBS_Beep
4-5*	Error Beep*

*Default



1.9 System Memory

The AIMB-233 has two sockets for a 260-pin DDR4 SODIMM. DRAM is available in capacities of 1 GB, 2 GB, 4 GB, 8 GB and 16 GB. The sockets can be filled in any combination with SODIMMs of any size, giving a total memory size between 1 GB, 2 GB, 4 GB, 8GB and 16GB. AIMB-232 does NOT support ECC (error checking and correction).

1.10 Memory Installation Procedures

To install SODIMMs, first make sure the two handles of the SODIMM socket are in the “open” position, i.e., the handles lean outward. Slowly slide the SODIMM module along the plastic guides on both ends of the socket. Then firmly but gently (avoid pushing down too hard) press the SODIMM module well down into the socket, until you hear a click when the two handles have automatically locked the memory module into the correct position of the SODIMM socket. To remove the memory module, just push both handles outward, and the memory module will be ejected by the mechanism.

1.11 CPU Frequency

The AIMB-233 supports a CPU with one of the following built-in full speed :

1.7 GHzfor Intel Core i7-8665UE

1.6 GHzfor Intel Corecore i5-8365UE

2.2 GHz for Intel Core i3-8145UE

2.0 GHz for Intel Celeron 4305UE

The built-in third-level cache in the processor yields much higher performance than conventional external cache memories.

Chapter 2

Connecting
Peripherals

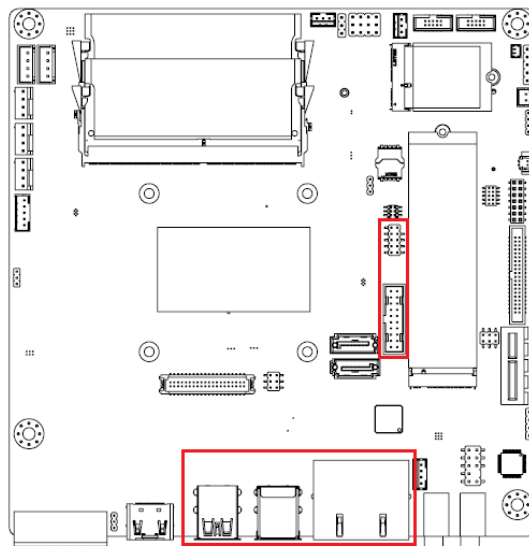
2.1 Introduction

You can access most of the connectors from the top of the board as it is being installed in the chassis. If you have a number of cards installed or have a packed chassis, you may need to partially remove the card to make all the connections.

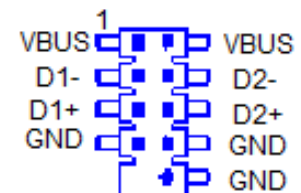
2.2 LAN Ports and USB Ports (LAN12, USB0102, USB03,USB04, USB0506,USB0809)

The AIMB-233 provides up to eight USB ports. Four USB3.1 on the rear side, two USB3.0 and two USB2.0 on the board. The USB interface can be disabled in the system BIOS setup.

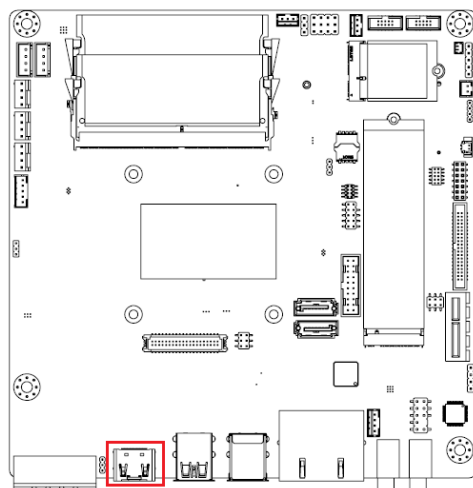
The AIMB-233 is equipped with two high-performance 1000 Mbps Ethernet LAN adapter, both of which are supported by all major network operating systems. The RJ-45 jacks on the rear panel provide for convenient LAN connection.



USB03 USB0102 LAN12
USB04



2.3 HDMI Connector (HDMI1)



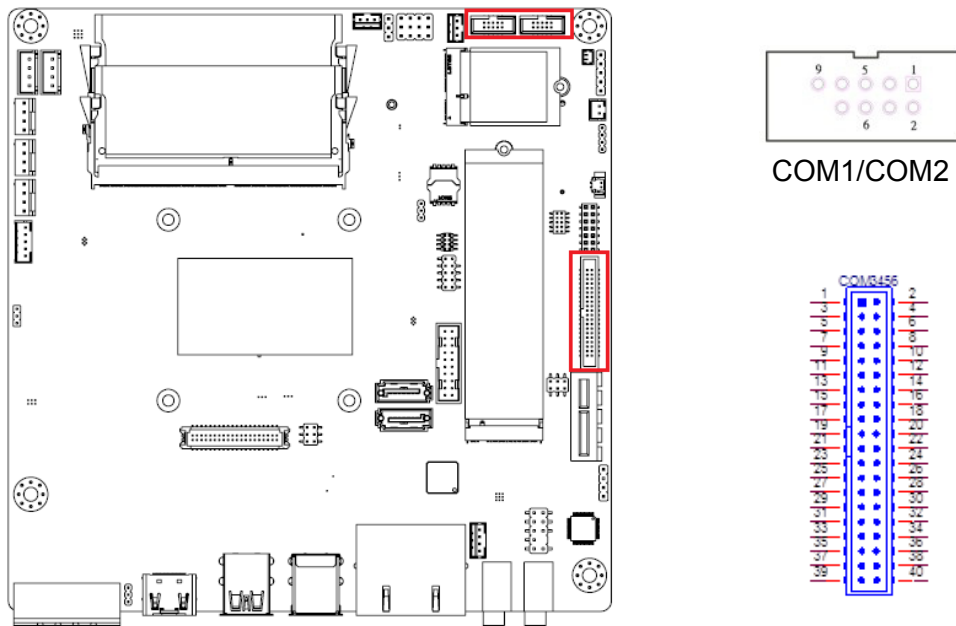
HDMI1



**USB04
(Type C Alt)**

The AIMB-233 includes HDMI and Type C alt (USB04) interface that can drive conventional HDMI and DP displays.

2.4 Serial Ports (COM1,COM2,COM3456)



AIMB-233 supports six serial ports, include 5 x RS-232, 1 x RS-232/422/485 (supports hardware auto flow control).

It support RS-232 in COM4 via 5V/12V jumper selecting.

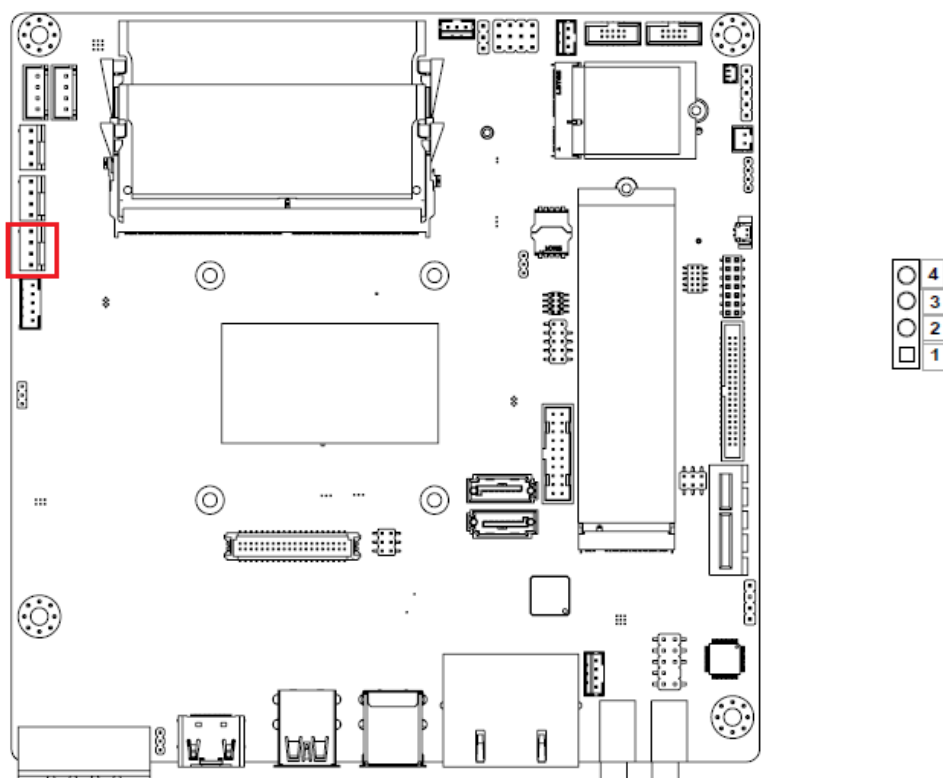
These ports can connect to serial devices, such as a mouse or a printer, or to a communications network.

The IRQ and address ranges for both ports are fixed. However, if you want to disable the port or change these parameters later, you can do this in the system BIOS setup.

Different devices implement the RS-233 standards in different ways. If you have problems with a serial device, be sure to check the pin assignments for the connector.

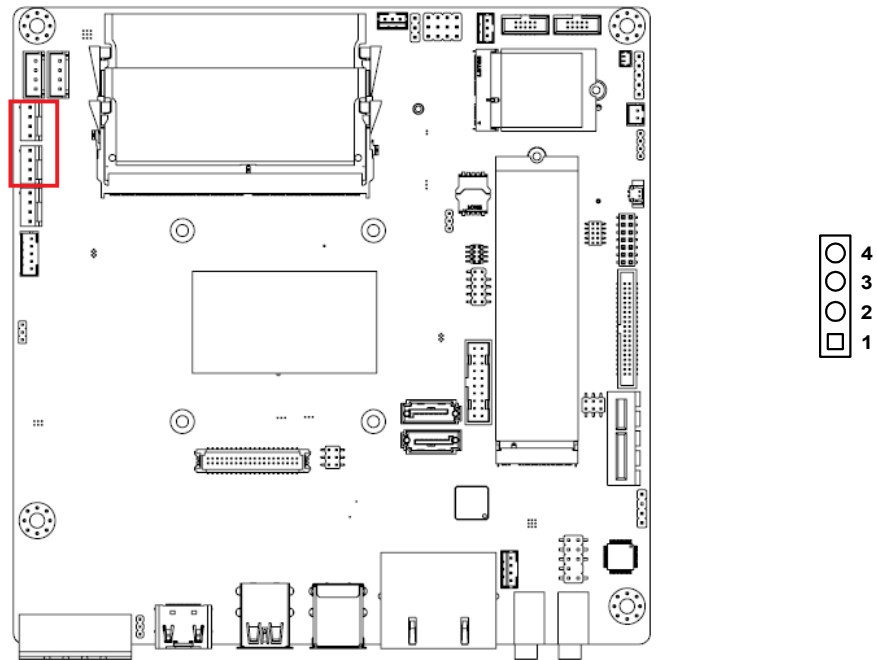
Serial Ports Voltage Select

2.5 CPU Fan Connector (CPU_FAN1)



If a fan is used, this connector supports cooling fans of 500 mA (6 W) or less.

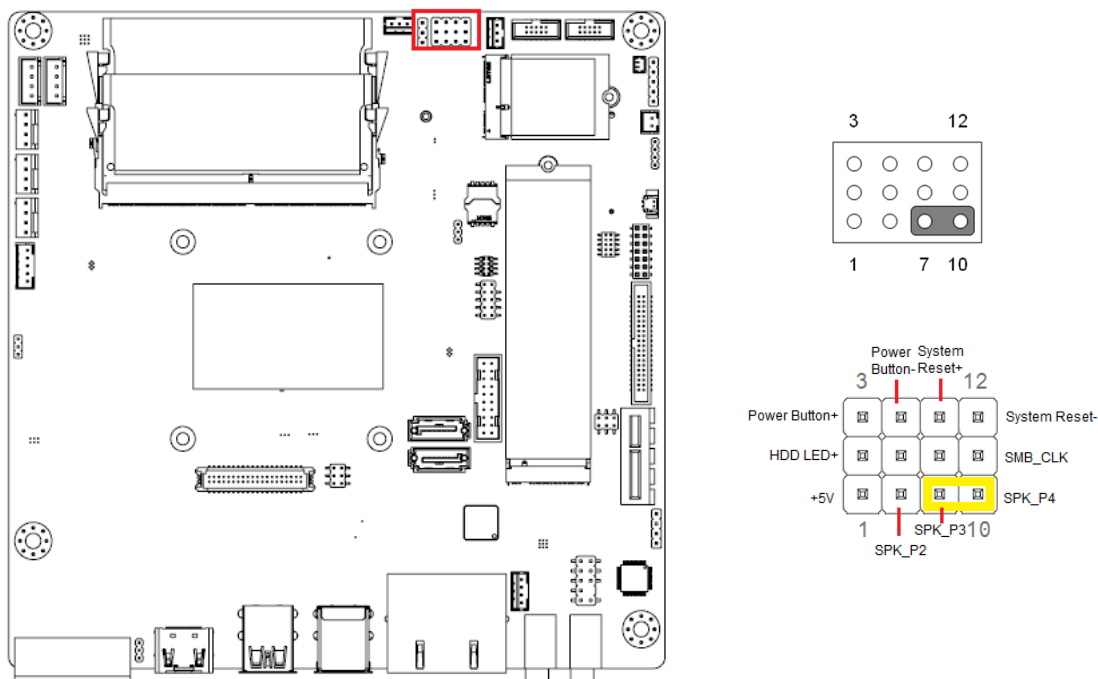
2.6 System FAN Connector (SYSFAN1, SYSFAN2)



If a fan is used, this connector supports cooling fans of 500 mA (6 W) or less.

2.7 Front Panel Connectors (JFP1,JFP2)

There are several headers for monitoring and controlling the AIMB-233.



2.7.1 ATX soft power switch ((JFP1/PWR_BTN))

If your computer case is equipped with an ATX power supply, you should connect the power on/off button on your computer case to ((JFP1/ PWR_BTN)), for convenient power on and off.

2.7.2 Reset (JFP1/RST_BTN)

Many computer cases offer the convenience of a reset button. Connect the wire for the reset button.

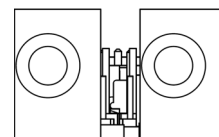
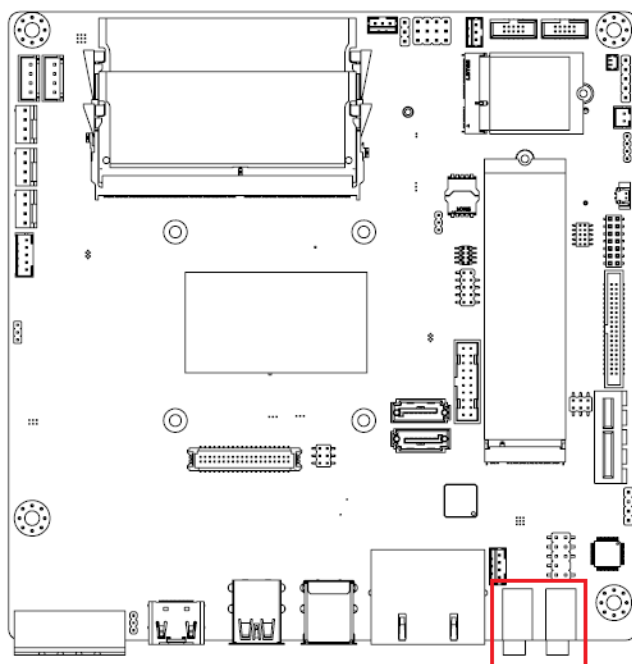
2.7.3 HDD LED (JFP2/HDD_LED)

You can connect an LED to connector (JFP2/HDDLED) to indicate when the HDD is active.

2.7.4 External speaker (JFP2/SPEAKER)

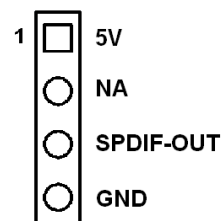
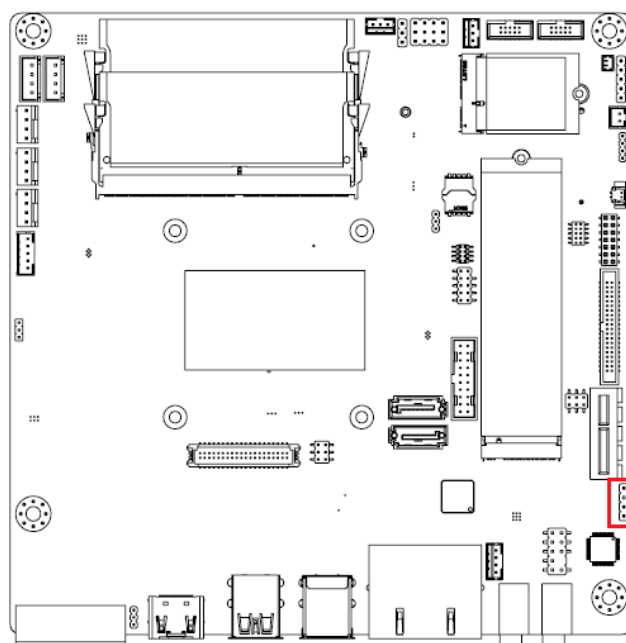
JFP2/Speaker is a 4-pin connector for an external speaker. If there is no external speaker, the AIMB-233 provides an onboard buzzer as an alternative. To enable the buzzer, set pins 7 & 10 as closed.

2.8 Line Out, Mic In Connector (AUDIO1/AUDIO2)

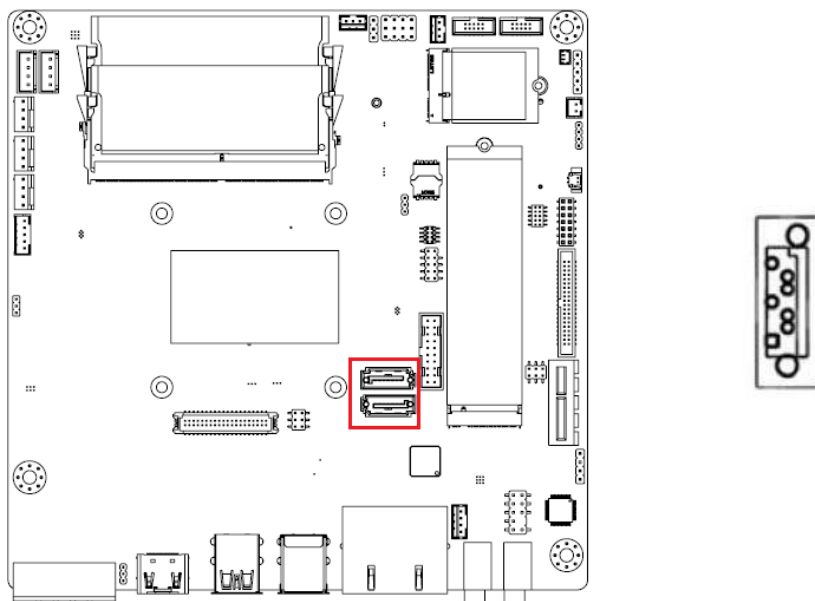


Line Out Mic In

2.9 Digital Audio Connector (SPDIF1)

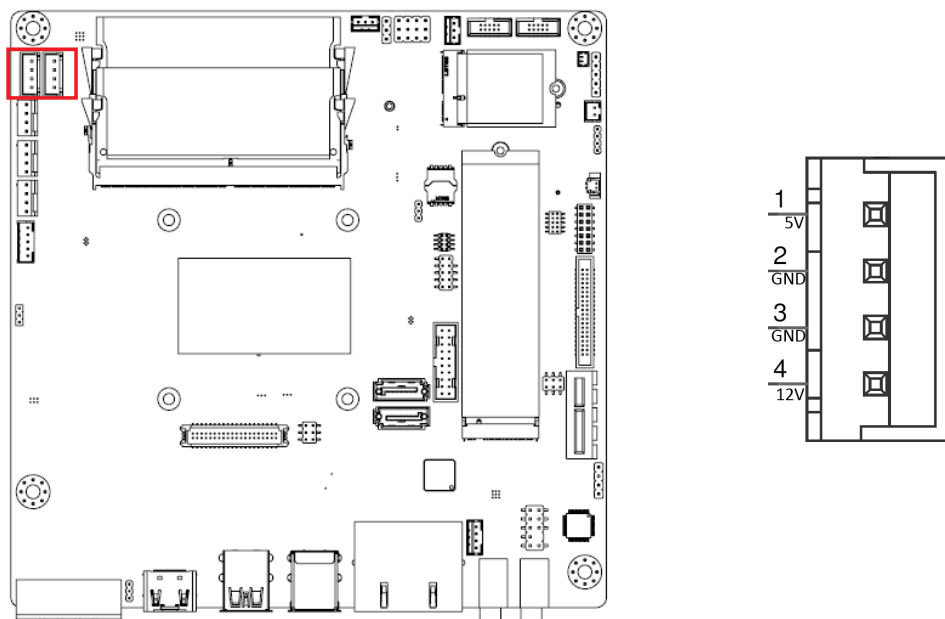


2.10 Serial ATA Interface (SATA1~SATA2/ SATAPWR1 ~ SATAPWR2)

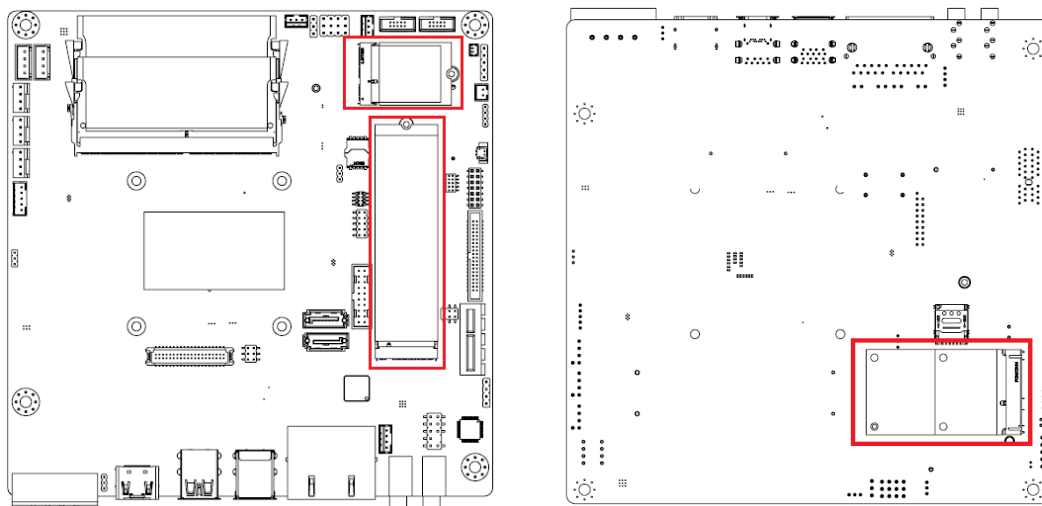


AIMB-233 features a high performance Serial ATA interface (up to 300 MB/s) and Serial ATA III interface (up to 600 MB/s) which eases hard drive cabling with thin, space-saving cables.

2.11 SATA power connector(SATA_PWR1~2)



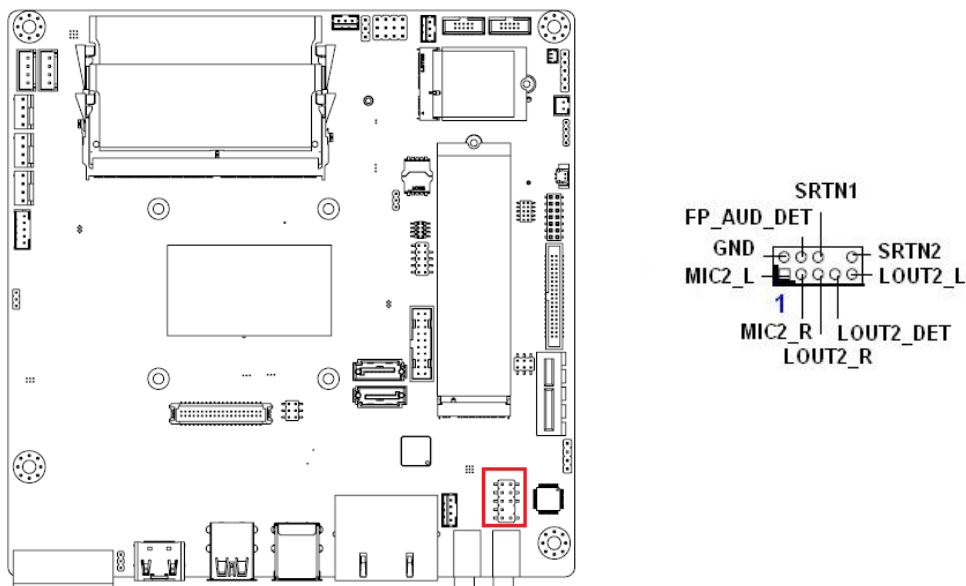
2.12 M.2 M key & E key connector



The AIMB-233 provides 1 M.2 M key_2242 & 2280, 1 M.2 E key_2230 (colay with full size miniPCIe, back side)

2.13 Front Headphone Connector (FPAUD1)

This connector is for a chassis-mounted front panel audio I/O module that supports either HD Audio or legacy AC'97 (optional) audio standard. Connect this connector with the front panel audio I/O module cable.

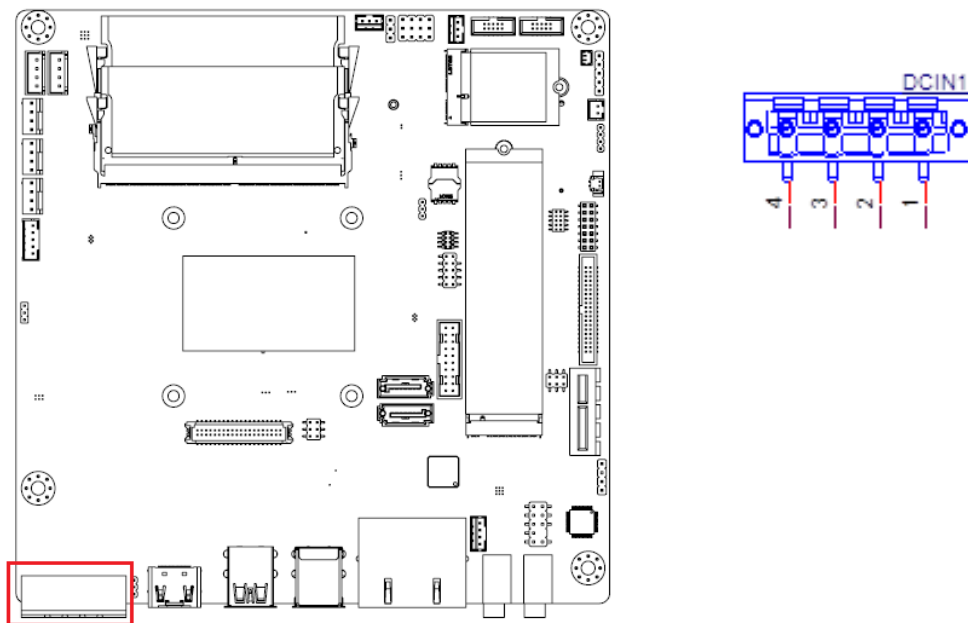


Note! *For motherboards with the optional HD Audio feature, we recommend that you connect a high-definition front panel audio module to this connector to take advantage of the motherboard's high definition audio capability.*

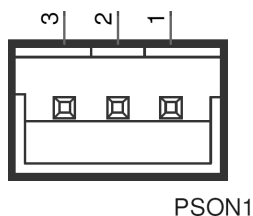


2.14 Input Power Connector (DCIN1)

The phoenix connector for 12~24V DCIN. The plugs from the power supply are designed to fit these connectors in only one direction. Determine the proper orientation and push down firmly until the connectors mate completely.



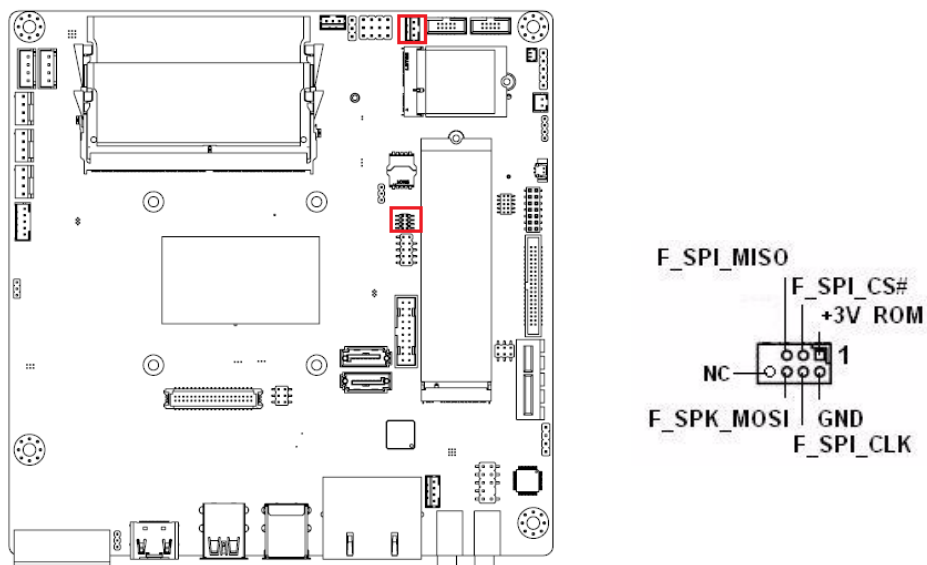
PSON1 is for AT/ATX mode selection(PSON1)



PSON1:1-2	AT*
2-3	ATX

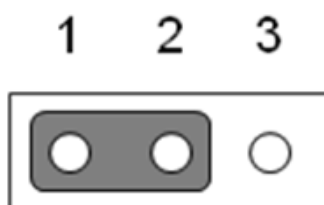
2.15 SPI Flash Connector(SPI_CN1)

The SPI flash card pin header may be used to flash BIOS if the AIMB-233 cannot power on.



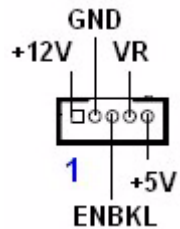
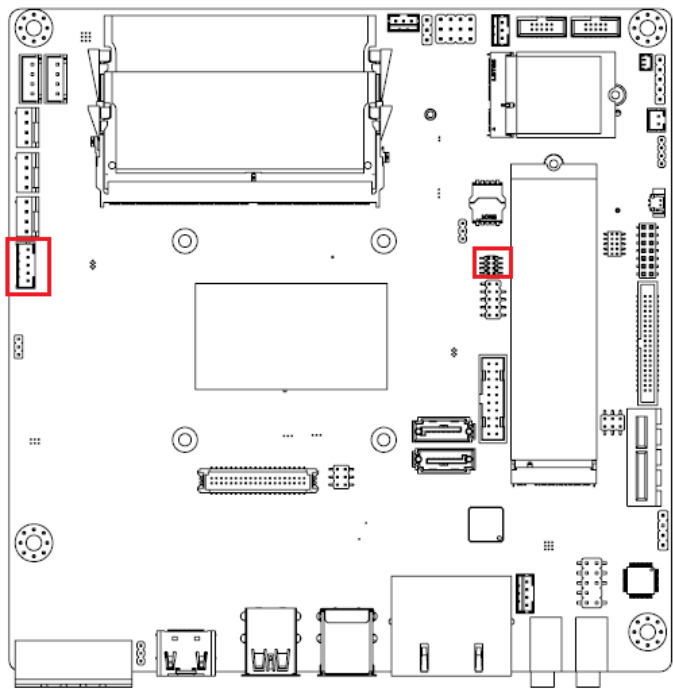
Clean CMOS(JCMOS1)

Clean CMOS and reset RTC



CMOS1: 1-2 Keep CMOS data*
3-4 Clean CMOS

2.17 LCD Inverter Connector (INV1)



Note!



■ **Signal Description**

Signal

VR

ENBKl

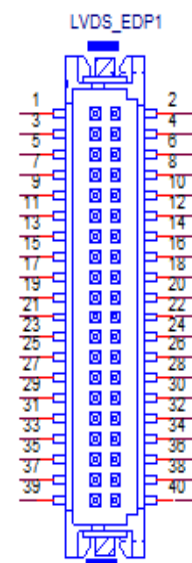
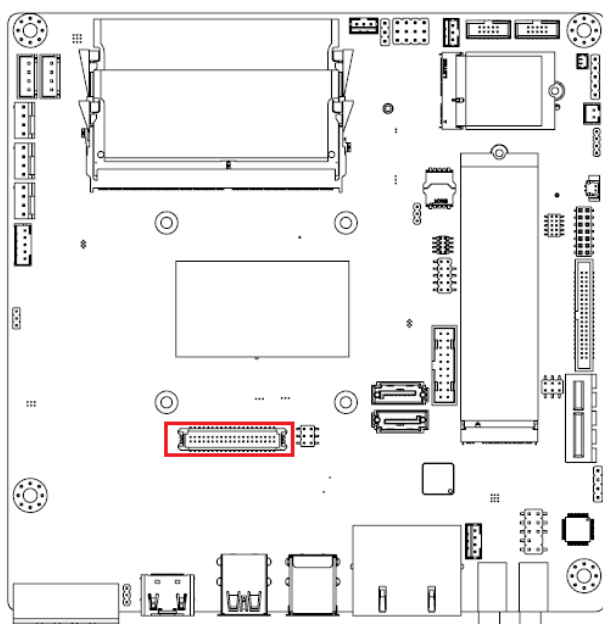
Signal Description

$V_{adj}=0.75\text{ V}$

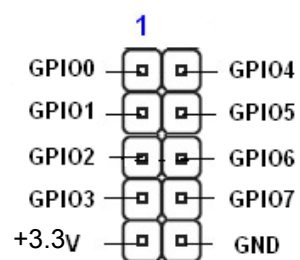
(Recommended: $4.7\text{ K}\Omega$, $>1/16\text{ W}$)

LCD backlight ON/OFF control signal

2.18 LVDS Connector (LVDS_EDP1)



2.19 General Purpose I/O Connector (GPIO1)



Chapter 3

BIOS Operation

3.1 Introduction

With the AMI BIOS Setup program, you can modify the BIOS settings and control the special features of your computer. The Setup program uses a number of menus for making changes and turning special features on or off. This chapter describes the basic navigation of the AIMB-233 setup screens.

3.2 BIOS Setup

The AIMB-233 Series system has AMI BIOS built in, with a CMOS SETUP utility that allows users to configure required settings or to activate certain system features.

The CMOS SETUP saves the configuration in the CMOS RAM of the motherboard. When the power is turned off, the battery on the board supplies the necessary power to preserve the CMOS RAM.

When the power is turned on, press the button during the BIOS POST (Power-On Self Test) to access the CMOS SETUP screen.

Control Keys

< ↑ >< ↓ >< ← >< → >	Move to select item
----------------------	---------------------

<Enter>	Select Item
---------	-------------

<Esc>	Main Menu - Quit and not save changes into CMOS Sub Menu - Exit current page and return to Main Menu
-------	---

<Page Up/+>	Increase the numeric value or make changes
-------------	--

<Page Down/->	Decrease the numeric value or make changes
---------------	--

<F1>	General help, for Setup Sub Menu
------	----------------------------------

<F2>	Item Help
------	-----------

<F5>	Load Previous Values
------	----------------------

<F7>	Load Setup Defaults
------	---------------------

<F10>	Save all CMOS changes
-------	-----------------------

3.2.1 Main Menu

Press to enter AMI BIOS CMOS Setup Utility, the Main Menu will appear on the screen. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.



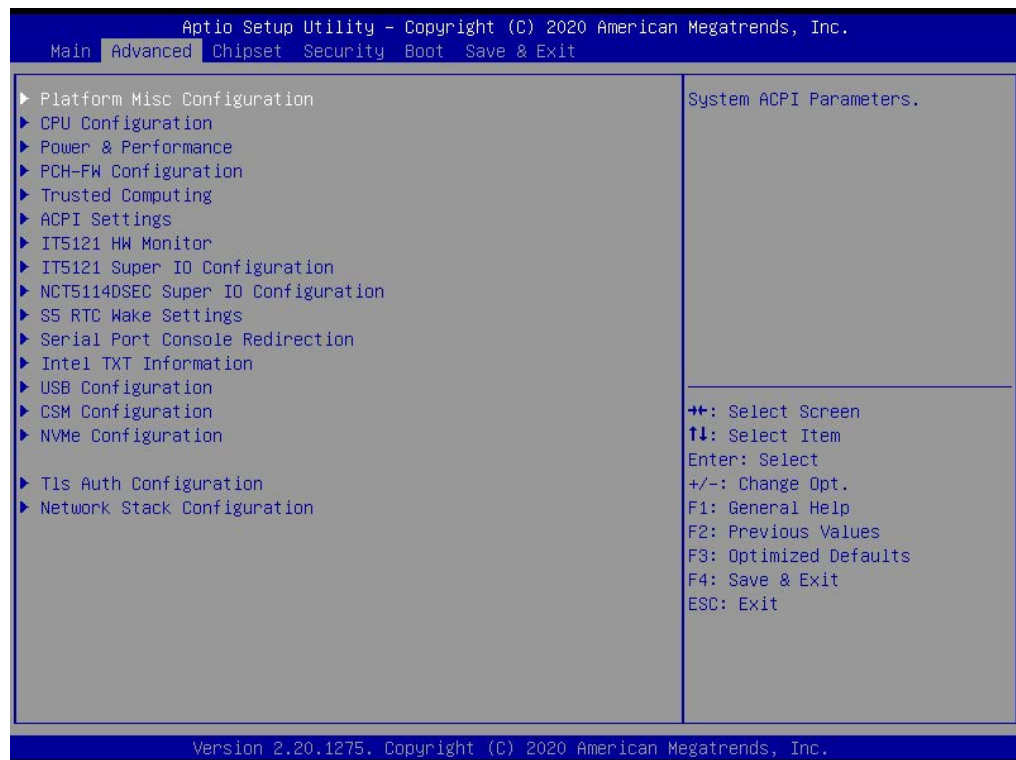
The Main BIOS setup screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured; options in blue can. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

3.2.1.1 System time / System date

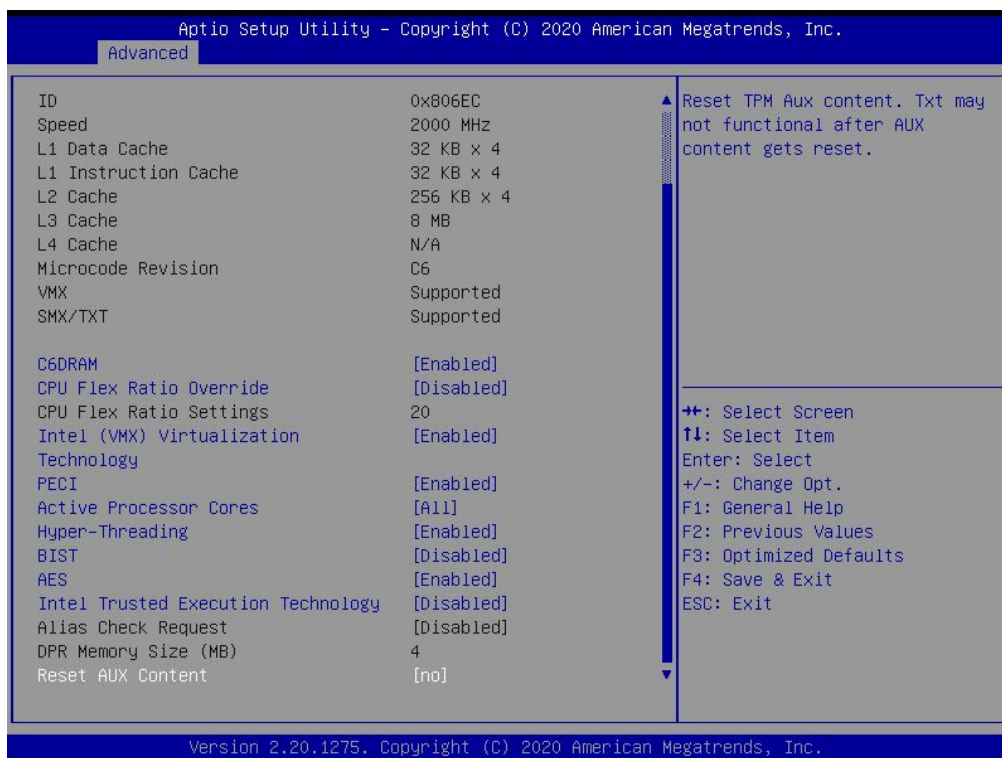
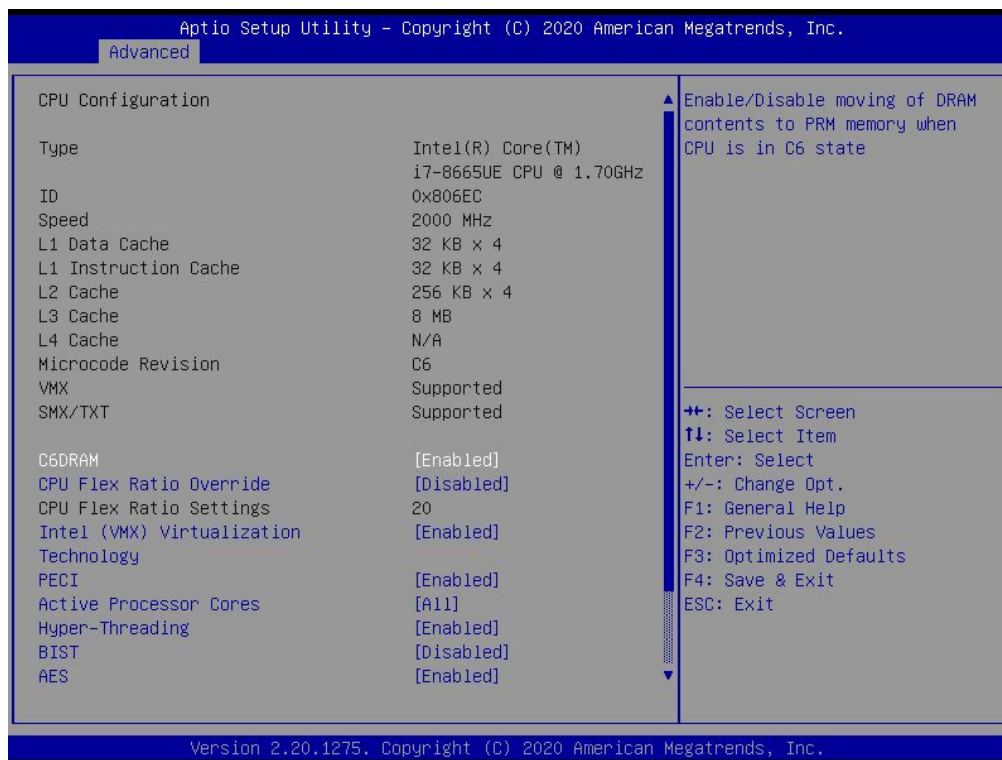
Use this option to change the system time and date. Highlight System Time or System Date using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in MM/DD/YY format. The time must be entered in HH:MM:SS format.

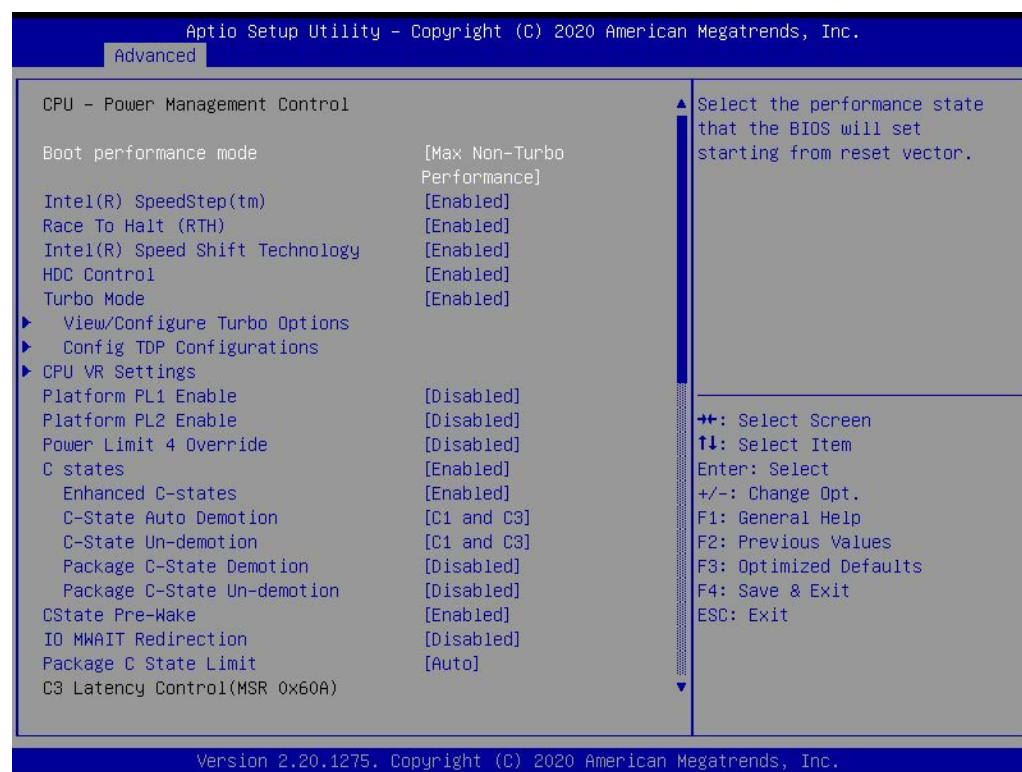
3.2.2 Advanced BIOS Features

Select the Advanced tab from the AIMB-233 setup screen to enter the Advanced BIOS Setup screen. You can select any of the items in the left frame of the screen, such as CPU Configuration, to go to the sub menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced BIOS Setup options are described in this section. The Advanced BIOS Setup screen is shown below. The sub menus are described on the following pages.



3.2.2.1 CPU Configuration



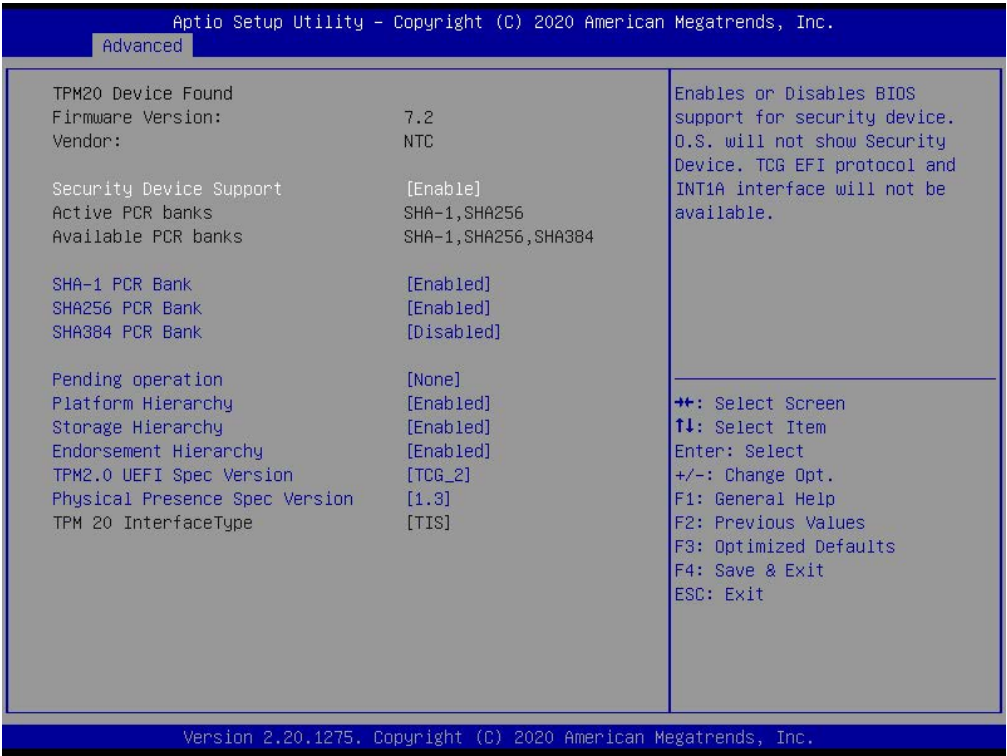


3.2.2.2 ACPI settings



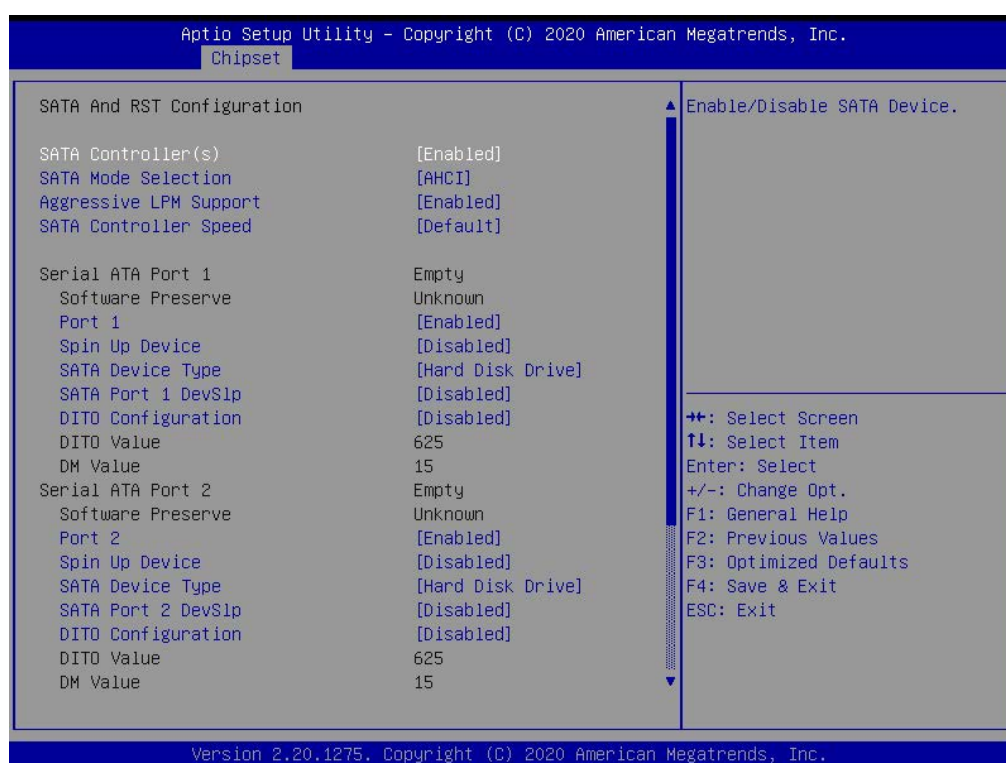
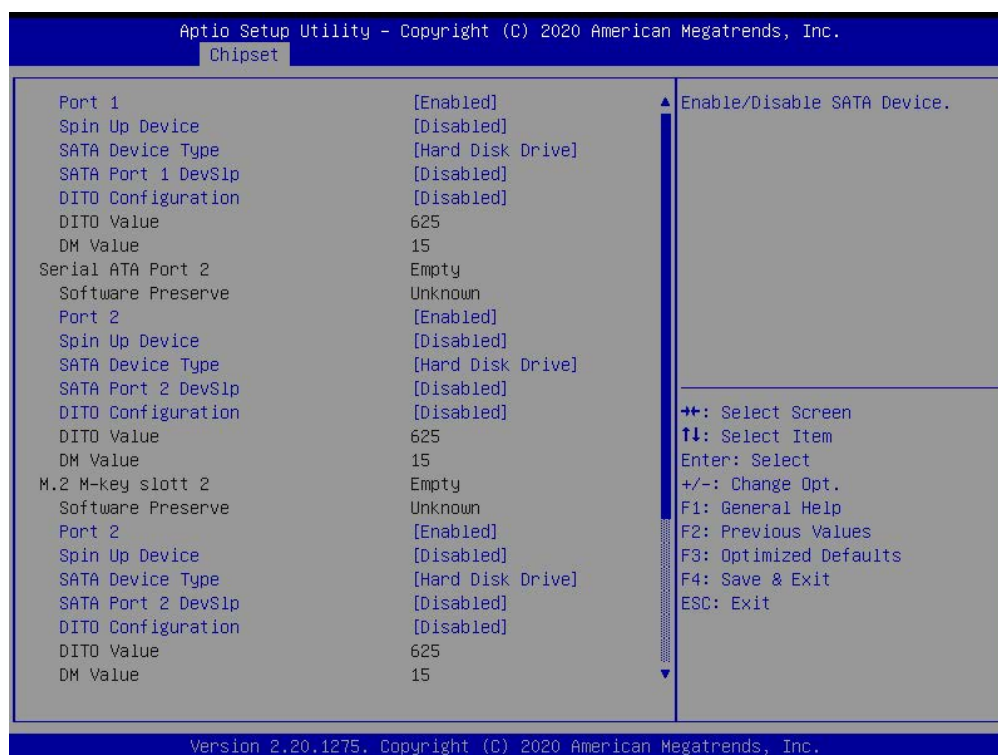
- **Enable ACPI Auto Configuration**
Enable or disable BIOS ACPI Auto Configuration
- **Enable Hibernation**
This item allows users to enable or disable hibernation
- **ACPI Sleep state**
This item allows users to set the ACPI sleep state
- **Lock Legacy Resources**
This item allows users to lock legacy devices' resources.
- **S3 Video Repost**
Enable or disable video repost
- **Power on by Modem**
Disable/Enable power on modem function

3.2.2.3 Trusted Computing



- **Security Device Support**
Enable or disable BIOS support for security device.

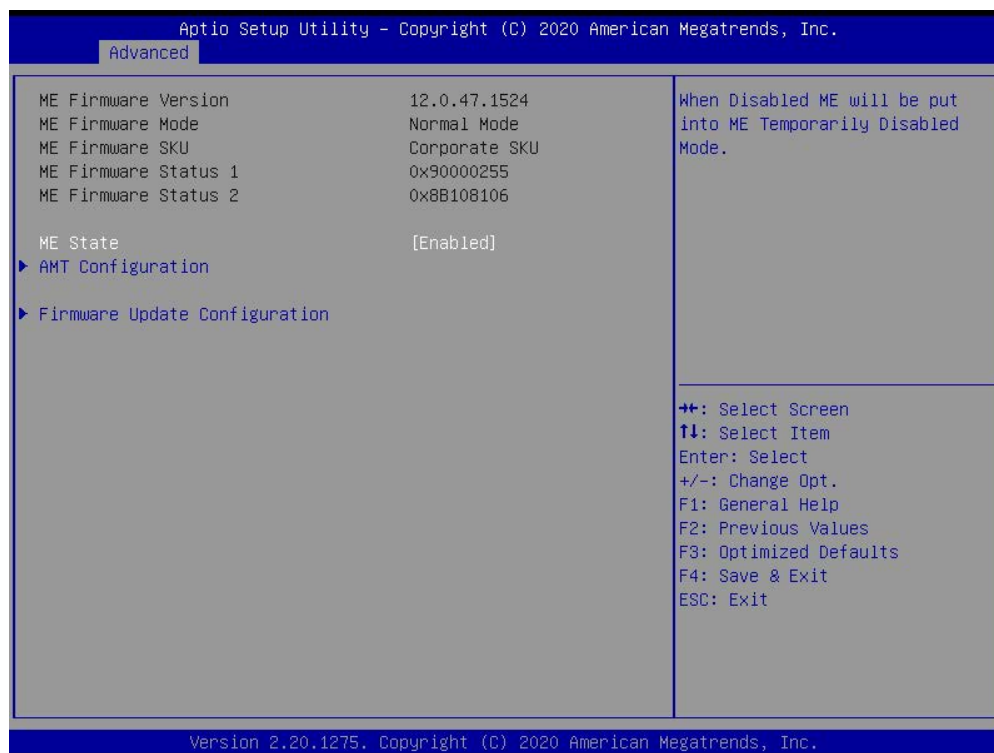
3.2.2.4 SATA configuration



- **SATA Controller(s)**
This item allows users to enable or disable the SATA controller(s).
- **SATA Mode Selection**
This item allows users to select mode of SATA controller(s).

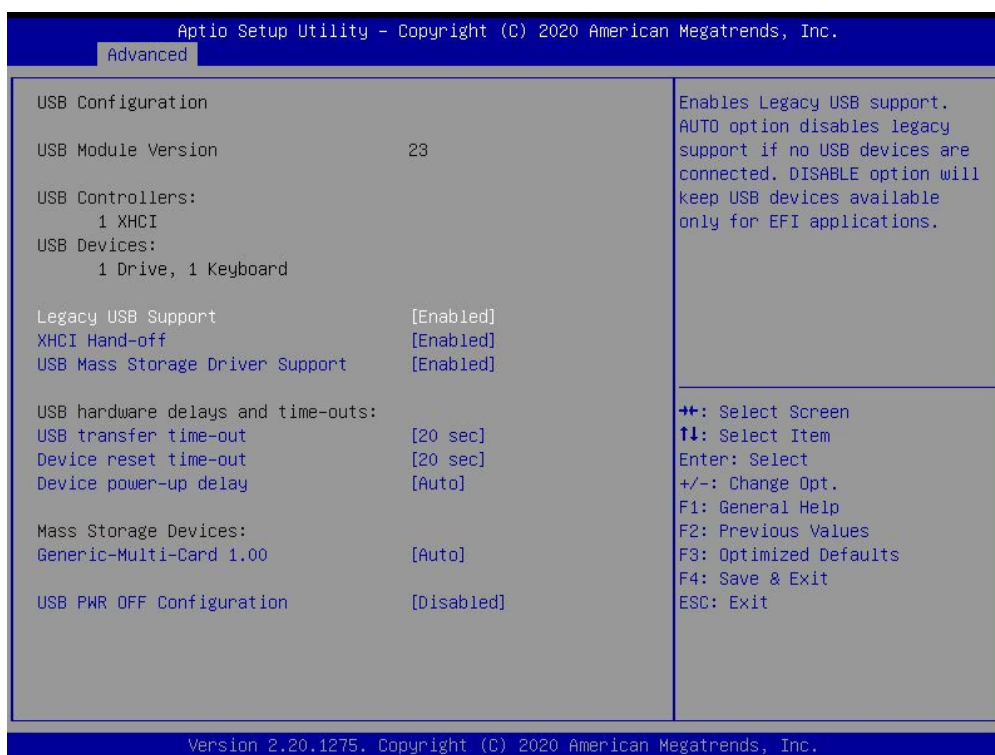
- **Aggressive LPM Support**
This item allows users to enable or disable the Aggressive LPM Support.
- **SATA Device type**
This item allows users to select mode of SATA Device type.
- **Spin up Device**
This item allows users to enable or disable the Spin up Device.
- **Device Sleep**
This item allows users to enable or disable the Device Sleep.
- **SATA DEVSLEP idle Timeout Config**
This item allows users to enable or disable the SATA DEVSLEP idle Timeout Config.
- **Serial ATA Port 1/2**
This item allows users to enable or disable the SATA Port.

3.2.2.5 PCH (FW) Configuration



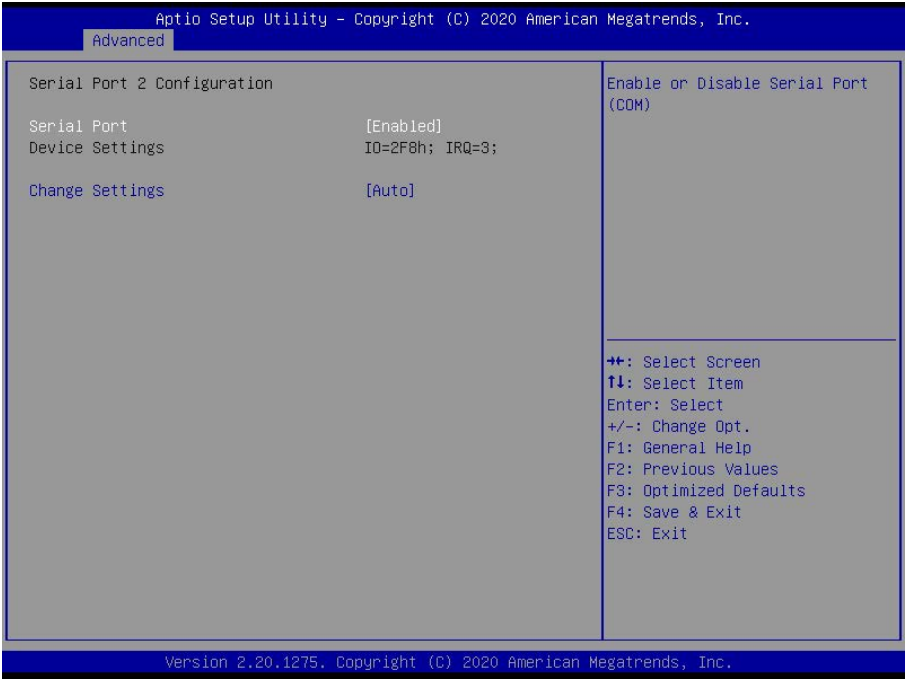
- **Me FW Image Re-Flash**
This item allows users to enable or disable Me FW image re-flash function.

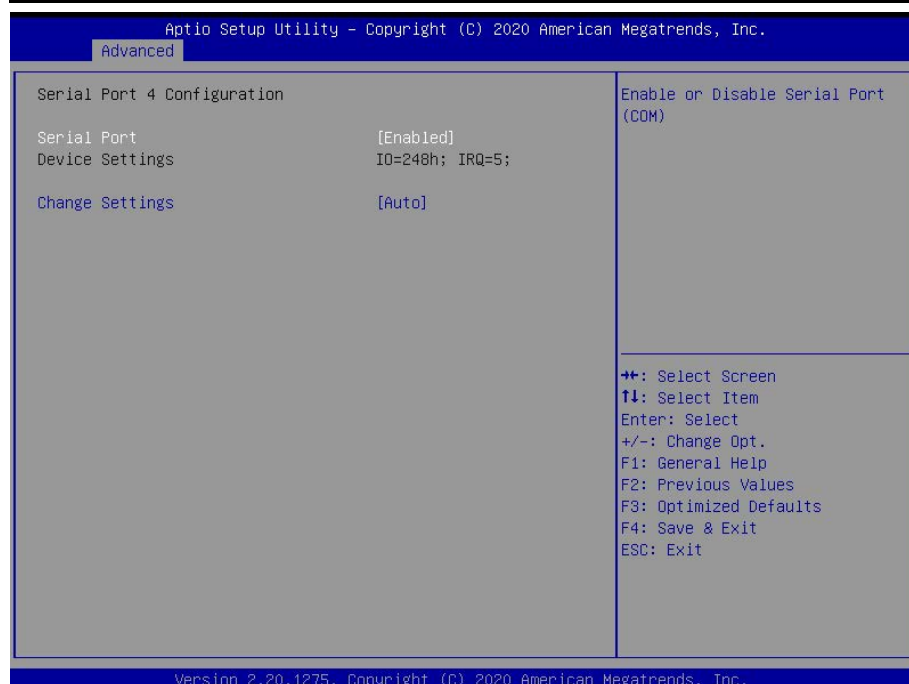
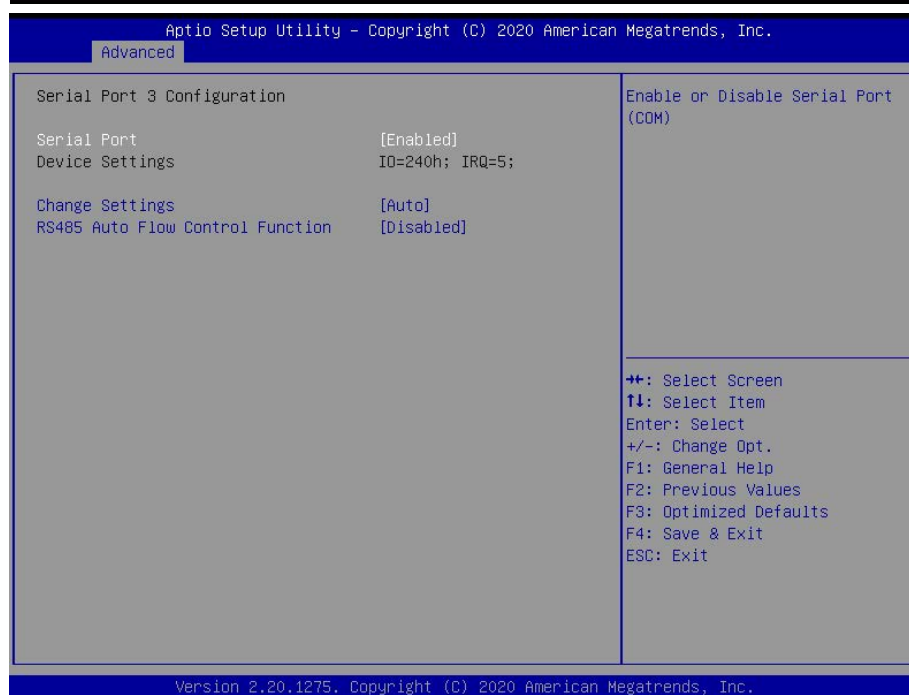
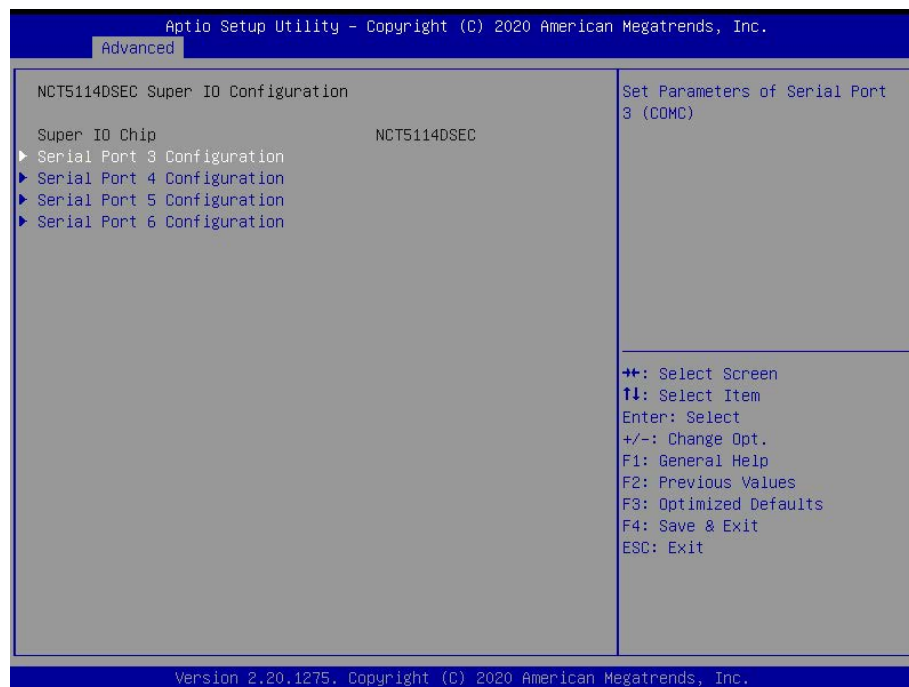
3.2.2.6 USB Configuration

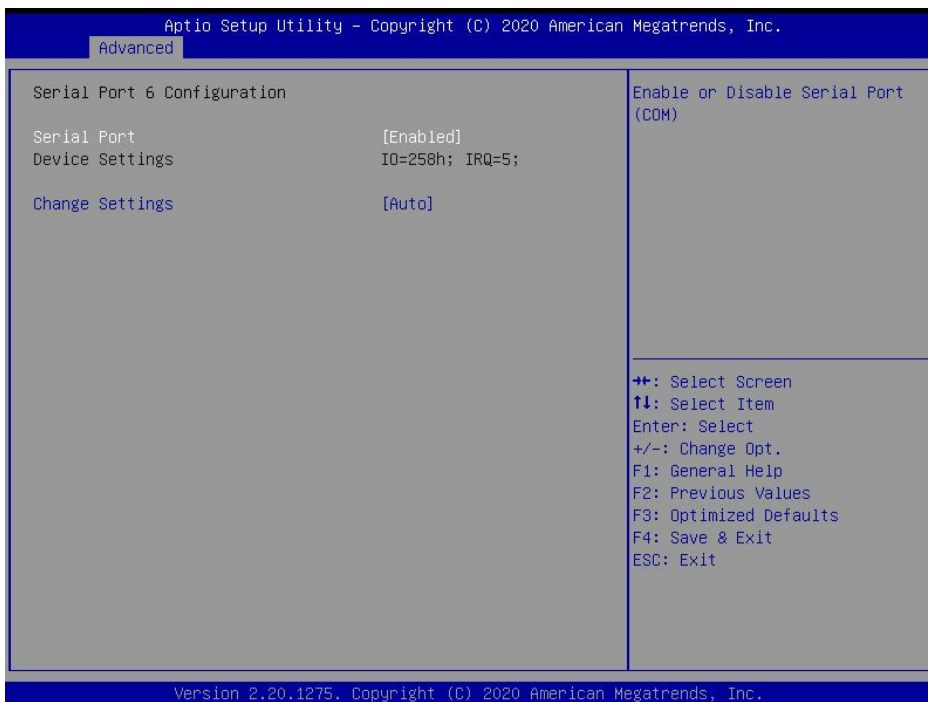
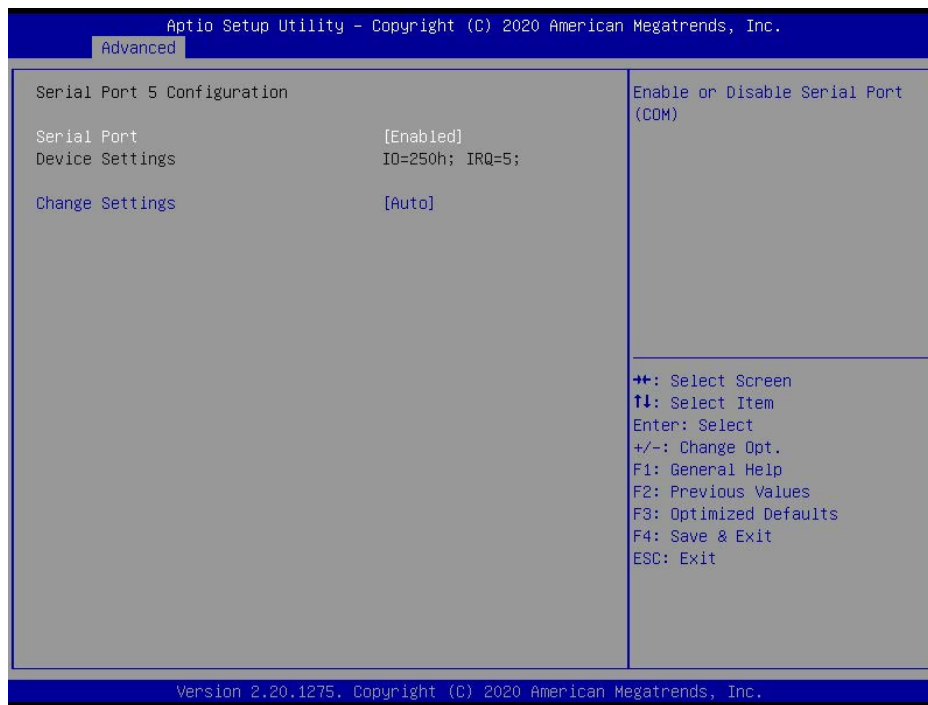


- **Legacy USB support**
Enables support for legacy USB. Auto option disables legacy support if no USB devices are connected.
- **XHCI Hand-off**
This is a workaround for OS without XHCI hand-off support. The XHCI ownership change should claim by XHCI driver.
- **USB transfer time-out**
This item allows users to set the USB transfer time-out
- **Device reset time-out**
This item allows users to set the Device reset time-out
- **Device power-up delay**
This item allows users to set the Device power-up delay

3.2.2.7 Serial port Configuration

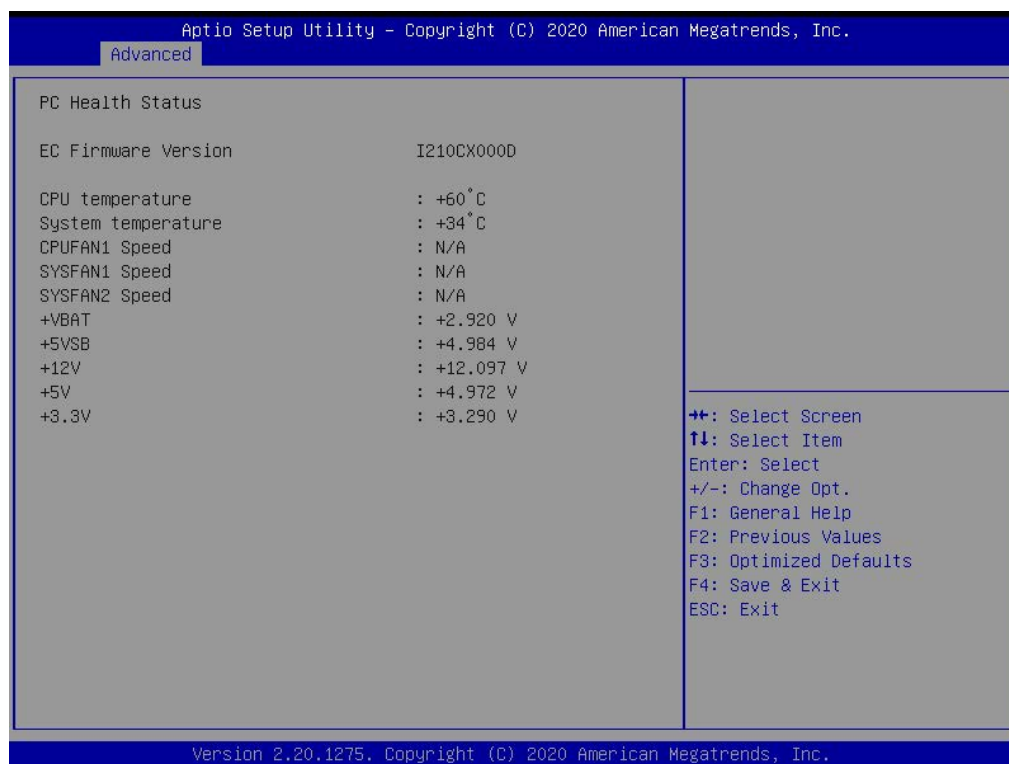




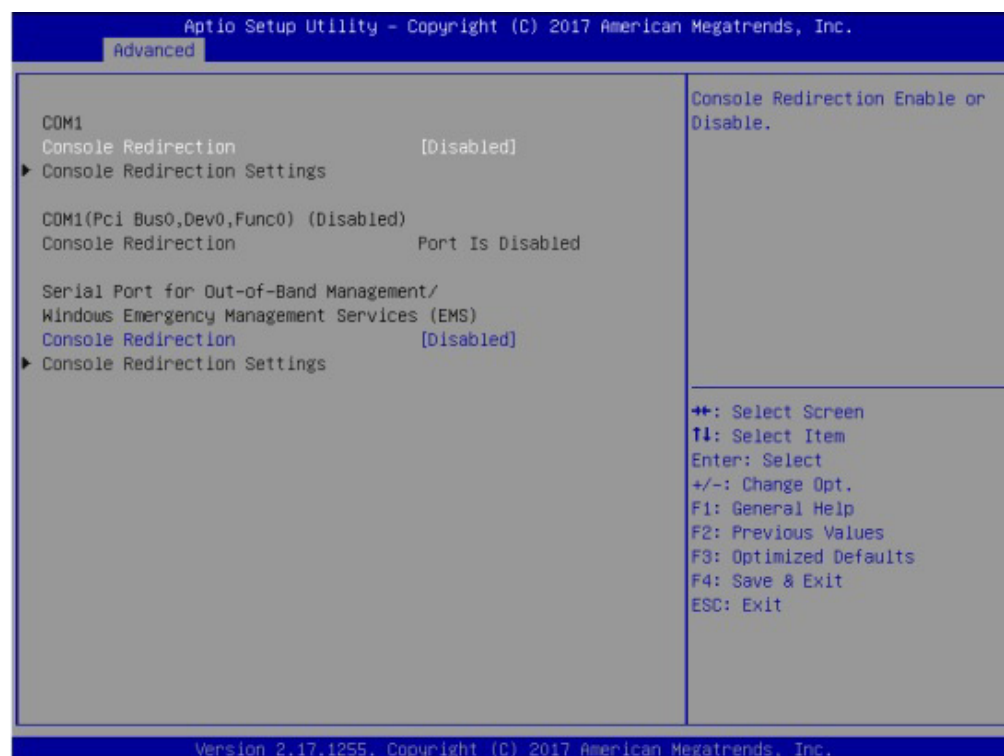


- **Serial Port**
This item will allow users to enable or disable serial port.
- **Change Settings**
This item allows users to change the serial port setting.
- **Device Mode**
This item allows users to change the device mode.

3.2.2.8 IT5121 HW Monitor



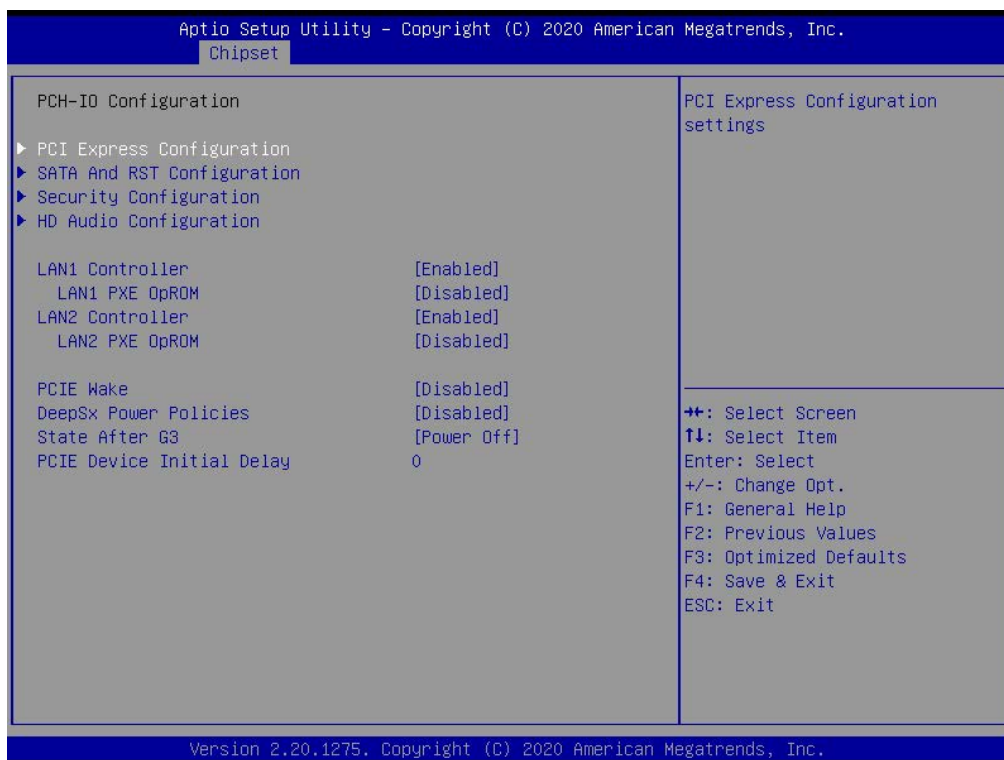
3.2.2.9 Serial Port Console Redirection



■ Console Redirection

This item allows users to enable or disable console redirection for Microsoft Windows Emergency Management Services (EMS).

3.2.3 Chipset



■ PCI Express Configuration

Details of PCI Express items.

■ USB Configuration

Details of USB items.

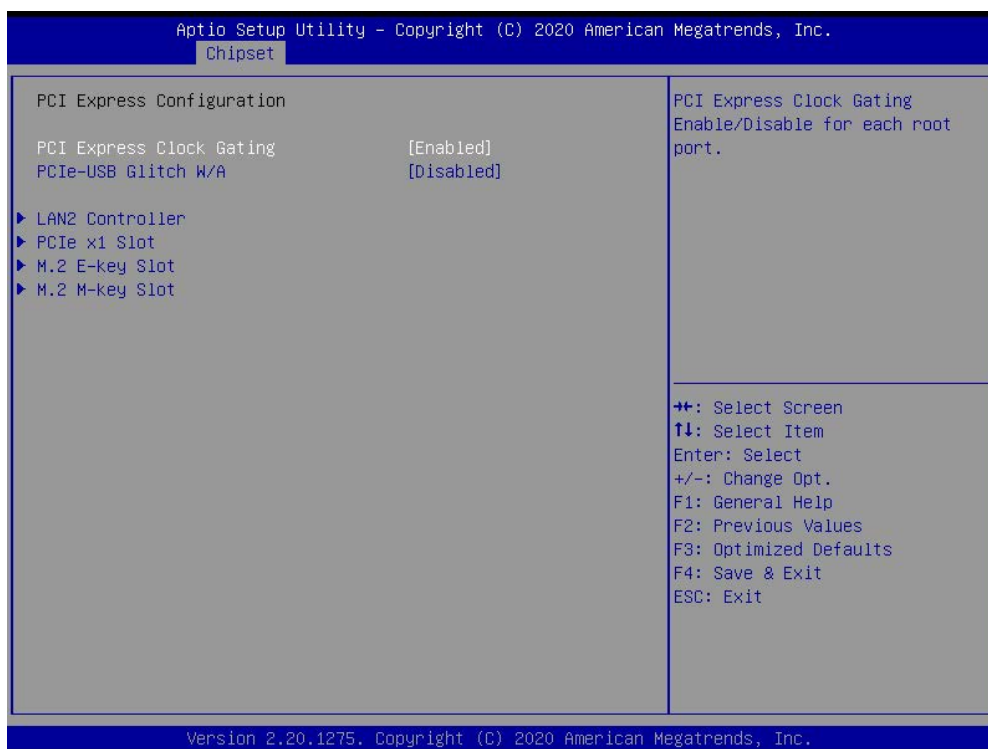
■ LAN controller

Enables or disables the LAN1/2 controller.

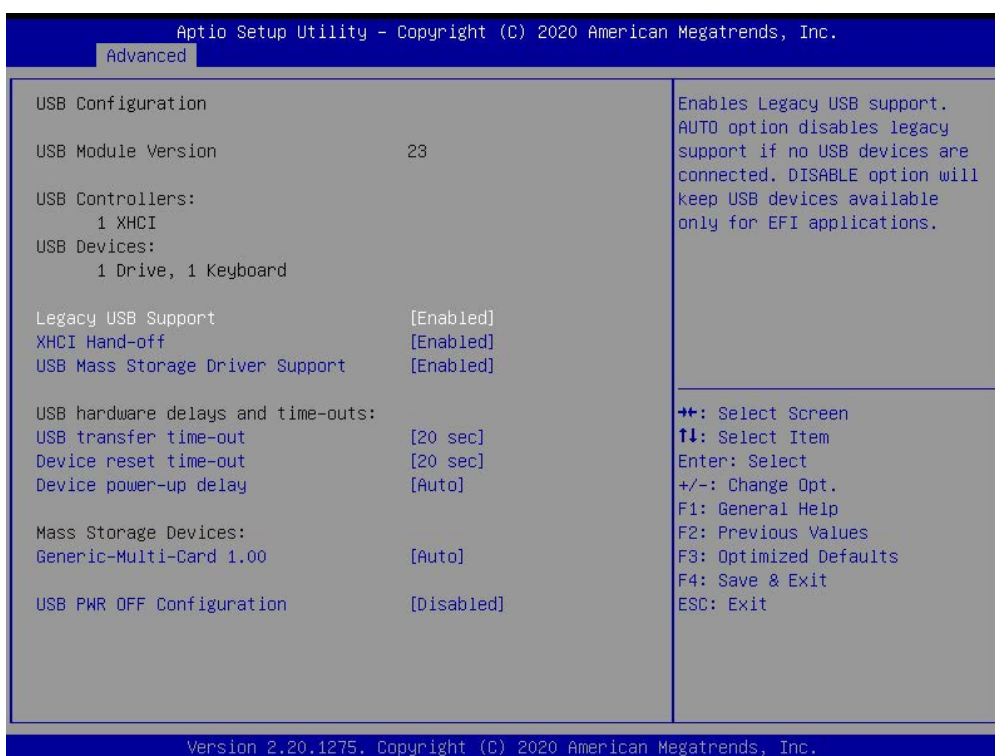
■ PCIE Wake

Enables or disables LAN1/2 wake up from sleep state.

3.2.3.1 PCI Express Configuration



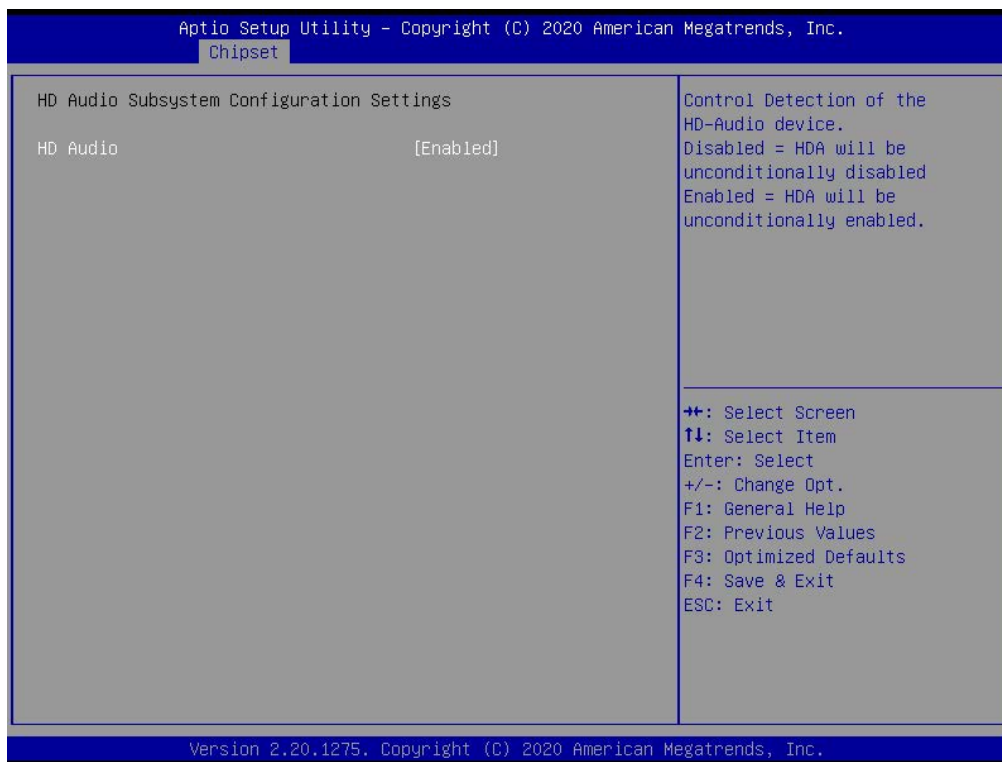
3.2.3.2 USB Configuration



■ USB Precondition

This item allows user to enable or disable USB pre-condition.

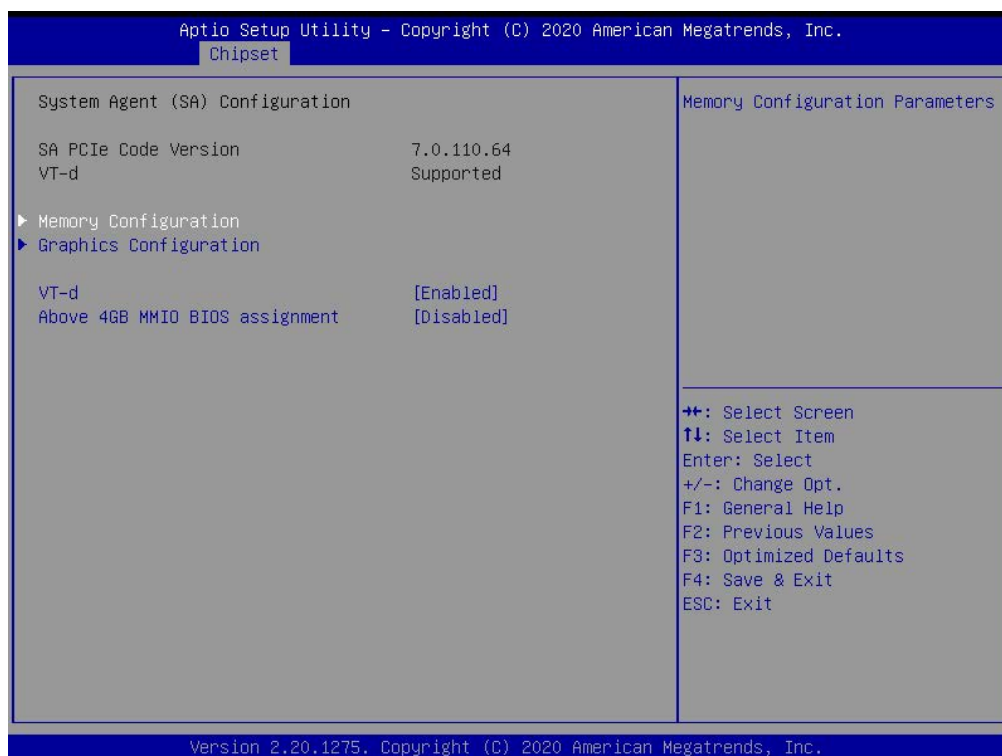
3.2.3.3 HD Audio Configuration



■ HD Audio

This item allows user to enable or disable azalea device.

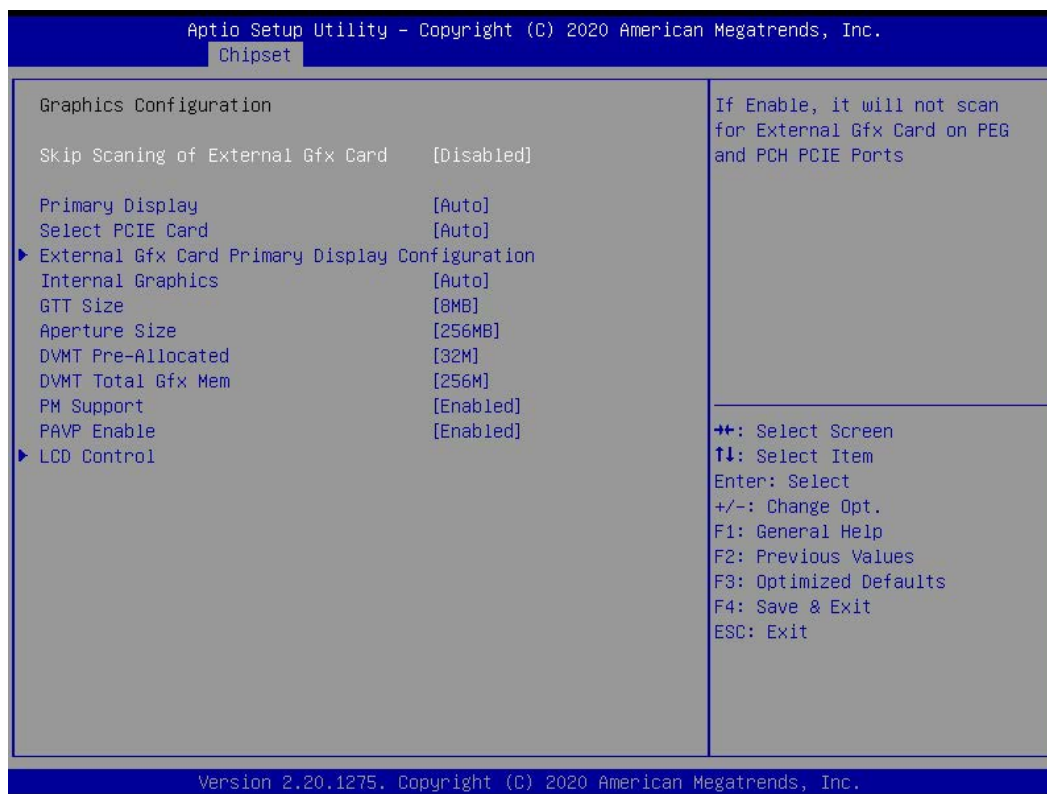
3.2.3.4 System Agent (SA) Configuration



■ VT-d

This item allows users to enable or disable VT-d.

3.2.3.5 Graphic Configuration



■ Graphics Turbo IMON Current

This item allows users to select which Graphics Turbo IMON Current.

■ Internal Graphics

This item allows users to enable or disable IGD.

■ Aperture Size

This item allows users to select aperture size.

■ DVMT Pre-Allocated

This item allows users to select DVMT pre-allocated memory size.

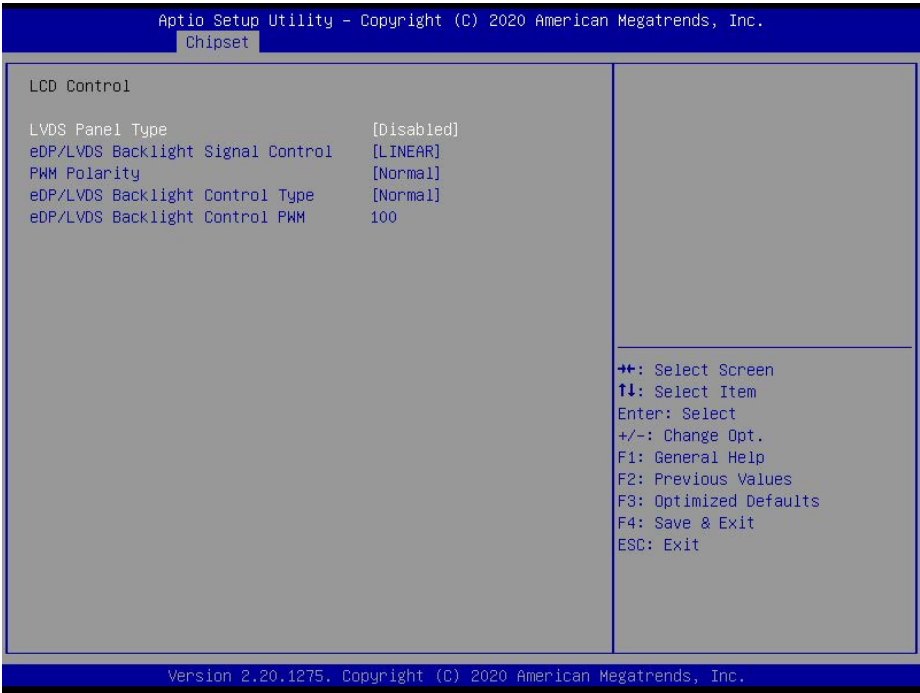
■ DVMT Total Gfx Mem

This item allows users to select DVMT total memory size.

■ Gfx Low Power Mode

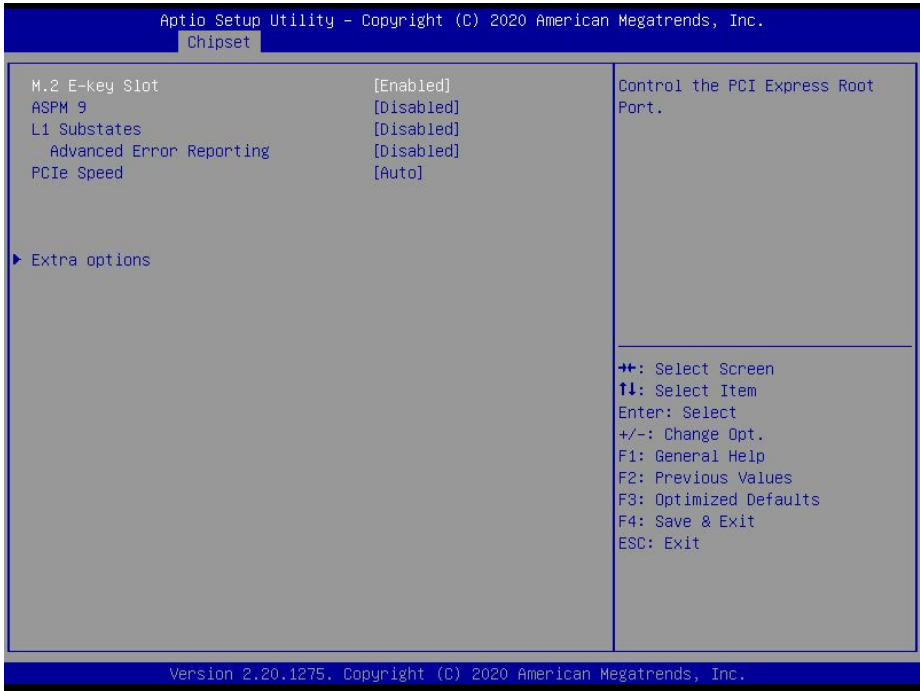
This item allows users to enable or disable IGD low power mode.

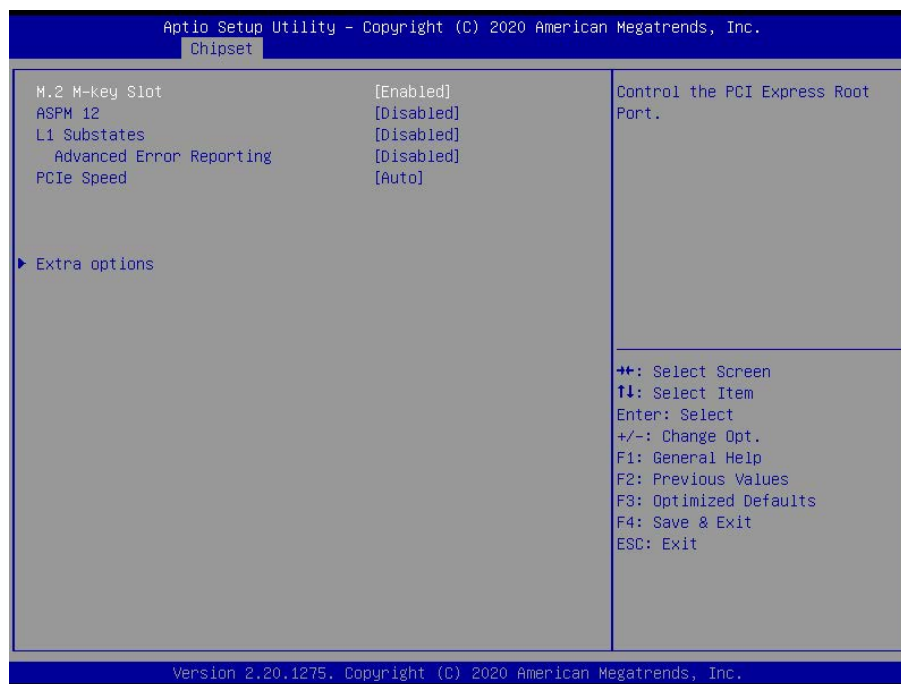
3.2.3.6 LCD Control



- **LVDS Panel Type**
Select LVDS Panel type.

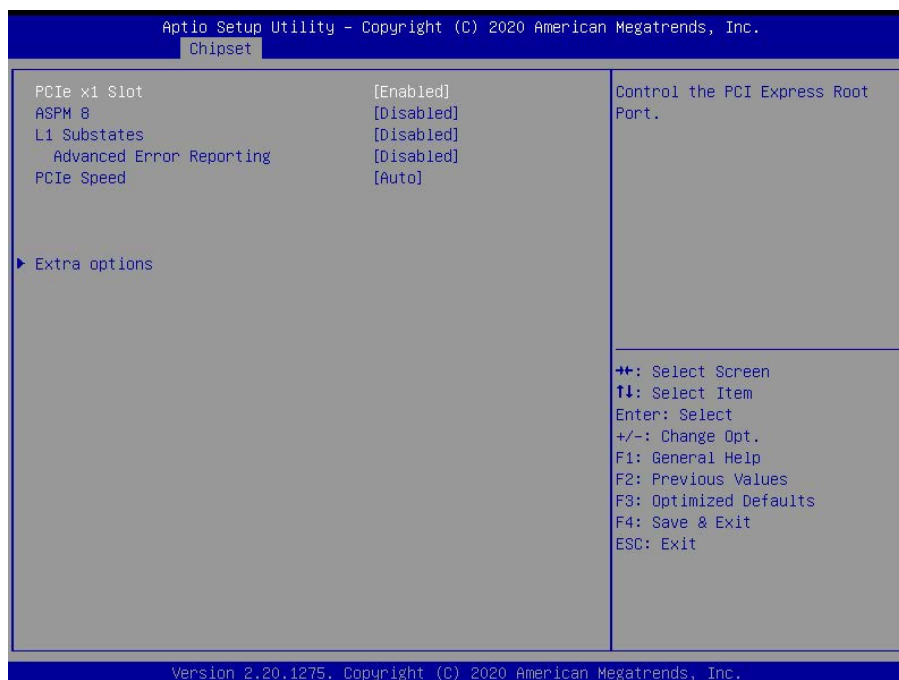
3.2.3.7 M.2 configuration





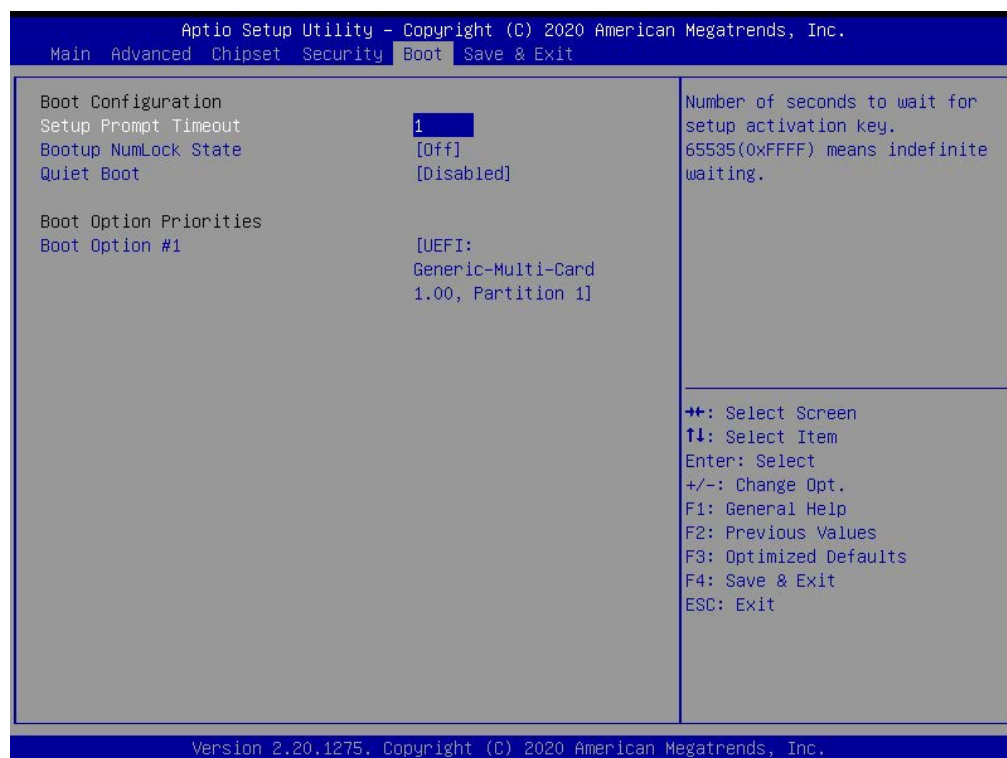
- **M.2 [Enabled]**
Enable or disable PCI Express Root Port controller.
- **ASPM Support [Auto]**
- **L1 Substates [L1.1 & L1.2]**
- **PCIe Speed [Auto]**

3.2.3.7 PCIe configuration



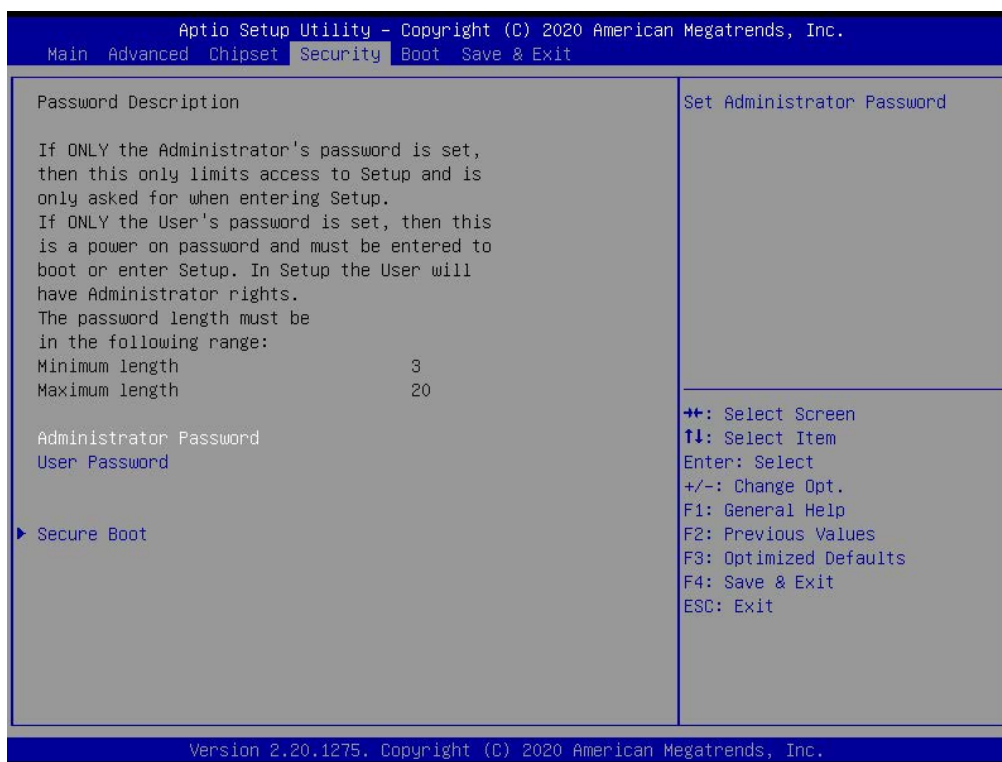
- **PCIEx1 [Enabled]**
Enable or disable PCI Express Root Port controller.
- **ASPM Support [Auto]**
- **L1 Substates [L1.1 & L1.2]**
- **PCIe Speed [Auto]**

3.2.4 Boot



- **Setup Prompt Timeout**
This item allows you to change number of seconds to wait for setup activation key.
- **Bootup NumLock State**
Select the Power-on state for Numlock.
- **Quiet Boot**
If this option is set to Disabled, the BIOS display normal POST messages. If Enabled, an OEM Logo is shown instead of POST messages.
- **Boot Option Priorities**
Set the system boot order.

3.2.5 Security



Select Security Setup from the AIMB-233 Setup main BIOS setup menu. All Security Setup options, such as password protection and virus protection are described in this section. To access the sub menu for the following items, select the item and press<Enter>: Change Administrator / User Password.

3.2.6 Save & Exit



- **Save Changes and Exit**
This item allows you to exit system setup after saving changes.
- **Discard Changes and Exit**
This item allows you to exit system setup without saving any changes.
- **Save Changes and Reset**
This item allows you to reset the system after saving the changes.
- **Discard Changes and Reset**
This item allows you to rest system setup without saving any changes.
- **Save Changes**
This item allows you to save changes done so far to any of the options.
- **Discard Changes**
This item allows you to discard changes done so far to any of the options.
- **Restore Defaults**
This item allows you to restore/load default values for all the options.
- **Save as User Defaults**
This item allows you to save the changes done so far as user defaults.
- **Restore User Defaults**
This item allows you to restore the user defaults to all the options.
- **Boot Override**
Boot device selection can override your boot priority.

Chapter 4

Software Introduction
& Service

4.1 Introduction

We enable Windows® Embedded software products on Advantech platforms to more effectively support the embedded computing community. Customers are freed from the hassle of dealing with multiple vendors (hardware suppliers, system integrators, embedded OS distributors) for projects. Our goal is to make Windows® Embedded software solutions easily and widely available to the embedded computing community.

4.2 Value-Added Software Services

Software API: An interface that defines the ways by which an application program may request services from libraries and/or operating systems. Provides not only the underlying drivers required but also a rich set of user-friendly, intelligent and integrated interfaces, which speeds development, enhances security and offers add-on value for Advantech platforms. It plays the role of catalyst between developer and solution, and makes Advantech embedded platforms easier and simpler to adopt and operate with customer applications.

4.2.1 Software API

4.2.1.1 Control

GPIO



General Purpose Input/Output is a flexible parallel interface that allows a variety of custom connections. It allows users to monitor the level of signal input or set the output status to switch on/off the device. Our API also provide Programmable GPIO, which allows developers to dynamically set the GPIO input or output status.

SMBus



SMBus is the System Management Bus defined by Intel Corporation in 1995. It is used in personal computers and servers for low-speed system management communications. The SMBus API allows a developer to interface a embedded system environment and transfer serial messages using the SMBus protocols, allowing multiple simultaneous device control.

4.2.1.2 Display

Brightness Control



The Brightness Control API allows a developer to access embedded devices and easily control brightness.

Backlight



The Backlight API allows a developer to control the backlight (screen) on/off in embedded devices.

4.2.1.3 Monitor

Watchdog



A watchdog timer (WDT) is a device that performs a specific operation after a certain period of time if something goes wrong and the system does not recover on its own. A watchdog timer can be programmed to perform a warm boot (restarting the system) after a certain number of seconds.

Hardware Monitor



The Hardware Monitor (HWM) API is a system health supervision API that inspects certain condition indexes, such as fan speed, temperature and voltage.

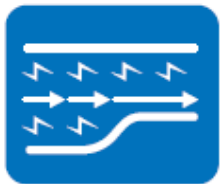
4.2.1.4 Power Saving

CPU Speed



Makes use of Intel SpeedStep technology to save power consumption. The system will automatically adjust the CPU speed depending on the system loading.

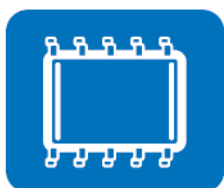
System Throttling



Refers to a series of methods for reducing power consumption in computers by lowering the clock frequency. This API allows the user to adjust the clock from 87.5% to 12.5%.

4.2.2 Software Utility

BIOS Flash



The BIOS Flash utility allows customers to update the flash ROM BIOS version, or use it to back up current BIOS by copying it from the flash chip to a file on customers' disk. The BIOS Flash utility also provides a command line version and an API for fast implementation into customized applications.

Embedded Security ID



The embedded application is the most important property of a system integrator. It contains valuable intellectual property, design knowledge and innovation; but it's also easy to copy. Embedded Security ID utility provides reliable security functions for customers to secure their application data within the embedded BIOS.

Monitoring



The Monitoring is a utility for customer to monitor the system health, like voltage, CPU and system temperature and fan speed. These items are important to a device, if the critical errors occur and are not solved immediately, permanent damage may be caused.

eSOS



The eSOS is a small OS stored in BIOS ROM. It will boot up in case of a main OS crash. It will diagnose the hardware status, and then send an e-mail to the designated administrator. The eSOS also provide for remote connection via Telnet server and FTP server so the administrator can attempt to rescue the system. Note: This function requires BIOS customization.

Chapter 5

Chipset Software
Installation Utility

5.1 Before You Begin

To facilitate the installation of the enhanced display drivers and utility software, read the instructions in this chapter carefully. The drivers for the AIMB-275 A2 are located on Advantech support website: <http://support.advantech.com/Support/>. The driver on the support website will guide and link you to the utilities and drivers under a Windows system. Updates are provided via Service Packs from Microsoft*.

Note! *The driver files on the website are compressed. Do not attempt to install the drivers by copying the files manually. You must download the files and decompress them first. Also, please use the supplied SETUP program to install the drivers.*



Before you begin, it is important to note that most display drivers need to have the relevant software application already installed in the system prior to installing the enhanced display drivers. In addition, many of the installation procedures assume that you are familiar with both the relevant software applications and operating system commands. Review the relevant operating system commands and the pertinent sections of your application software's user manual before performing the installation.

5.2 Introduction

The Intel® Chipset Software Installation (CSI) utility installs the Windows INF files that outline to the operating system how the chipset components will be configured. This is needed for the proper functioning of the following features:

- Core PCI PnP services
- Serial ATA interface support
- USB 2.0/3.0/3.1 support
- Identification of Intel® chipset components in the Device Manager

Note! *This utility is used for the following versions of Windows, and it has to be installed **before** installing all the other drivers:*



- Windows 8/8.1(64bit)
- Windows 10(64bit)

5.3 Windows Driver Setup

You can download driver from Advantech website.

Chapter 6

VGA Setup

6.1 Introduction

The Intel mobile Core i7, Core i5, Core i3, Celeron CPUs with dual core are embedded with an integrated graphics controller. You need to install the VGA driver to enable the function.

Optimized integrated graphic solution: With Intel Graphics Flexible, it supports versatile display options and 32-bit 3D graphics engine. Dual independent display, enhanced display modes for widescreen flat panels for extend, twin, and clone dual display mode, and optimized 3D support deliver an intensive and realistic visual experience.

6.2 Windows

Note! *Before installing this driver, make sure the CSI utility has been installed in your system. See Chapter 5 for information on installing the CSI utility.*



You can download driver from Advantech website.

Chapter 7

LAN Configuration

7.1 Introduction

The AIMB-233 has dual Gigabit Ethernet LANs via dedicated PCI Express x1 lanes Intel i219LM GbE PHY (LAN1) and Intel i211AT GbE(LAN2) that offer bandwidth of up to 500 MB/sec, eliminating the bottleneck of network data flow and incorporating Gigabit Ethernet at 1000 Mbps.

7.2 Features

- Integrated 10/100/1000 Mbps transceiver
- 10/100/1000 Mbps triple-speed MAC
- High-speed RISC core with 24-KB cache
- On-chip voltage regulation
- Wake-on-LAN (WOL) support
- PCI Express X1 host interface

7.3 Installation

Note! *Before installing the LAN drivers, make sure the CSI utility has been installed on your system. See Chapter 5 for information on installing the CSI utility.*



The AIMB-233's Intel i219LM GbE PHY (LAN1) and Intel i211AT GbE(LAN2) Gigabit integrated controllers support all major network operating systems. However, the installation procedure varies from system to system. Please find and use the section that provides the driver setup procedure for the operating system you are using.

Appendix **A**

I/O Pin Assignments

A.1 USB Header (USB0809)

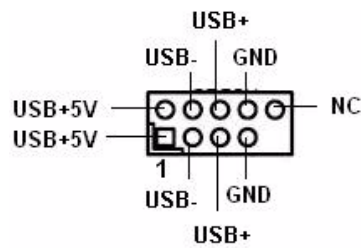


Table A.1: USB Header (USB56)

Pin	Signal	Pin	Signal
1	VBUS	2	VBUS
3	D1-	4	D2-
5	D1+	6	D2+
7	GND	8	GND
9	Key	10	GND

A.2 HDMI Connector (HDMI1)

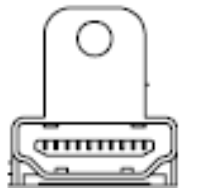


Table A.2: HDMI Connector (HDMI1)

Pin	Signal	Pin	Signal
1	D2+	11	GND
2	GND	12	CLK-
3	D2-	13	NC
4	D1+	14	NC
5	GND	15	SCL
6	D1-	16	SDA
7	D0+	17	GND
8	GND	18	+5V
9	D0-	19	HPD
10	CLK+		

A.3 DP(Type C Alt) (USB04)

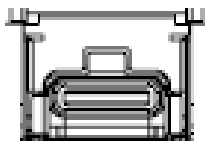


Table A.3: DP(Type C Alt.) Connector

Pin	Signal	Pin	Signal
A1	Ground	B1	GND
A2	TX1+	B2	TX2+
A3	TX1-	B3	TX2-
A4	VBUS	B4	VBUS
A5	CC1	B5	CC2
63	D+	B6	D+
A7	D-	B7	D-
A8	SBU1	B8	NC
A9	VBUS	B9	VBUS
A10	RX2-	B10	RX1-
A11	RX2+	B11	RX1+
A12	GND	B12	GND

A.4 SPI_CN1: SPI Fresh Card Pin Connector

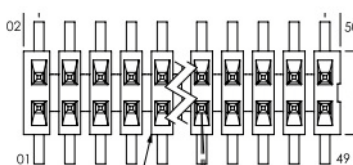
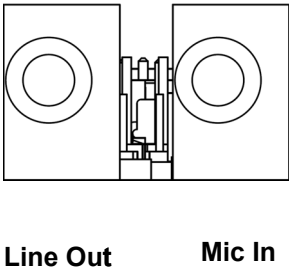


Table A.4: SPI_CN1:SPI Fresh Card Pin Connector

Pin	Signal	Pin	Signal
1	SPI_CS#	2	VCC3
3	SPI_MISO	4	NC
5	NC	6	SPI_CLK
7	GND	8	SPI_MOSI

A.5 Line Out, Mic In Connector (AUDIO1/AUDIO2)



A.6 CPU Fan Power Connector (CPUFAN1)

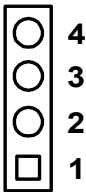


Table A.6: CPU Fan Power Connector (CPUFAN1)	
Pin	Signal
1	GND
2	CPU FAN VCC
3	DETECT
4	CPU PWM

A.7 System Fan Power Connector (SYSFAN1/2)

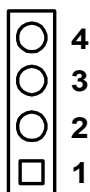


Table A.7: System Fan Power Connector (SYSFAN1/2)

Pin	Signal
1	GND
2	SYS FAN VCC
3	DETECT
4	SYS FAN PWM

A.8 Power Switch/Reset Switch/HDD LED/SMBus/Speaker (JFP1/JFP2)

The single board computer has its own buzzer. You can also connect it to the external speaker on your computer chassis.

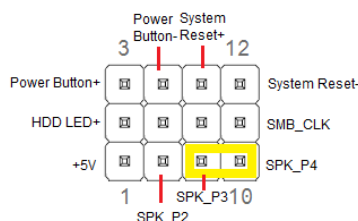
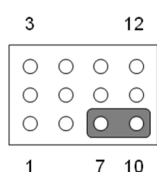


Table A.8: Power Switch/Reset Switch/HDD LED/SMBus/Speaker (JFP1/JFP2)

Pin	Signal	Pin	Signal
1	+5V	2	HDD LED+
3	Power Button+	4	SPK_P2
5	HDD LED-	6	Power Button-
7	SPK_P3	8	SMB_DATA
9	System Reset+	10	SPK_P4
11	SMB_CLK	12	System Reset-

A.9 USB/LAN Ports (LAN12/USB0102/USB03)

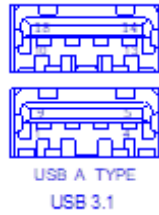


Table A.9: USB Port

Pin	Signal	Pin	Signal
1	VBUS	10	VBUS
2	D1-	11	D2-
3	D1+	12	D2+
4	GND	13	GND
5	RX1-	14	RX2-
6	RX1+	15	RX2+
7	GND	16	GND
8	TX1-	17	TX2-
9	TX1+	18	TX2+

Table A.10: Ethernet 10/100/1000 Mbps RJ-45 Port

Pin	Signal	Pin	Signal
C1	MDI0+	C5	MDI2+
C2	MDI0-	C6	MDI2-
C3	MDI1+	C7	MDI3+
C4	MDI1-	C8	MDI3-

A.10 Serial ATA (SATA1 ~ 2)

Table A.11: Serial ATA 0/1 (SATA1/SATA2)

Pin	Signal	Pin	Signal
1	GND	2	SATA_0TX+
3	SATA_0TX-	4	GND
5	SATA_0RX-	6	SATA_0RX+
7	GND	8	

A.11 AT/ATX Mode (PSON1)

Table A.12: AT/ATX Mode (PSON1)

Pin	Signal	Pin	Signal
1	VCCAT	2	+3.3V_AUX
3	VCCATX		

A.12 HD Audio Interface (FPAUD1)

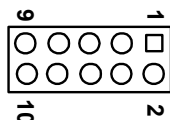


Table A.13: AC-97 Audio Interface (FPAUD1)

Pin	Signal	Pin	Signal
1	MIC-L	2	GND
3	MIC-R	4	PRESENT#
5	LINE-R	6	MIC-JD
7	SENSE	8	KEY
9	LINE-L	10	LINE-JD

A.13 GPIO Pin Header (GPIO1)

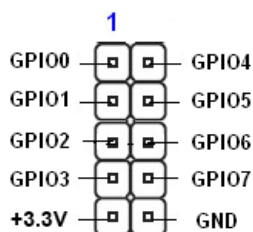


Table A.14: GPIO Pin Header (GPIO1)

Pin	Signal	Pin	Signal
1	GPIO0	2	GPIO4
3	GPIO1	4	GPIO5
5	GPIO2	6	GPIO6
7	GPIO3	8	GPIO7
9	+3.3V	10	GND

A.14 LVDS Connector: LVDS_EDP1

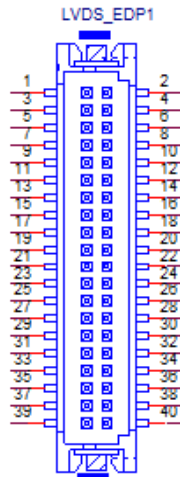


Table A.15: LVDS Connector: LVDS1

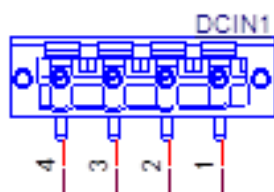
Pin	Signal	Pin	Signal
1	VDD	2	VDD
3	DETECT#	4	GND
5	VDD	6	VDD
7	A0N(EDP_TXN2)	8	A4N
9	A0P(EDP_TXP2)	10	A4P
11	GND	12	GND
13	A1N(EDP_TXN1)	14	A5N
15	A1P(EDP_TXP1)	16	A5P
17	GND	18	GND
19	A2N(EDP_TXN0)	20	A6N
21	A2P(EDP_TXP0)	22	A6P
23	GND	24	GND
25	CLK1N(EDP_TXN3)	26	CLK2N
27	CLK1P(EDP_TXP3)	28	CLK2P
29	GND	30	GND
31	EDP_AUX+	32	EDP_AUX+
33	GND	34	GND(EDP_CH7511_HPD)
35	A3N	36	A7N
37	A3P	38	A7P
39	ENBKL	40	VCON

A.15 LVDS Inverter (INV1)

Table A.16: LVDS Power Jumper

Pin	Signal
1	+12V
2	GND
3	BL_EN
4	BL_CLT
5	+5V

A.16 DC IN (DCIN1)



DCIN1

Table A.17: ATX 12 V connector (ATX12V1)

Pin	Signal	Pin	Signal
1	GND	2	12V ~ 24VD
3	12V ~ 24V	4	GND

A.17 HD Digital Audio Interface (SPDIF1)

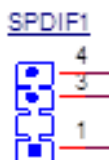


Table A.18: HD Digital Audio Interface (SPDIF_OUT1)

Pin	Signal
1	+5V
2	SPDIF Out
3	KEY
4	GND

A.18 Amplifier Audio Output (AMP1)

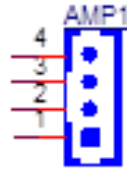


Table A.19: Amplifier Audio Output (AMP1)

Pin	Signal	Pin	Signal
1	AMP_R+	2	AMP_R-
3	AMP_L-	4	AMP_L+

A.19 COM1/COM2 Box Header (COM1/COM2/COM3456)

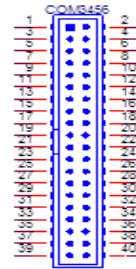
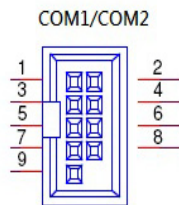


Table A.20: COM1/COM2 Box Header (COM1/COM2)

Pin	Signal	Pin	Signal
1	DCD#	2	DSR#
3	SIN	4	RTS#
5	SOUT	6	CTS#
7	DTR#	8	RI#
9	GND	10	NC

Table A.21: COM3456 Box Header (COM3456)

Pin	Signal	Pin	Signal
1	COM3_DCD#	2	COM3_DSR#
3	COM3_SIN	4	COM3_RTS#
5	COM3_SOUT	6	COM3_CTS#
7	COM3_DTR#	8	COM3_RI#
9	GND	10	GND
11	COM4_DCD#	12	COM4_DSR#
13	COM4_SIN	14	COM4_RTS#
15	COM4_SOUT	16	COM4_CTS#
17	COM4_DTR#	18	COM4_RI#
19	GND	20	GND

A.20 Watch dog and Open chassis alarm (JWDT1+JOBS1)

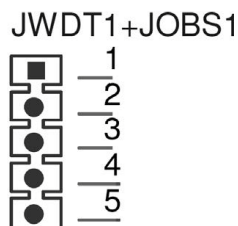


Table A.21: Watchdog and Open Chassis Alarm (JWDT1+JOBS1)

Pin	Signal	Pin	Signal
1	NC	2	EC_WG#
3	SYS_RESET#	4	ERR_BEEP
5	OBS_BEEP		

JWDT1+JOBS1: 2-3 WDT control by EC*; 4-5 Alarm by EC*

A.21 Low Pin Count Bus (LPC1)

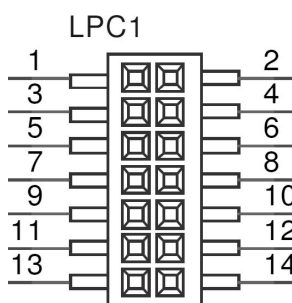


Table A.22: Low Pin Count Bus (LPC1)

Pin	Signal	Pin	Signal
1	LPC_CLK	2	LPC_AD1
3	PLTRST#	4	LPC_AD0
5	LPC_FRAME#	6	+3.3V
7	LPC_AD3	8	GND
9	LPC_AD2	10	SMB_CLK
11	LPC_SERIRQ	12	SMB_DAT
13	+5V_AUX	14	+5V



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