

PCA-6029

**Intel® Core™ i7/i5/i3/Pentium®
PICMG 1.0 Single Host Board
with VGA/ DVI-D/ DDR4 /SATA3.0
/ USB3.0 / Dual GbE**

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Enabling an Intelligent Planet

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Caution! *There is a danger of a new battery exploding if it is incorrectly installed. Do not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.*



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Before you begin installing your motherboard, please make sure that the following materials have been shipped:

- | | |
|---|--------------------|
| ■ 1 PCA-6029 PICMGG 1.0 Single Host Board | |
| ■ 1 PCA-6029 startup manual | |
| ■ 2 Serial ATA HDD data cable | P/N: 1700003194 |
| ■ 1 COM + printer ports cable kit | P/N: 1701260305 |
| ■ 1 4-port USB cable kit | P/N: 1700008461-11 |
| ■ Keyboard and mouse Y cable | P/N: 1700060202 |

If any of these items are missing or damaged, contact your distributor or sales representative immediately. We have carefully inspected the PCA-6029 mechanically and electrically before shipment. It should be free of marks and scratches and in perfect working order upon receipt. As you unpack the PCA-6029, check it for signs of shipping damage. (For example, damaged box, scratches, dents, etc.) If it is damaged or it fails to meet the specifications, notify our service department or your local sales representative immediately. Also notify the carrier. Retain the shipping carton and packing material for inspection by the carrier. After inspection, we will make arrangements to repair or replace the unit.

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Chapter 1

Hardware Configuration

1.1 Introduction

PCA-6029 is a PICMG 1.0 form-factor single host board which is designed with Intel® H110 PCH for industrial applications that need high computing power and strong I/O capability. PCA-6029 supports 14nm manufacturing technology, LGA1151 socket Intel® Core™ i7/i5/i3 and Pentium® processors that integrate memory and graphic controllers and support DDR4 2133/2400 MHz SDRAM up to 64 GB. By supporting advanced computing technology, PCA-6029 is suitable for computing power hungry industrial applications.

PCA-6029 has excellent graphic processing capability with its integrated Intel® HD graphics core with shared system memory dependent upon OS configuration. With this feature, PCA-6029 provides strong 2D/3D graphic processing power without a discrete graphic card to save extra cost, power consumption and thermal design effort.

PCA-6029 also has multiple I/O interfaces, and can support Advantech PICMG 1.0 compatible backplanes to offer various expansion slots such as PCI and ISA slots.

New SATA Gen3 (600MB/sec) ports meet high data transfer requests, like storage and DVR. Three SATA ports, two USB 3.0 ports reach 5Gbps high data transfer application, the two on-board RS-232 serial ports are COM ports for industrial control applications. With flexible I/O and graphic expansibility, PCA-6029 is an excellent, cost effective graphic or I/O oriented workstation class platform. With outstanding performance and exceptional features, PCA-6029 is the ideal computing platform for advanced industrial applications.

1.2 Features & Benefits

Features	Benefits
PCA-6029 supports Intel 6th/7th generation processors	Intel's Skylake Core i7/i5/i3/Pentium processor cores with quad/dual-core computing power brings quantum-leap performance improvement.
PCA-6029 supports DDR4 2133/2400 SDRAM up to 64 GB	Provides higher memory data transmission and processing efficiency, bringing higher system performance.
Fully supports Advantech SUSI APIs and Utilities.	Reduces customer S/W development effort with more reliable S/W, also provides value-added utilities such as system monitor and Embedded Security ID.
SATA Gen3	Provides high performance storage interface. SATA Gen3 is 6Gb/s which is double bandwidth with SATA Gen2.
USB 3.0	Provides high transfer data performance interface; USB 3.0 data transfer rate is 5Gbps which is 10 times faster than USB2.0.

1.3 Specifications

1.3.1 System

- **CPU:** LGA1151-socket Core i7/i5/i3 and Pentium series processors.
- **L3 Cache:** Please refer to CPU specification for detail information.
- **BIOS:** AMI SPI BIOS (128 Mb SPI).
- **System Chipset:** Intel H110.
- **SATA hard disk drive interface:** Four SATA3 (600MB/s) with blue connectors. These interfaces can be enabled/disabled in the BIOS.

Note! PCA-6029 does NOT support PATA (IDE) interface.



1.3.2 Memory

- **RAM:**
 - PCA-6029: Up to 64 GB in two 240-pin DIMM sockets. Supports dual-channel DDR4 2133/2400 MHz SDRAM WITHOUT ECC function.

1.3.3 Input/Output

- **ISA bus:** HISA (ISA high driver).
- **PCI bus:** Four PCI masters to the backplane, 32-bit, 33 MHz PCI 2.2 compliant.
- **Serial ports:** Two RS-232 serial ports.
- **PS/2 keyboard and mouse connector:** One 6-pin mini-DIN connectors is located on the mounting bracket for easy connection to a PS/2 keyboard and mouse via the Y-cable included in the package.
- **USB port:** Supports 7 USB 2.0 ports with transfer rate up to 480 Mbps, and 3 USB 3.0 ports with transfer rate up to 5 Gbps.
- **LPC:** One LPC connector to support Advantech TPM LPC modules.
- **GPIO:** Supports 8-bit GPIO from super I/O for general purpose control application.

1.3.4 Graphics

- **Controller:** Intel® HD Graphics embedded in the processor.
- **Display memory:** Shared memory is subject to OS (Please install 2 GB or above memory for basic system configuration).
- **CRT:** Up to 1920 x1080 resolution, 400 MHz RAMDAC.
- **DVI-D:** One DVI-D pin header supports resolution up to 1920 x 1200 @ 60 Hz.
- **PCI slot on the backplane:** An external graphic card can be installed on PCI slots for stronger 2D/3D graphic capability.

1.3.5 Ethernet LAN

- Supporting single/dual 10/100/1000 Mbps Ethernet port(s) via the dedicated PCI Express x1 bus which provides 500 MB/s data transmission rate.
- **Controller:**
 - LAN 1: Intel® i219-V.
 - LAN 2: Intel® i211-AT.

1.3.6 Industrial features

- **Watchdog timer:** Can generate a system reset. The watchdog timer is programmable, with each unit equal to one second (255 levels).

1.3.7 Mechanical and environmental specifications

- **Operating temperature:** 0 ~ 60° C (32 ~ 140° F, depending on CPU and thermal solution)
- **Storage temperature:** -40 ~ 85° C (-40 ~ 185° F)
- **Humidity:** 20 ~ 95% non-condensing
- **Power supply voltage:** +5 V, +12 V, +5 VSB
- **Power consumption:**
Test Environment: i7-6700 CPU, 32G 2Rx8 DDR4 3200U*2, SEAGATE 500GB*1
Voltage: +V12, +V5, +V5_SB, +V12_4P
Current: 0.13A, 0.94A, 0.08A, 1.33A
- **Board size:** 338.58 mm (L) x 126.39 mm (W) (13.3" x 4.98")
- **Board weight:** 0.5 kg

1.4 Jumpers and Connectors

Connectors on the PCA-6029 single host board link it to external devices such as hard disk drives and a keyboard. In addition, the board has a number of jumpers used to configure your system for your application.

The tables below list the function of each of the board jumpers and connectors. Later sections in this chapter give instructions on setting jumpers. Chapter 2 gives instructions for connecting external devices to your motherboard.

Table 1.1: Jumper list

Label	Function
JCMOS1	CMOS clear
JME1	ME FW update
JWDT1	Watchdog timer output selection
JOBS1	HW Monitor Alarm
JFV1	VGA dummy load setting

Table 1.2: Connectors

Label	Function
LPT1	Parallel port, Parallel port x 1, supports SPP/EPP/ECP mode
LAN1	Intel i219-V
LAN2	Intel i211-AT
VGA1	VGA connector
KBMS1	PS/2 keyboard and mouse connector
KBMS2	External keyboard/mouse connector
COM1	Serial port: COM1; RS-232 (9-pin Box Header)
COM2	Serial port: COM2; RS-232 (9-pin Box Header)
JIR	Infrared connector
JFP3 (Keyboard Lock and Power LED)	Power LED
	Suspend: Fast flash (ATX/AT)
	System On: ON (ATX/AT)
	System Off: OFF ((ATX/AT))
JFP2	External speaker / SATA HDD LED connector
JFP1	Power Switch / Reset connector
JCASE1	Case Open
CPUFAN1	CPU FAN connector (4-pin)
LANLED1	LAN1/2 LED extension connector
HDAUD1	HD audio extension module connector
USB3H1	2* USB 3.0 ports (20-pin header)
USB3C1	USB 3.0 port (external Type-A)
USB2H1	2* USB 2.0 ports (10-pin header)
USB2H2	2* USB 2.0 ports (10-pin header)
USB2H3	2* USB 2.0 ports (10-pin header)
USB2A1	USB 2.0 port (internal Type-A)
SATA0	Serial ATA0 (3.0)
SATA1	Serial ATA1 (3.0)

SATA2	Serial ATA2 (3.0)
SATA3	Serial ATA3 (3.0)
CPU1	CPU Socket
DIMMA1	Memory connector channel A
DIMMB1	Memory connector channel B
GPIO1	GPIO pin header (SMD pitch-2.0 mm)
LPC1	COM port module expansion pin-header
DVI1	DVI connector
M.2_1	M.2 2280 PCIe 2.0 x4 connector
SMBUS1	SMBus connector
ATX12V1	12V connector
ATXF1	ATX power connector

1.5 Board Layout: Jumper and Connector Locations

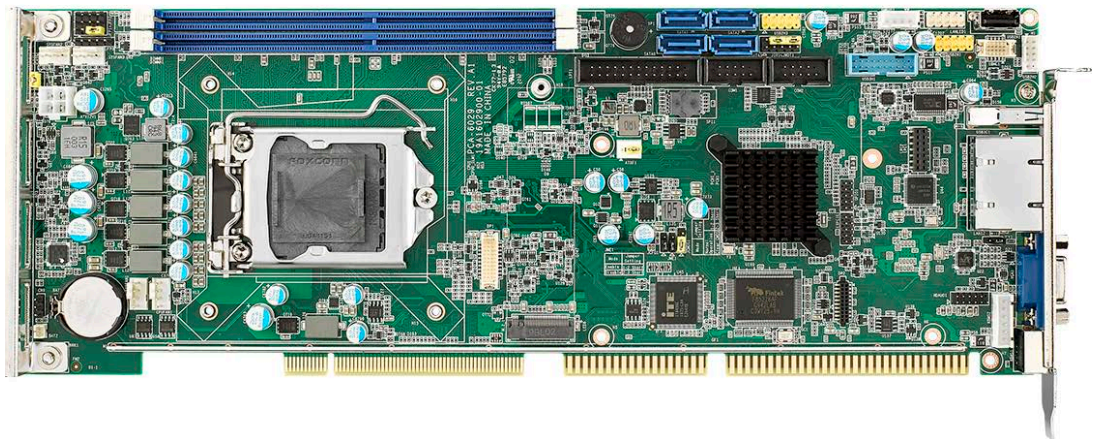


Figure 1.1 Jumper and connector locations

1.6 Block Diagram

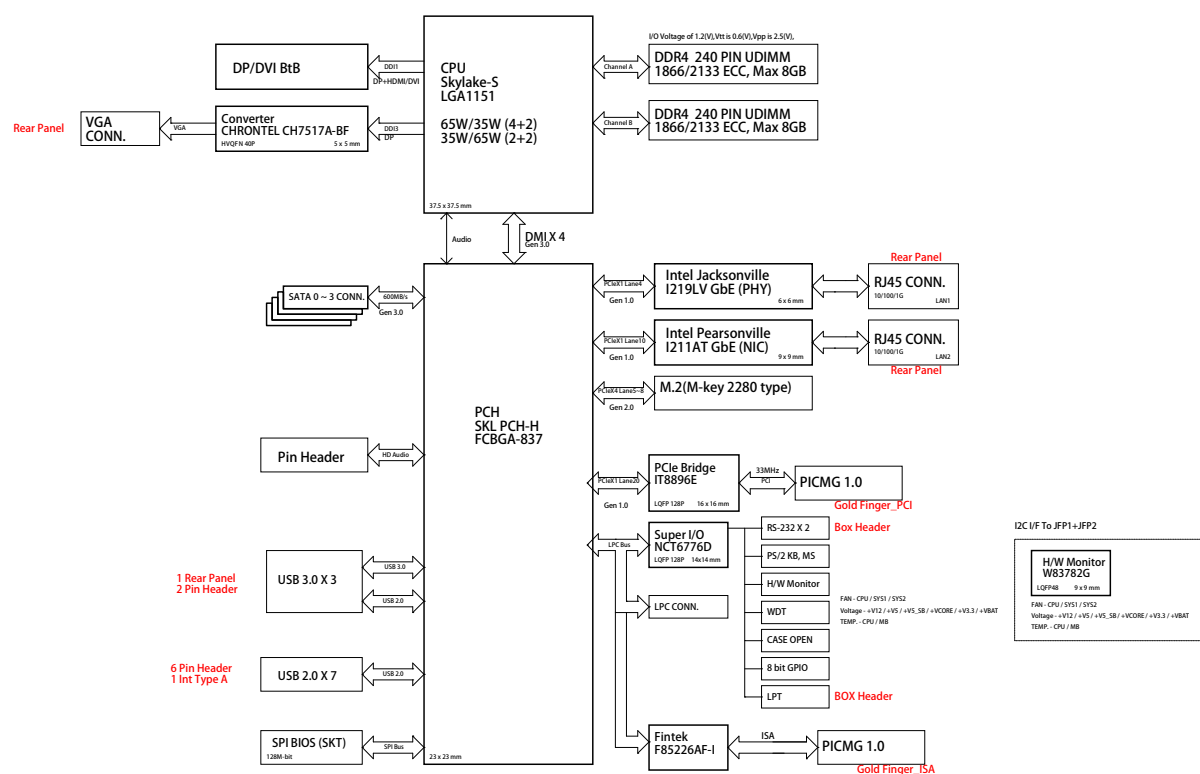


Figure 1.2 PCA-6029 Block Diagram

1.7 Safety Precautions

Warning! *Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.*



Caution! *Always ground yourself to remove any static charge before touching the motherboard. Modern electronic devices are very sensitive to static electrical discharges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis.*



Caution! *The computer is provided with a battery-powered Real-time Clock. There is a danger of explosion if battery is incorrectly replaced. Replace only with same or equivalent type recommended by the manufacturer. Discard used batteries according to manufacturer's instructions.*



Caution! *There is a danger of a new battery exploding if it is incorrectly installed. Do not attempt to recharge, force open or heat the battery. Replace the battery only with the same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.*



1.8 Jumper Settings

This section provides instructions on how to configure your motherboard by setting the jumpers. It also includes the motherboard's default settings and your options for each jumper.



1.8.1 How to set jumpers

You can configure your motherboard to match the needs of your application by setting the jumpers. A jumper is a metal bridge that closes an electrical circuit. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” (or turn ON) a jumper, you connect the pins with the clip. To “open” (or turn OFF) a jumper, you remove the clip. Sometimes a jumper consists of a set of three pins, labeled 1, 2 and 3. In this case you connect either pins 1 and 2, or 2 and 3. A pair of needle-nose pliers may be useful when setting jumpers.

1.8.2 BIOS CMOS (JCMOS1)

The PCA-6029 CPU card contains a jumper that can erase BIOS CMOS/ME data and reset the system BIOS information. Normally this jumper should be set with pins 1-2 closed. If you want to reset those data, set JCMOS1/JME1 to 2-3 closed for just a few seconds, and then move the jumper back to 1-2 closed. This procedure will reset the CMOS/ME to its last status or default setting.

Table 1.3: JCMOS1/JME1: clear CMOS/ME FW update



Function	Jumper Setting
*Keep CMOS data *ME normal mode	 1-2 closed
Clear CMOS data ME recovery mode	 2-3 closed

* default setting

1.8.3 Watchdog timer output (JWDT1)



The PCA-6029 contains a watchdog timer that will reset the CPU in the event the CPU stops processing. This feature means the PCA-6029 will recover from a software failure or an EMI problem. The JWDT1 jumper settings control what the computer will do in the event the watchdog timer is tripped.

Table 1.4: Watchdog timer output (JWDT1)

Function	Jumper Setting
* Reset	 2-3 closed
Reserved	 1-2 closed

*default setting

Table 1.5: H/W monitor alarm (JOBS1)

Function	Jumper Setting
Enabled	 1-2 closed
Disabled	 1-2 opened

(JOBS1) is a 2-pin connector for setting enable/disable alarm while the on-board security event acts.



1.8.4 VGA dummy load setting(JEV1)

The PCA-6029 contains an optional VGA dummy load feature set by the JFV1 jumper. The default setting is disabled.

If JFV1 is set to enable, a dummy monitor on the VGA port is created to prevent the on-board graphics from being switched off while no physical monitors are connected.

This function is useful when you control the PCA-6029 via remote software and do not intend to connect any monitors.

Table 1.6: VGA dummy load setting(JEV1)

Function	Jumper Setting
Enable VGA dummy load	<div style="display: flex; align-items: center;"> <div style="margin-right: 10px;"> <div style="display: flex; justify-content: space-around; width: 60px;"> 123 </div>  </div> <div>1-2 closed</div> </div>
*Disable VGA dummy load	<div style="display: flex; align-items: center;"> <div style="margin-right: 10px;"> <div style="display: flex; justify-content: space-around; width: 60px;"> 123 </div>  </div> <div>2-3 closed</div> </div>

* default setting

Note! *It is recommended to leave this function disabled if you use DVI/DP as your main display.*



1.9 System Memory

PCA-6029 has two 240-pin memory sockets for (non-ECC) DDR4 1600 MHz memory modules with maximum capacity of 32 GB. (Maximum 16 GB for each DIMM)

Note! *PCA-6029 does NOT support registered DIMMs (RDIMMs).*



1.10 Memory Installation Procedures

To install DIMMs, first make sure the two handles of the DIMM socket are in the “open” position. i.e. the handles lean outward. Slowly slide the DIMM module along the plastic guides on both ends of the socket. Then press the DIMM module right down into the socket, until you hear a click. This is when the two handles have automatically locked the memory module into the correct position of the DIMM socket. To remove the memory module, just push both handles outward, and the memory module will be ejected by the mechanism in the socket.

1.11 Cache Memory

CPUs supported by PCA-6029 have 8 MB, 6 MB, 3 MB L3 cache memory sizes.

Note! *Please refer to Intel CPU data sheet for detail information.*



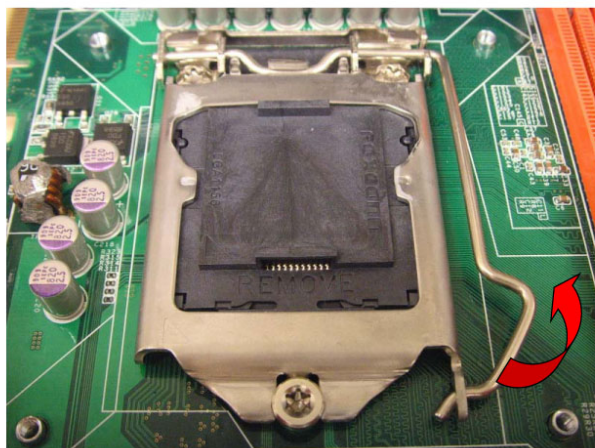
1.12 Processor Installation

Warning! Without a fan or heat sink, the processor will overheat and cause damage to both the processor and the single board computer. To install a processor, first turn off your system.

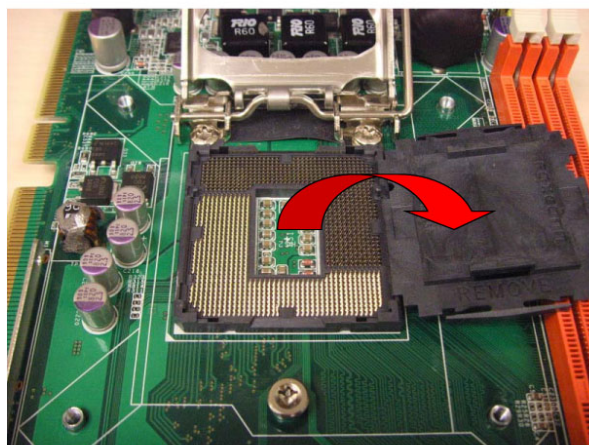


The PCA-6029 is designed for Intel® LGA 1151 socket processors.

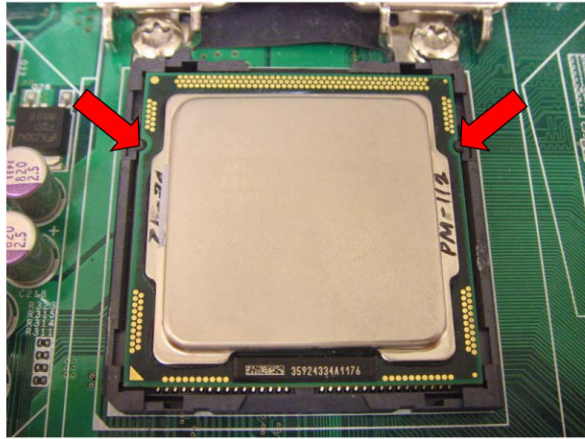
1. Pull the bar beside the processor socket outward and lift it.



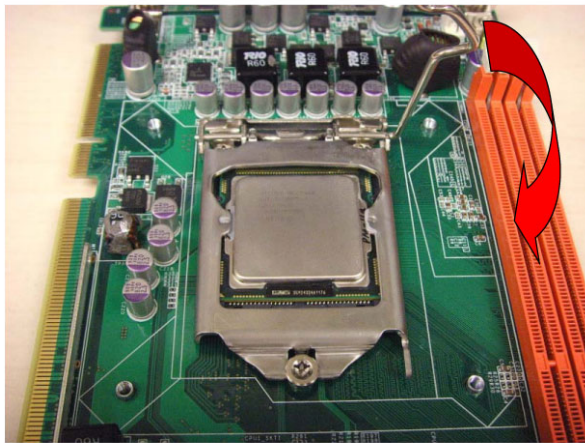
2. Remove the socket protection cap.



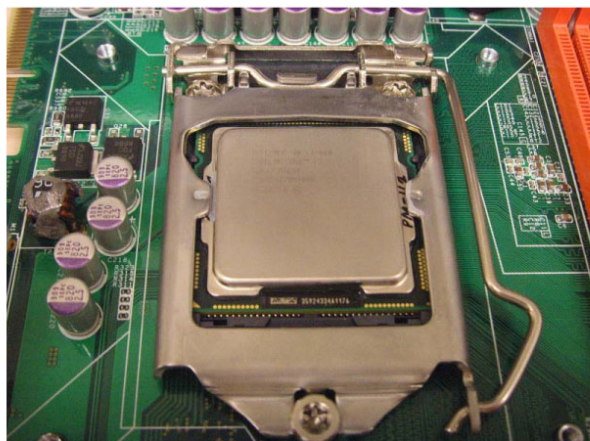
3. Align the cuts on the processor with the edges of the socket.



4. Replace the socket cap; lower the retainer bar and clip it shut.



5. Finished processor installation.



1.13 Processor Cooler Installation

Purchasing PCA-6029 optimized CPU cooler (P/N: 1960047831N001) from Advantech is a must. Other brand CPU coolers are NOT compatible with PCA-6029.

Advantech offers a specially designed CPU cooler for PCA-6029 for better heat dissipation efficiency and enhancing rigidity of CPU card, part number 1960047831N001.

Buy it for PCA-6029 CPU card since it is NOT compatible with other brand CPU coolers (neither is it compatible with Intel boxed CPU cooler).

Please install 1960047831N001 CPU cooler following these instructions:

Attach the CPU cooler on CPU card by fastening four screws of the CPU cooler into the steel back-plate on PCB.



Note the direction of CPU cooler; it must follow that shown above. Installing a CPU cooler in the wrong direction may cause poor heat dissipation that may damage the CPU card.

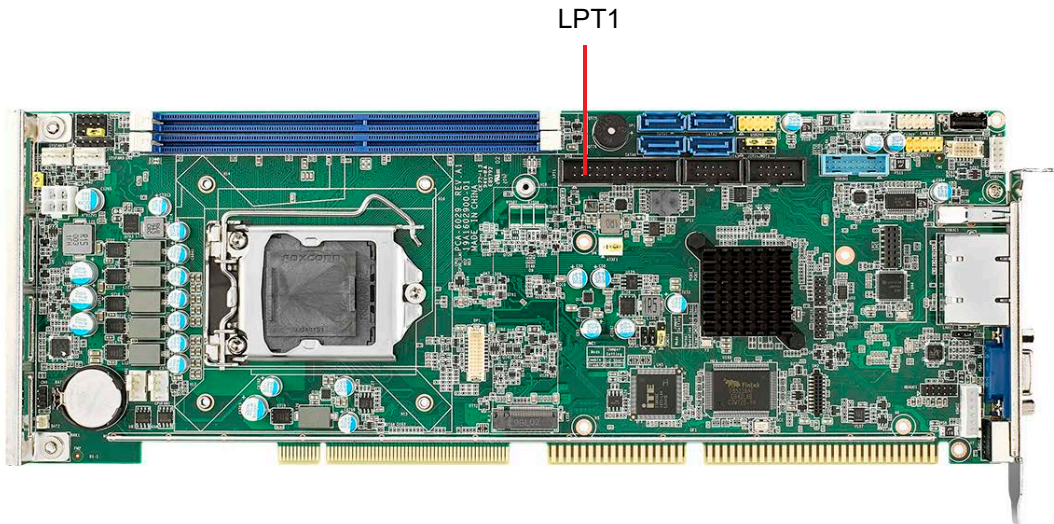
Chapter 2

Connecting
Peripherals

2.1 Introduction

You can access most of the connectors from the top of the board. If you have a number of cards installed, you may need to partially remove the card to make all the connections.

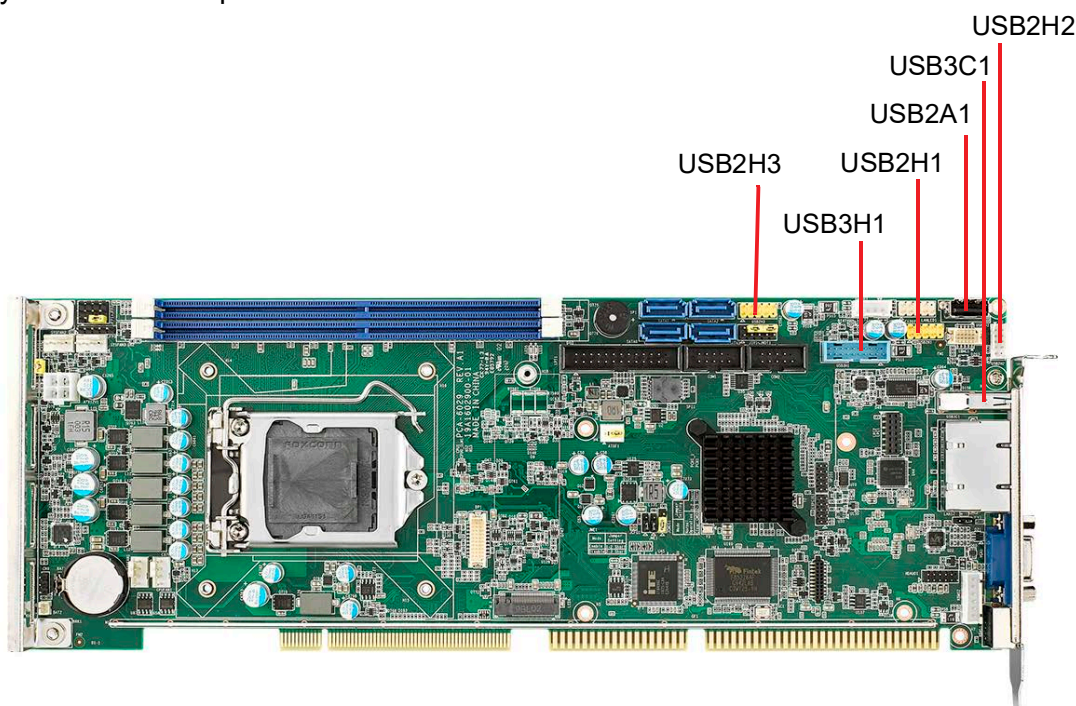
2.2 Parallel Port (LPT1)



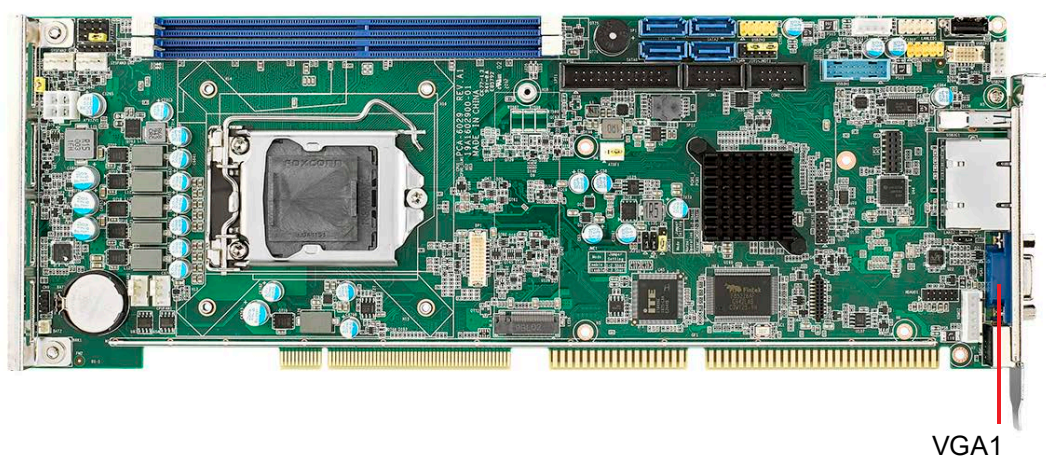
The parallel port is normally used to connect the motherboard to a printer. The PCA-6029 includes an onboard parallel port, accessed through a 26-pin flat-cable connector, LPT1.

2.3 USB Ports (USB3H1, USB3C1, USB2H1-3, USB2A1)

The PCA-6029 provides up to 10 USB (Universal Serial Bus) on-board ports with complete Plug & Play and hot swap support for up to 127 external devices. These USB ports comply with USB Specification 2.0 and 3.0, support transfer rates up to 480 Mbps (USB2.0) and 5 Gbps (USB3.0). The USB interface can be disabled in the system BIOS setup.

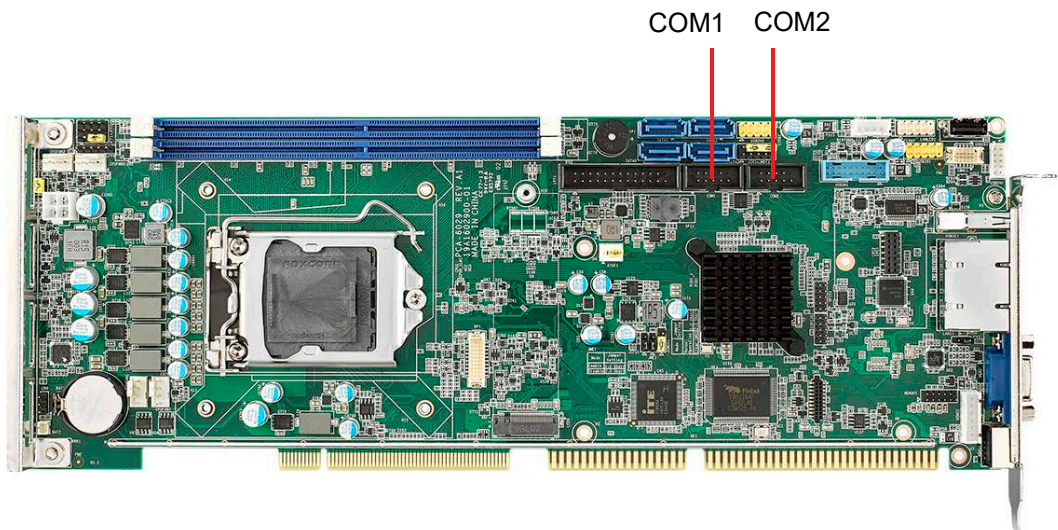


2.4 VGA Connectors (VGA1)



This CPU card has VGA outputs that can drive conventional CRT displays. VGA1 is a standard 15-pin D-SUB connector commonly used for VGA.

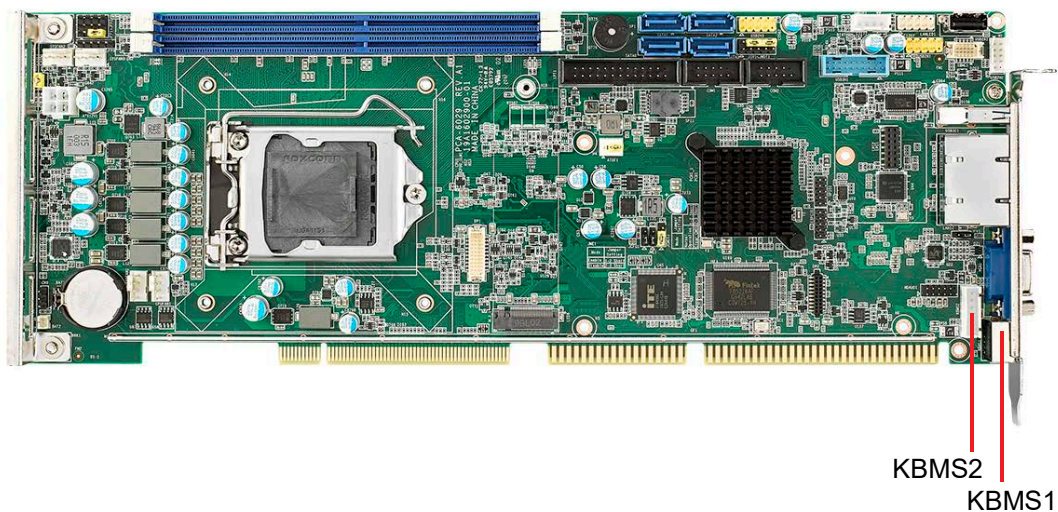
2.5 Serial Ports (COM1 & COM2)



The PCA-6029 offers two serial ports. These ports can connect to serial devices, such as a mouse or a printer, or to a communications network.

The IRQ and address ranges for both ports are fixed. However, if you want to disable the port or change these parameters later, you can do this in the system BIOS setup. Optional dual COM cable, 1701092300, is available as well.

2.6 PS/2 Keyboard and Mouse Connector (KBMS1/KBMS2)



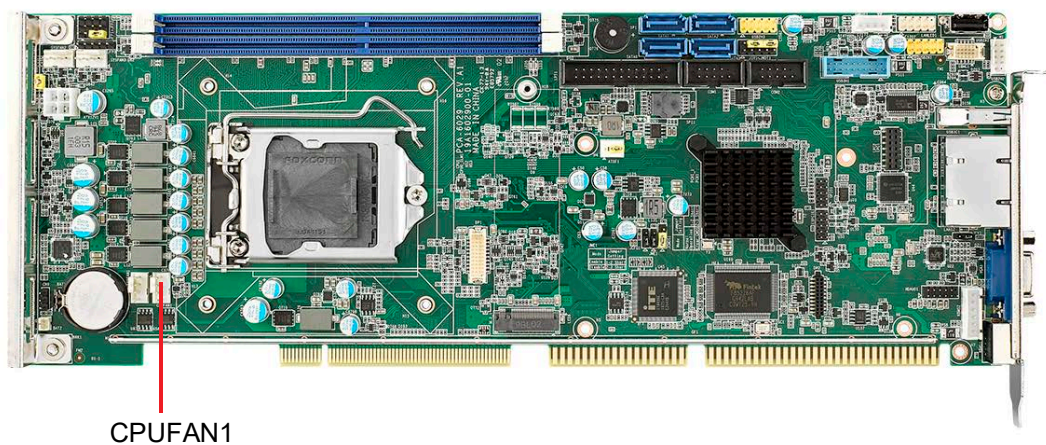
Two on-board 6-pin mini-DIN connectors (KBMS1) provide connection to PS/2 keyboard and mouse by the Y-cable (1700060202) in the package.

The on-board KBMS2 pin header provides connection to the front panel PS/2 keyboard and mouse connector of the chassis.

2.7 CPU Fan Connector (CPUFAN1)

This fan connector supports 3-pin or 4-pin fan coolers and smart fan functions.

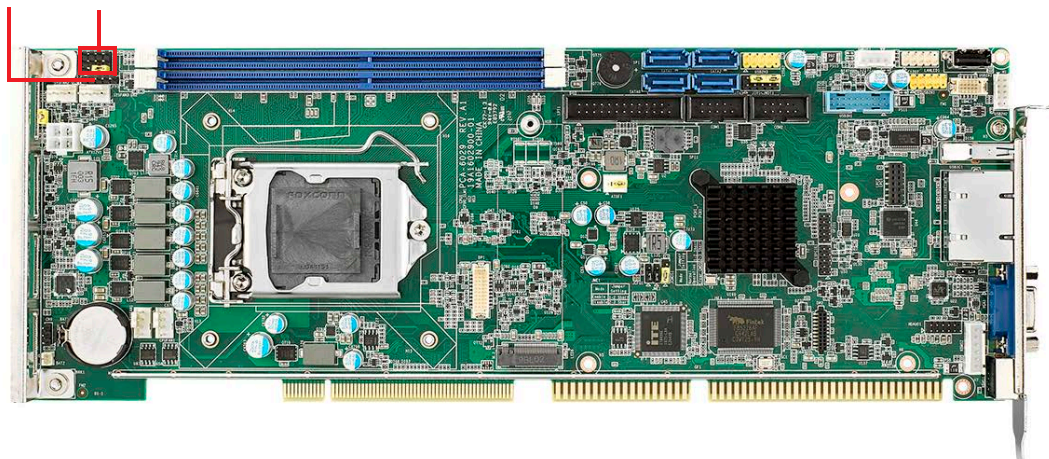
Note! Fan speed is controlled by voltage.



2.8 Front Panel Connectors (JFP1, JFP2 & JFP3)

There are several external switches to monitor and control the PCA-6029.

JFP3 JFP1+JFP2

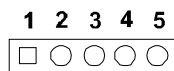


2.8.1 Power LED and keyboard lock (JFP3)

JFP3 is a 5-pin connector for the power LED. Refer to Appendix B for detailed information on the pin assignments. If a PS/2 or ATX power supply is used, the system's power LED status will be as indicated below:

Table 2.1: PS/2 or ATX power supply LED status

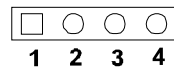
Power mode	LED (PS/2 power)	LED (ATX power)
System On	On	On
System Suspend	Flashes	Flashes
System Off	Off	Off



JFP1	PWR_SW	Reset
JFP2	HDD LED	SNMP
	Speaker	
JFP3	PWR_LED & Key Lock	

2.8.2 External speaker (JFP2)

JFP2 is a 4-pin connector for an external speaker. The PCA-6029 provides an onboard buzzer as an alternative to an external speaker. To enable the buzzer, set pins 3 and 4 as closed.

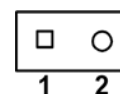


JFP1	PWR_SW	Reset
JFP2	HDD LED	SNMP
	Speaker	
JFP3	PWR_LED & Key Lock	

2.8.3 Reset connector (JFP1)

Many computer cases offer the convenience of a reset button. Connect the wire from the reset button.

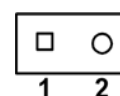
JFP1	PWR_SW	Reset
JFP2	HDD LED	SNMP
	Speaker	
JFP3	PWR_LED & Key Lock	



2.8.4 HDD LED connector (JFP2)

You can connect LED to connector JFP2 to indicate when the HDD is active.

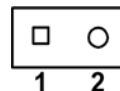
JFP1	PWR_SW	Reset
JFP2	HDD LED	SNMP
	Speaker	
JFP3	PWR_LED & Key Lock	



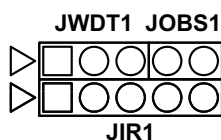
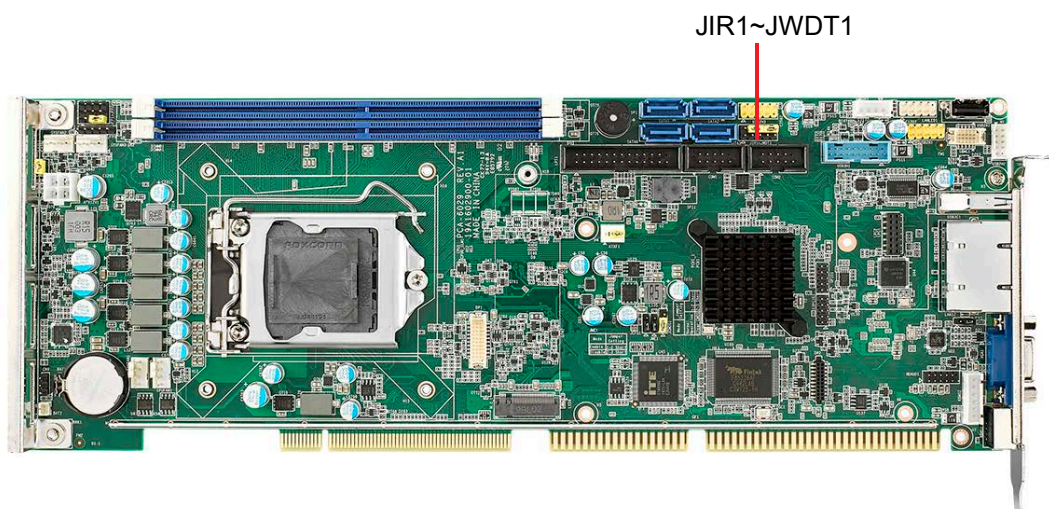
2.8.5 ATX soft power switch (JFP1)

If your computer case is equipped with an ATX power supply, you should connect the power on/off button on your computer case to JFP1. This connection enables you to turn your computer on and off.

JFP1	PWR_SW	Reset
JFP2	HDD_LED	SNMP
	Speaker	
JFP3	PWR_LED & Key Lock	



2.9 H/W Monitor/Watchdog Timer/Infrared



2.9.1 H/W monitor alarm (JOBS1)

This 2-pin header is for enabling/disabling H/W monitor alarm function.

Closed: Enables OBS Alarm

Open: Disables OBS Alarm

2.9.2 Watchdog timer (JWDT1)

This is for setting action trigger by watchdog timer.

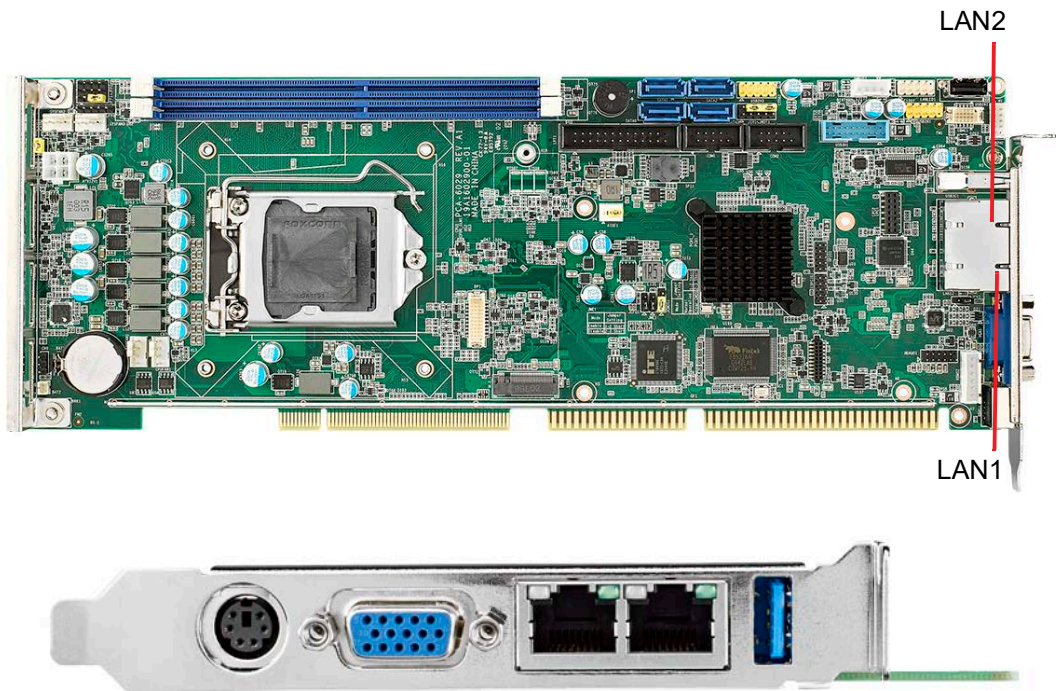
1-2 Pin Close: No Action

2-3 Pin Close: System Reset

2.9.3 Infrared interface (JIR1)

This is a 5-pin header for an infrared device.

2.10 LAN Ports (LAN1 & LAN2)

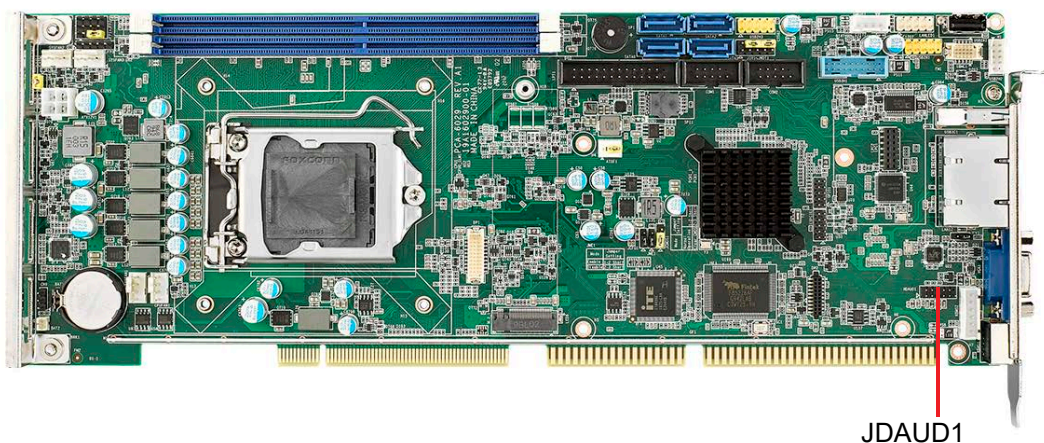


The PCA-6029 is equipped with one or two high-performance 1000 Mbps Ethernet LANs. They are supported by all major network operating systems. The RJ-45 jacks on the rear plate provides convenient connectivity.

Table 2.2: LAN LED Indicators

LAN Mode	LED1	LED2
1000Mbps Link On	Green On	On
1000Mbps Active	Green on	Flash
1000Mbps Link Off	Off	Off
100Mbps Link On	Orange On	On
100Mbps Active	Orange On	Flash
100Mbps Link Off	Off	Off
10Mbps Link On	Off	On
10Mbps Active	Off	Flash
10Mbps Link Off	Off	Off

2.11 High Definition Audio Module Interface (HDAUD1)



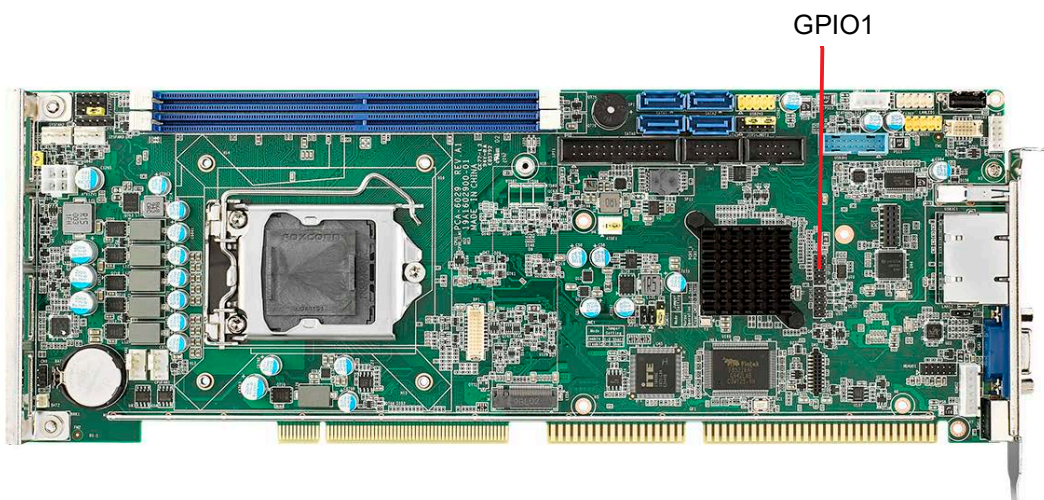
This HDAUD1 pin header is the connection interface to Advantech's high definition audio module.

Note! Advantech high definition audio module ordering information.



P/N: PCA-AUDIO-HDB1E

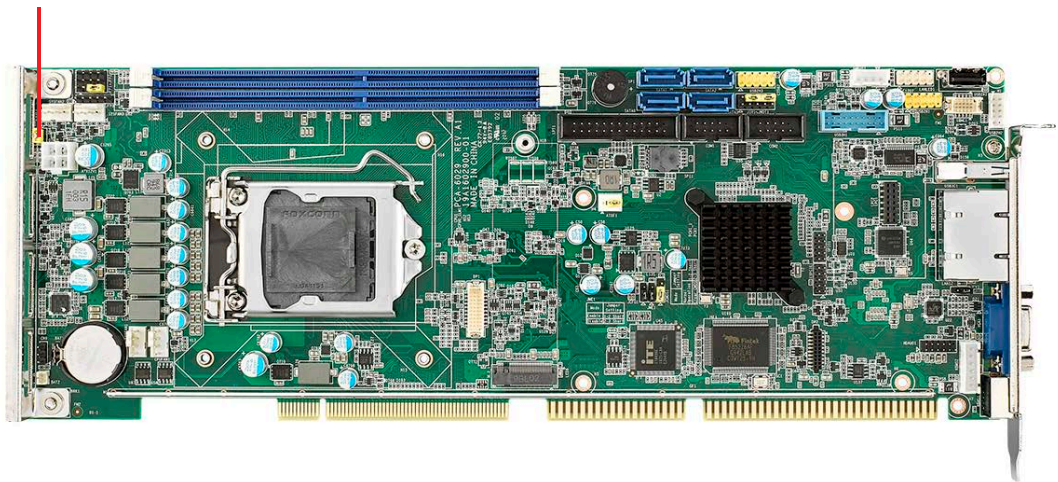
2.12 GPIO Header (GPIO1)



Provides 10-Pin pin header for 8-bit Digital I/O usage. Refer to Appendix B for detailed information on the pin assignments and programming guide in Appendix C.

2.13 Case Open Connector (JCASE1)

JCASE1



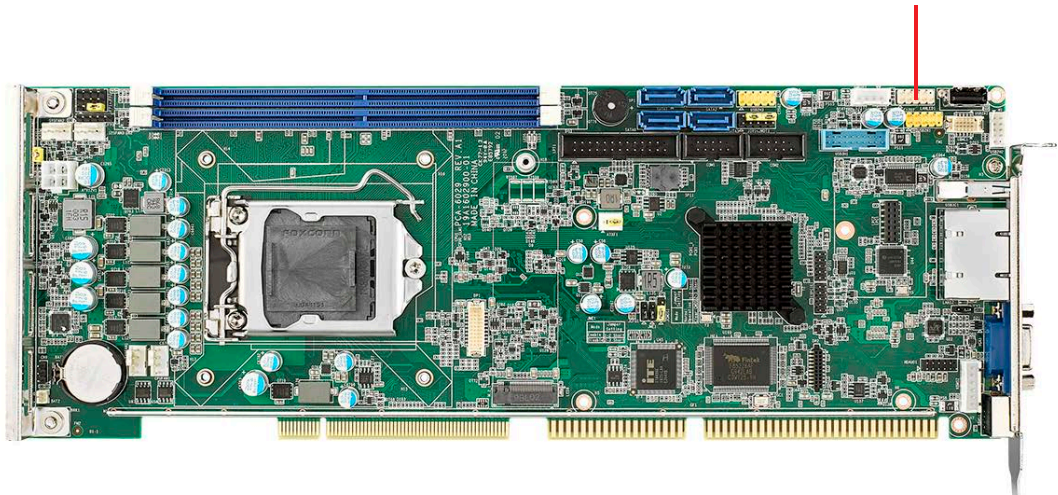
The 2-pin case open connector is for chassis with a case open sensor. When the case is open, the buzzer on motherboard will beep.

2.14 Front Panel LAN Indicator Connector (LANLED1)

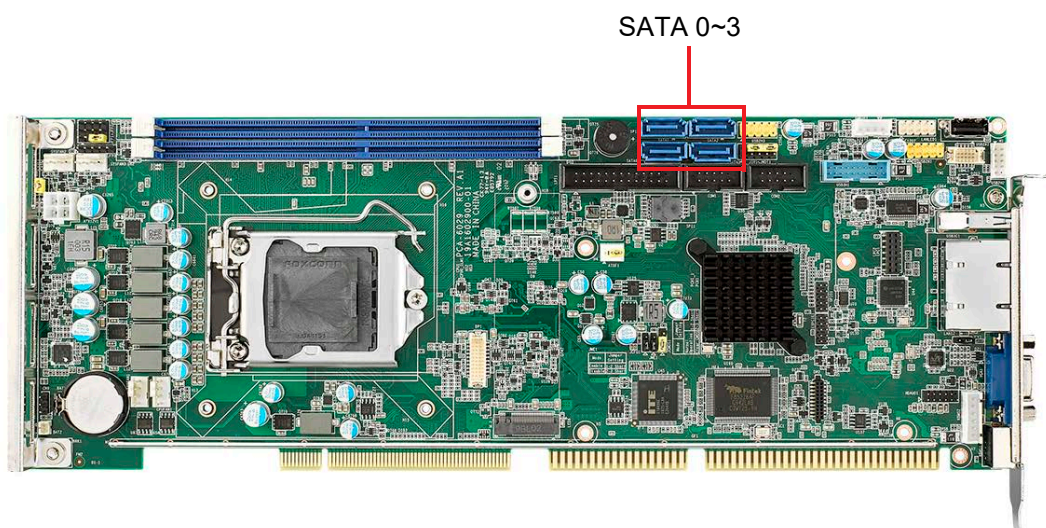
Table 2.3: LAN LED Indicators

LAN Mode	LED1	LED2
1000Mbps Link On	Green On	On
1000Mbps Active	Green on	Flash
1000Mbps Link Off	Off	Off
100Mbps Link On	Orange On	On
100Mbps Active	Orange On	Flash
100Mbps Link Off	Off	Off
10Mbps Link On	Off	On
10Mbps Active	Off	Flash
10Mbps Link Off	Off	Off

LANLED1



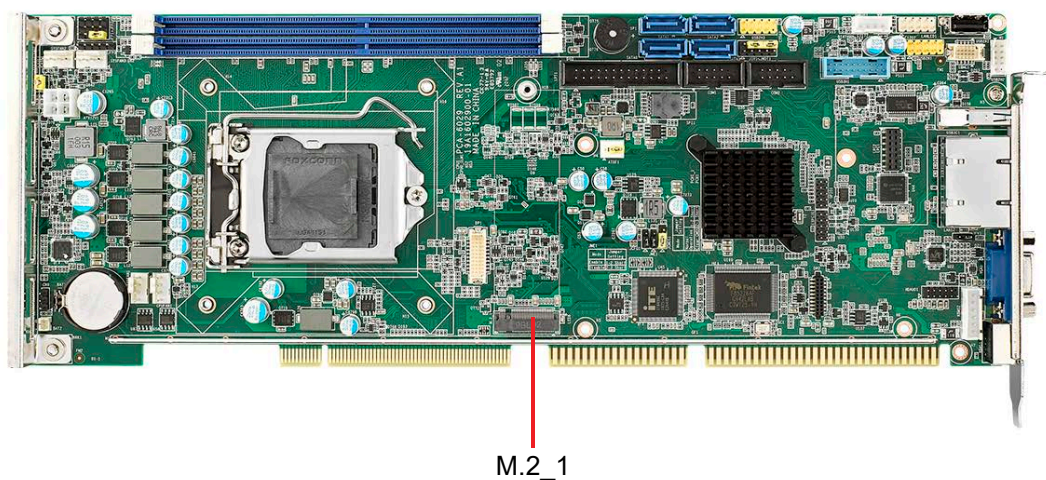
2.15 Serial ATA Interface (SATA0~SATA3)



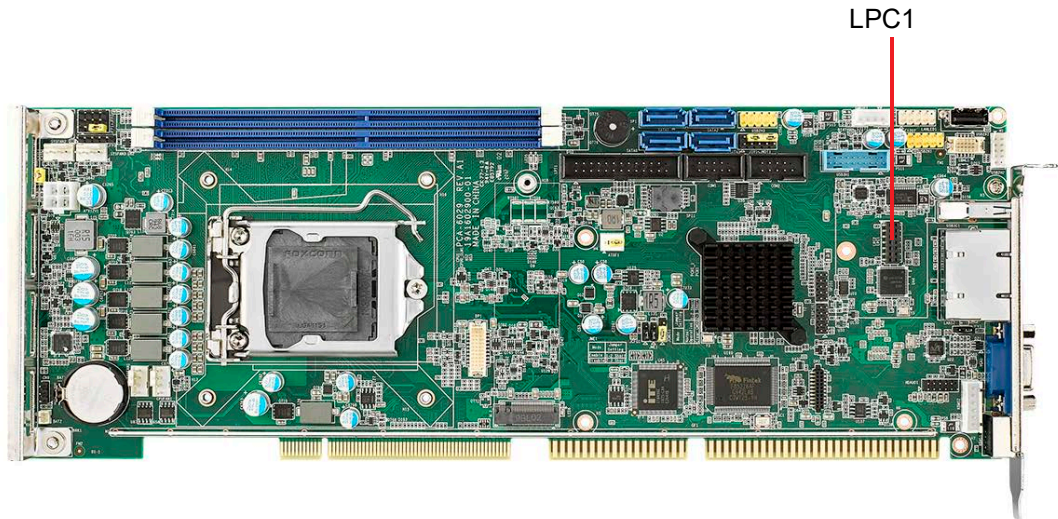
The PCA-6029 features high performance serial ATA interface (4* 600 MB/s) which eases cabling to hard drivers or CD/DVD drivers with long cables.

2.16 M.2 Connector

PCA-6029 provides a m.2 socket.



2.17 LPC Extension Interface (LPC1)



LPC1 is a 14-pin female pin header for adopting Advantech LPC module.

Chapter 3

AMI BIOS Setup

3.1 Introduction

With the AMI BIOS Setup program, you can modify BIOS settings and control the special features of your computer. The Setup program uses a number of menus for making changes and turning the special features on or off. This chapter describes the basic navigation of the PCA-6029 setup screens.

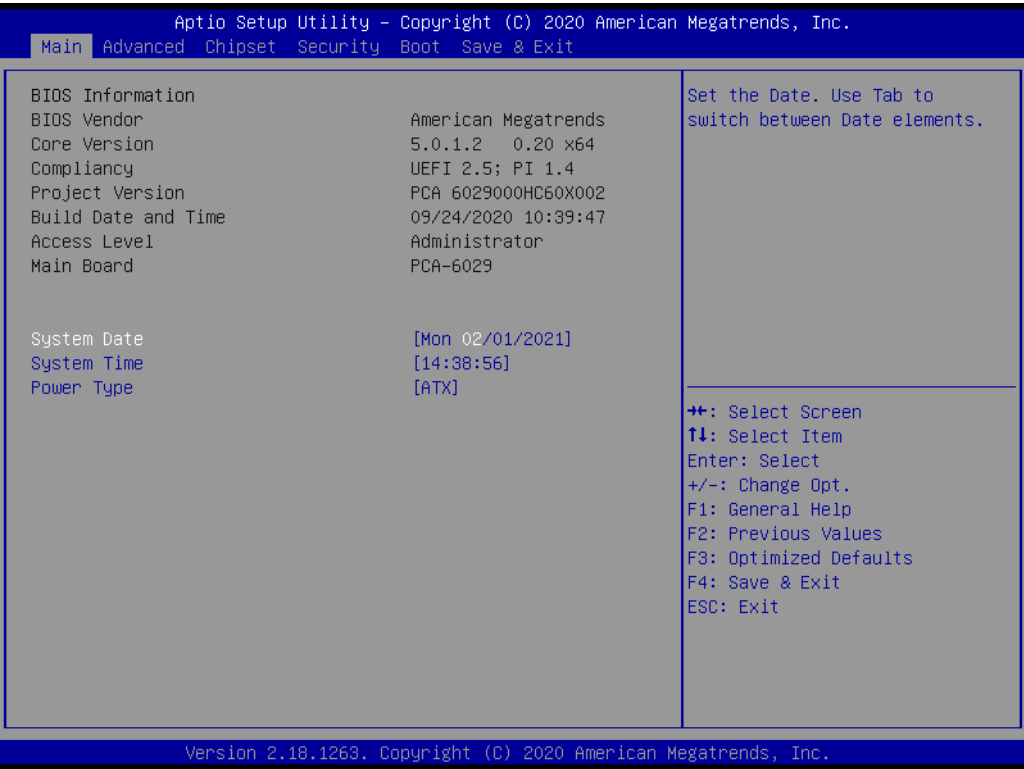


Figure 3.1 Setup program initial screen

3.2 Entering Setup

Turn on the computer and the BIOS is activated as well. The setup program can be triggered by pressing "DEL" or "F2" key.

Note! *If the message disappears before you press the "DEL" or "F2" key, please restart the computer and try it again.*



3.2.1 Main Setup

When you first enter the BIOS Setup Utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab. There are two Main Setup options. They are described in this section. The Main BIOS Setup screen is shown below.

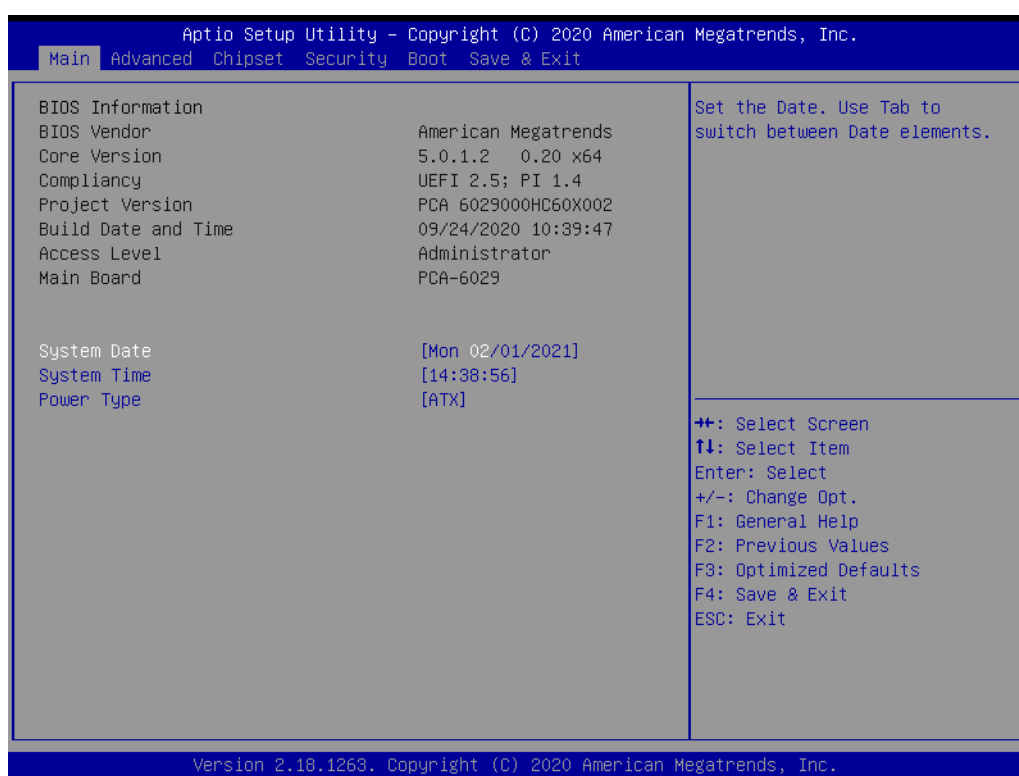


Figure 3.2 Main setup screen

The Main BIOS setup screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured; options in blue can. The right frame displays the key legend.

Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

■ System Time / System Date

Use this option to change the system time and date. Highlight System Time or System Date using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in MM/DD/YY format. The time must be entered in HH:MM:SS format.

3.2.2 Advanced BIOS Features Setup

Select the Advanced tab from the PCA-6029 setup screen to enter the Advanced BIOS Setup screen. You can select any of the items in the left frame of the screen, such as CPU Configuration, to go to the sub menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced BIOS Setup options are described in this section. The Advanced BIOS Setup screen is shown below, and the sub menus are described on the following pages.

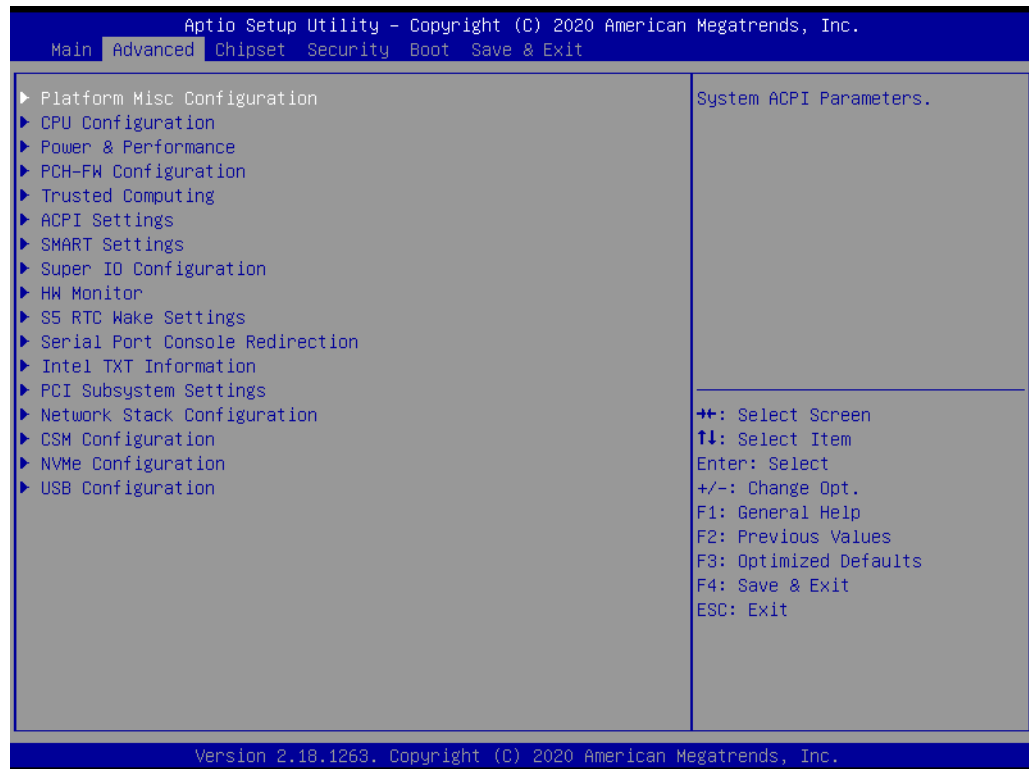


Figure 3.3 Advanced BIOS features setup screen

3.2.2.1 Platform Misc Configuration

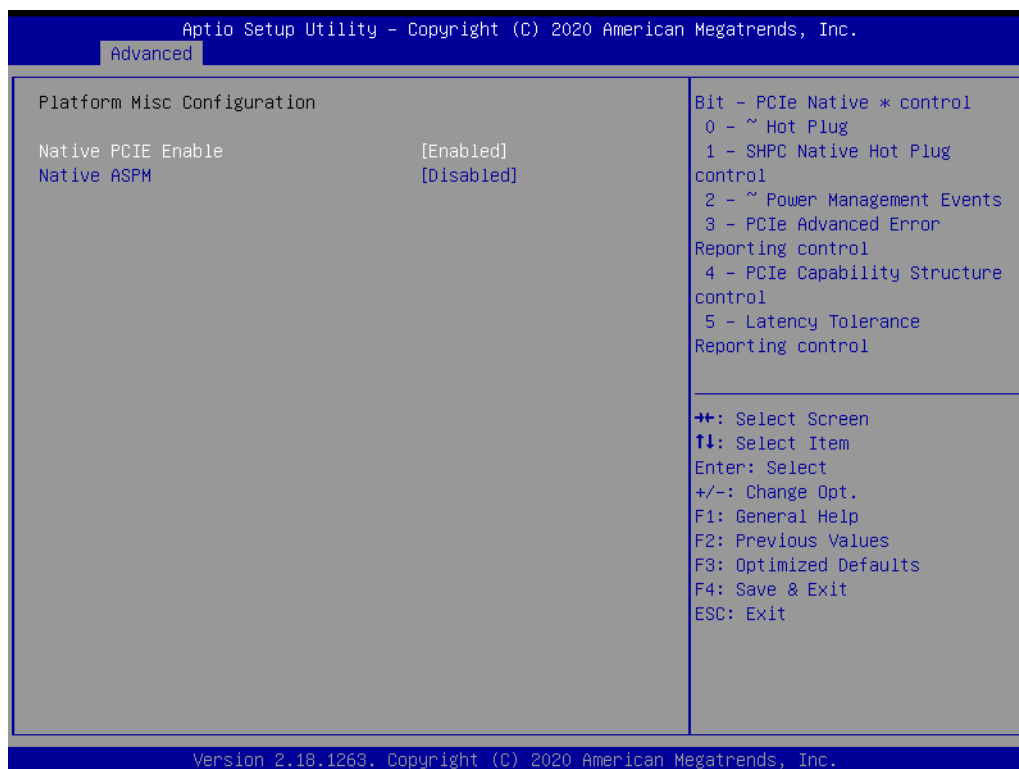


Figure 3.4 Native PCIE Enable setup screen



Figure 3.5 Native ASPM setup screen

- **Native PCIe Enable**
 - Bit - PCIe Native * control
 - 0 - ~ Hot Plug
 - 1 - SHPC Native Hot Plug control
 - 2 - ~ Power Management Events
 - 3 - PCIe Advanced Error Reporting control
 - 4 - PCIe Capability Structure control
 - 5 - Latency Tolerance Reporting control
- **Native ASPM**
 - Enabled - OS controlled ASPM
 - Disabled - BIOS controlled ASPM

3.2.2.2 CPU Configuration

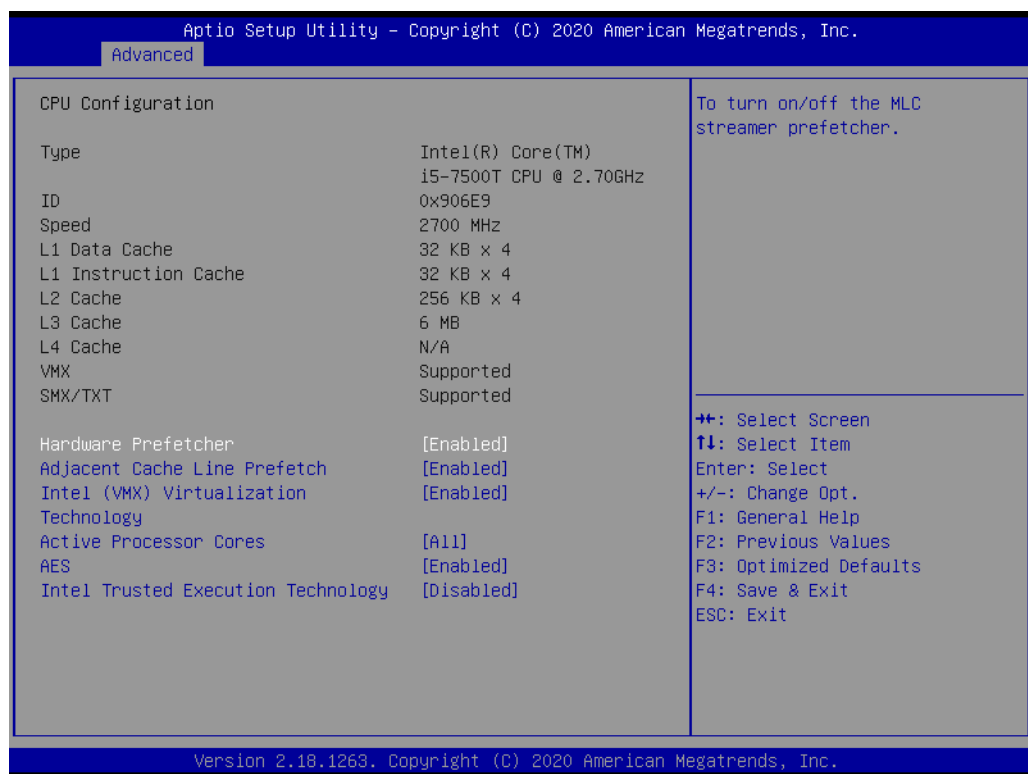


Figure 3.6 Hardware Prefetcher setup screen

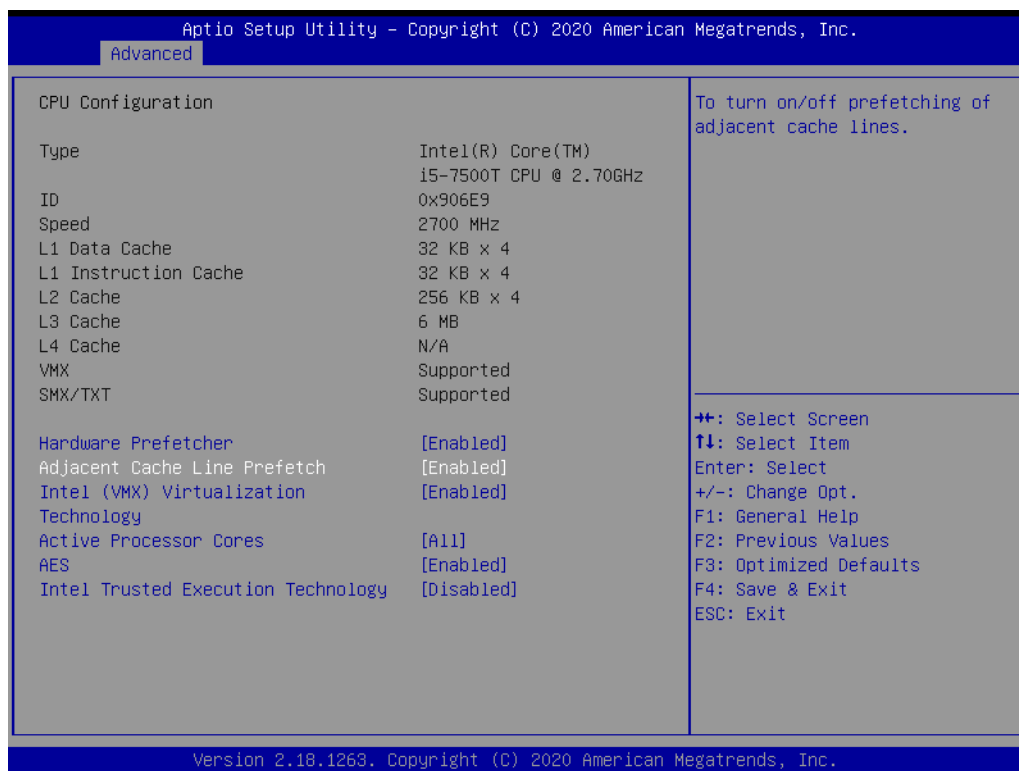


Figure 3.7 Adjacent Catch Line Prefetch setup screen

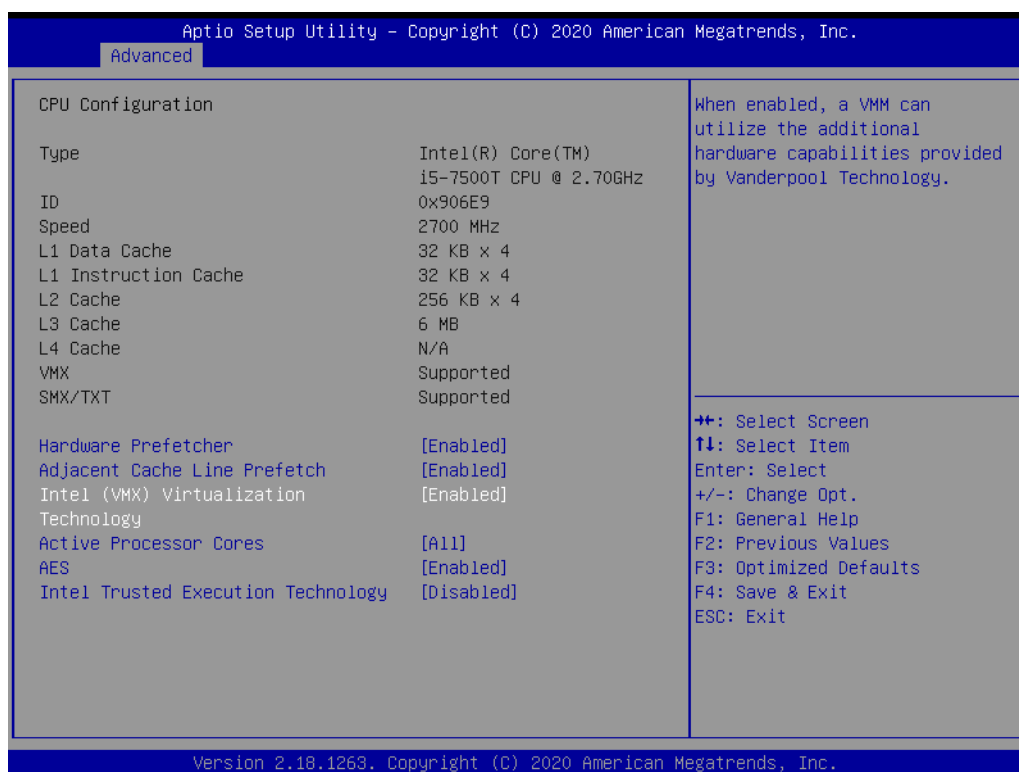


Figure 3.8 Intel (VMX) Virtualization Technology setup screen

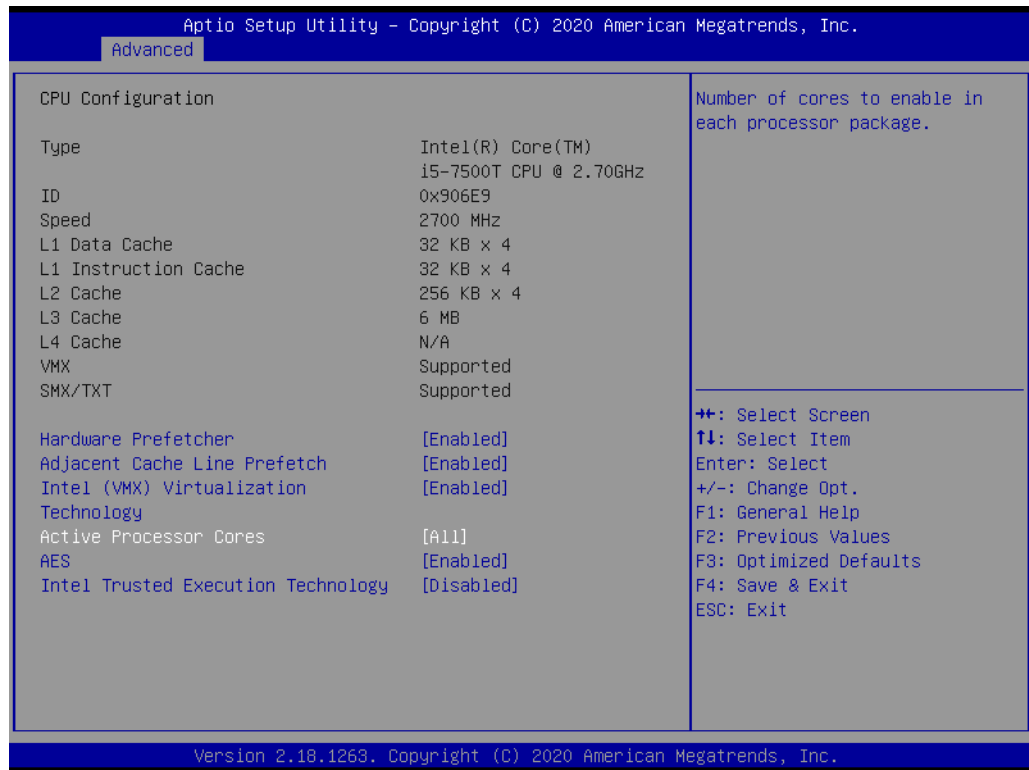


Figure 3.9 Active Processor Cores setup screen



Figure 3.10 AES setup screen



Figure 3.11 Intel Trusted Execution Technology setup screen

- **Hardware Prefetcher**
To turn on/off the MLC streamer prefetcher.
- **Adjacent Cache Line Prefetch**
To turn on/off prefetching of adjacent cache lines.
- **Intel (VMX) Virtualization**
When enabled, virtualization can utilize the additional hardware capabilities provided by Vanderpool Technology.
- **Active Processor Cores**
Number of cores to enable in each processor package.
- **AES**
Enable or disable AES (Advanced Encryption Standard)
- **Intel Trusted Execution Technology**
Enables utilization of additional hardware capabilities provided by Intel (R) Trusted Execution Technology.
Changes require a full power reboot to take effect.

3.2.2.3 Power & Performance

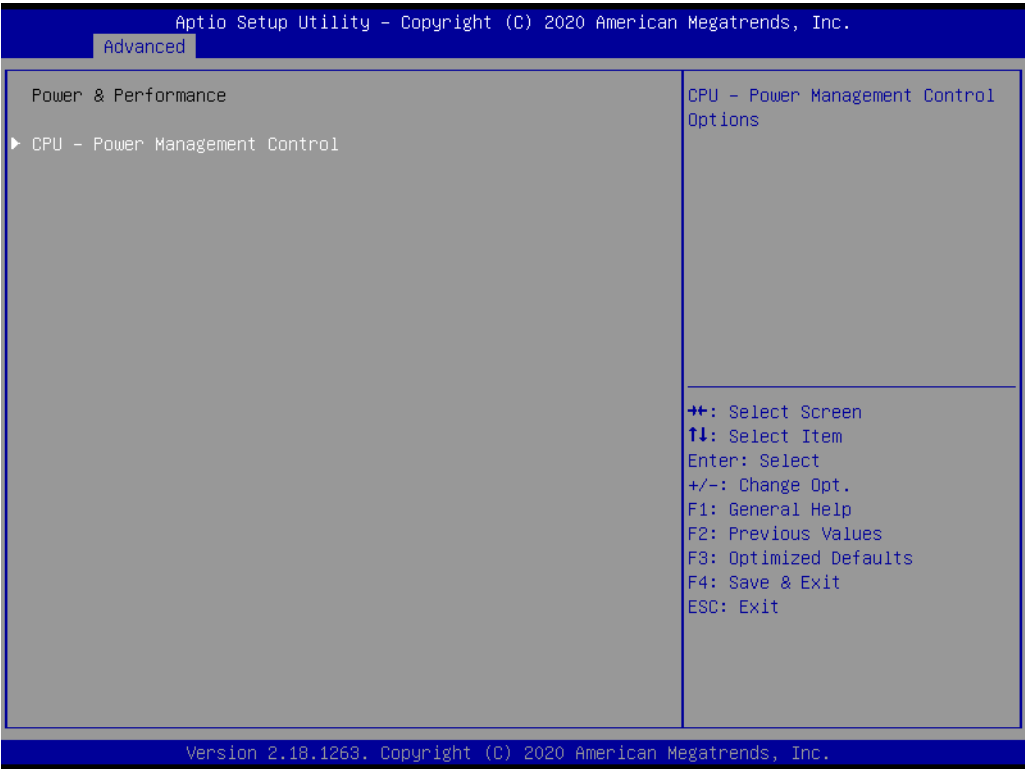


Figure 3.12 CPU - Power Management Control setup screen

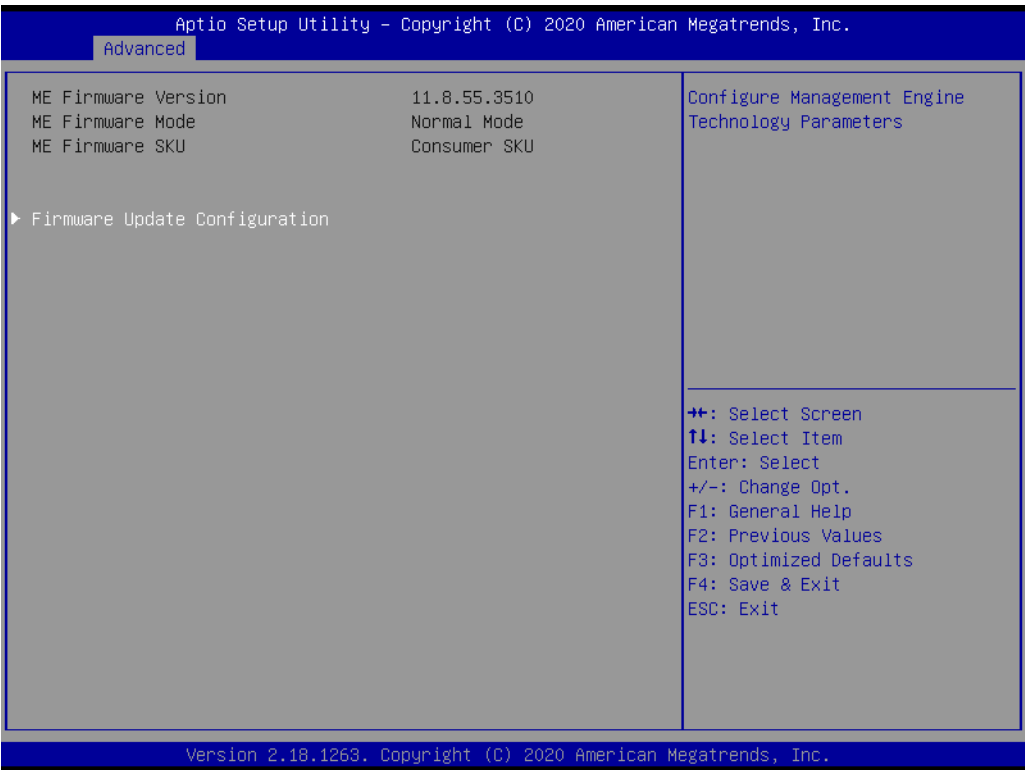


Figure 3.13 Firmware Update Configuration setup screen

- **CPU – Power Management Control**
CPU – Power Management Control
- **Firmware Update Configuration**
Configure Management Engine Technology Parameters

3.2.2.4 Configuration



Figure 3.14 Security Device Support setup screen

■ **Security Device Support**

Enable or disable BIOS support for security device. OS will not show Security Device. TCG EFI protocol and INT1A Interface will not be available.

3.2.2.5 ACPI Settings

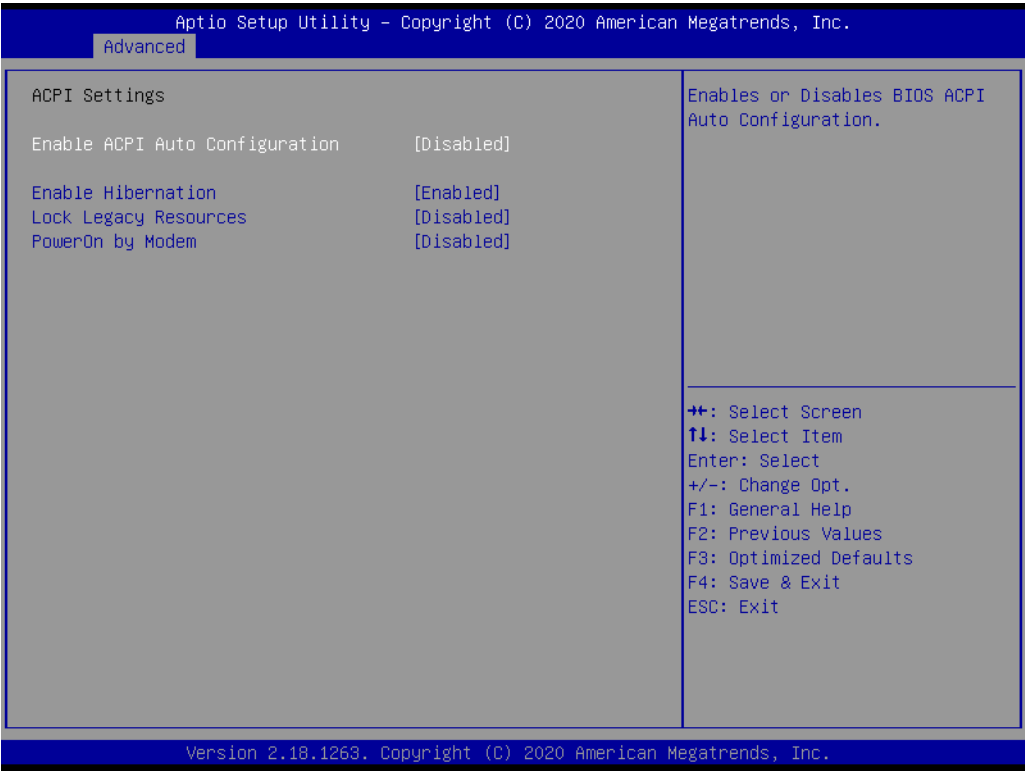


Figure 3.15 Enable ACPI Auto Configuration setup screen

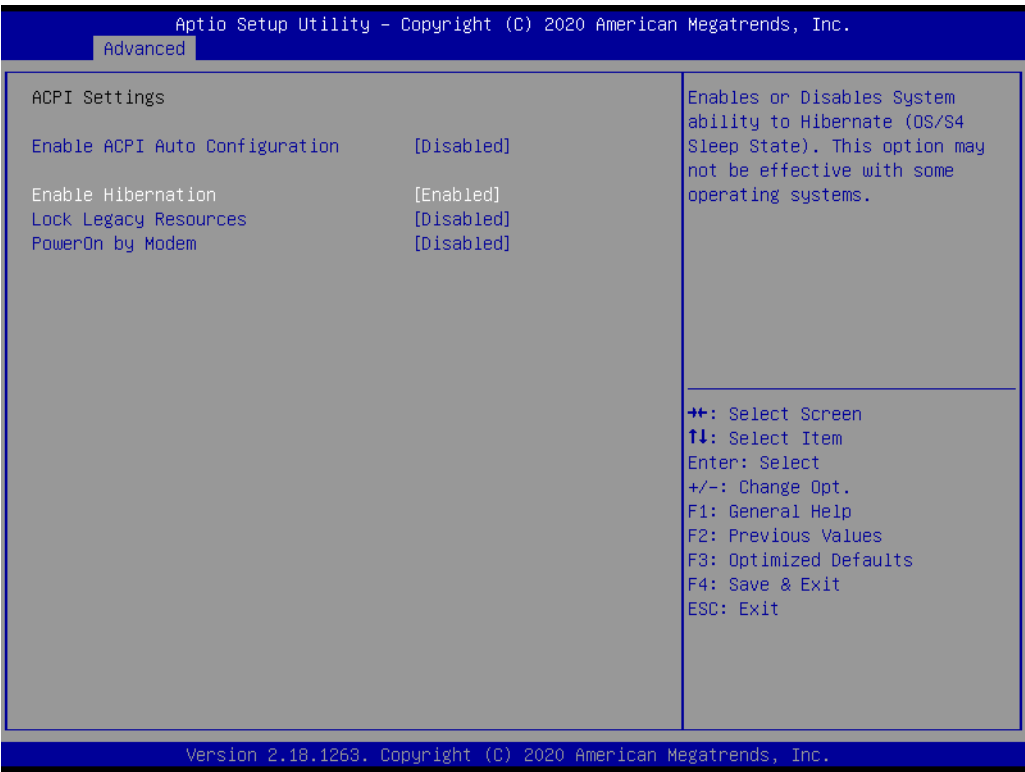


Figure 3.16 Enable Hibernation setup screen

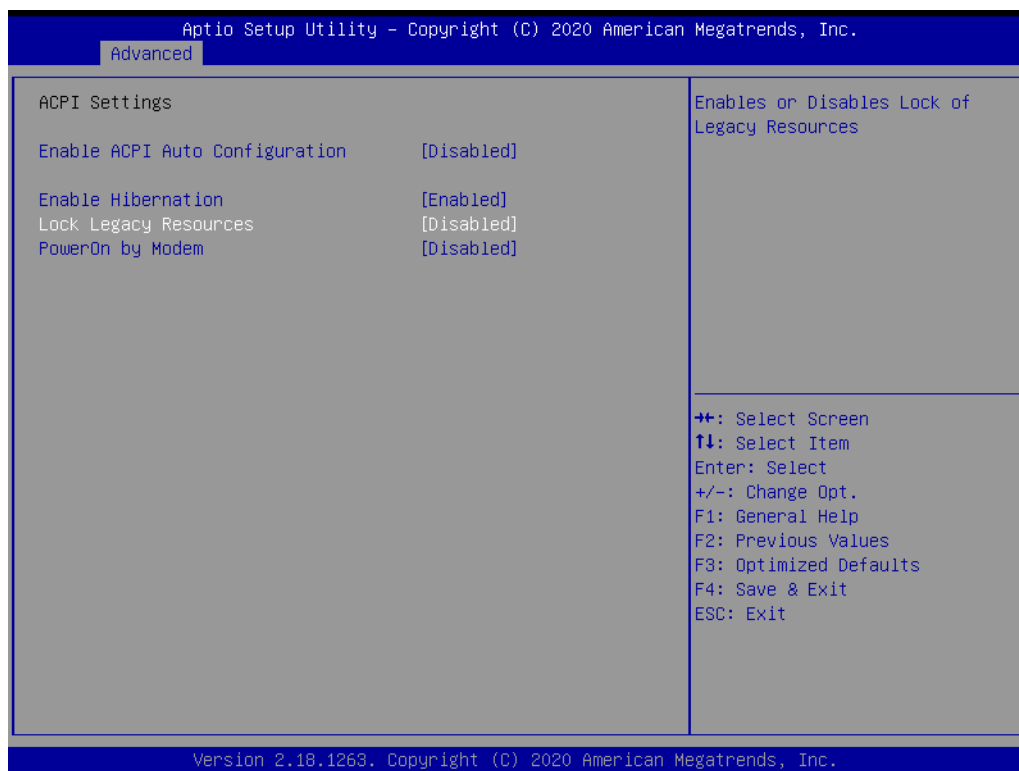


Figure 3.17 Lock Legacy Resources setup screen



Figure 3.18 PowerOn by Modem setup screen

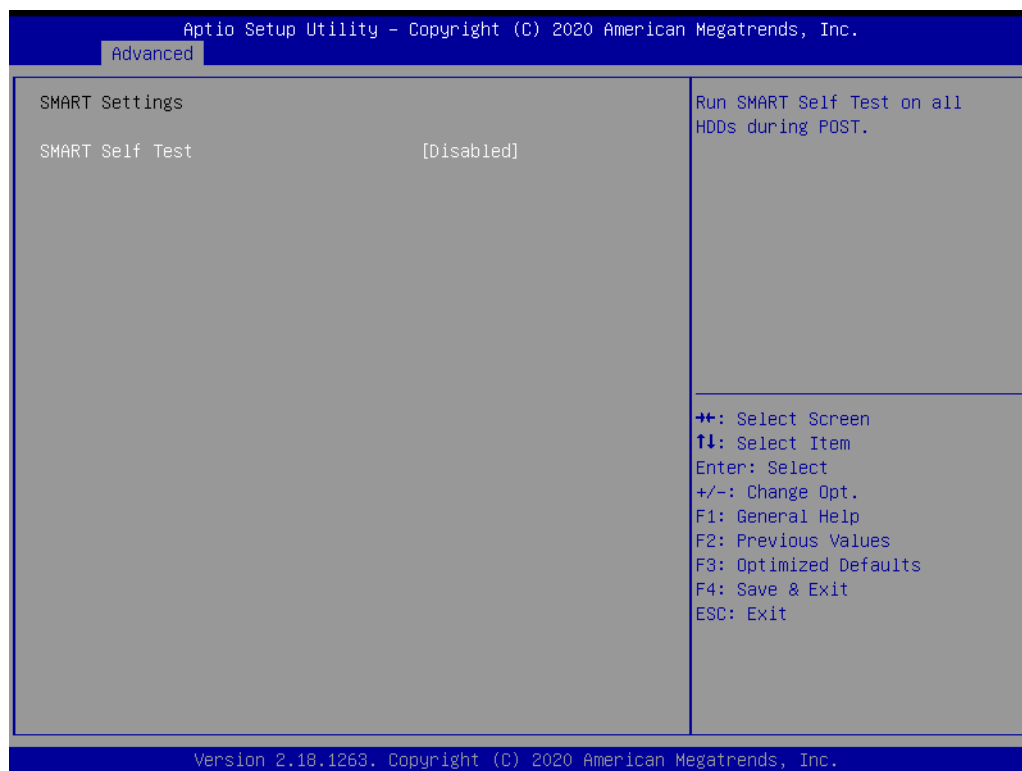


Figure 3.19 SMART Self Test setup screen

- **Enable ACPI Auto Configuration**
Enable or disable BIOS ACPI Auto Configuration.
- **Enable Hibernation**
Enable or disable System ability to Hibernate (OS/S4 Sleep State). This option may not be effective with some operating systems.
- **Lock Legacy Resources**
Enable or disable Lock of Legacy Resources.
- **PowerOn by Modem**
Enable or disable Lock of Legacy Resources.
- **SMART Settings**
Run SMART Self Test on all HDDs during POST.

3.2.2.6 Super IO Configuration

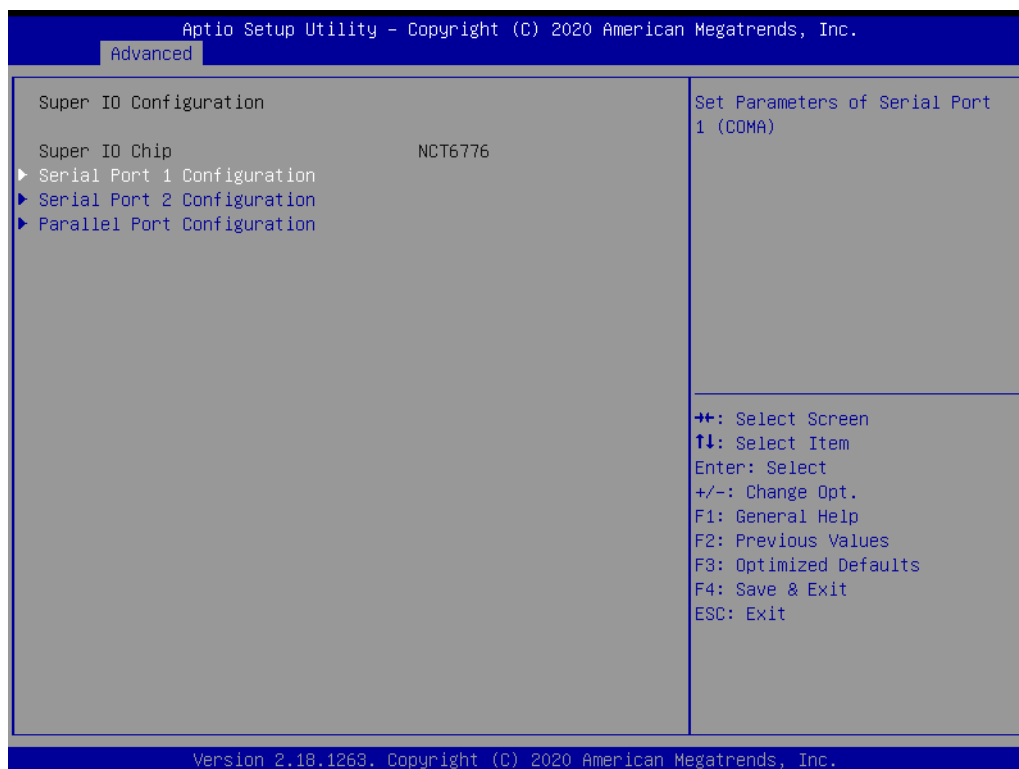


Figure 3.20 Serial Port 1 Configuration setup screen

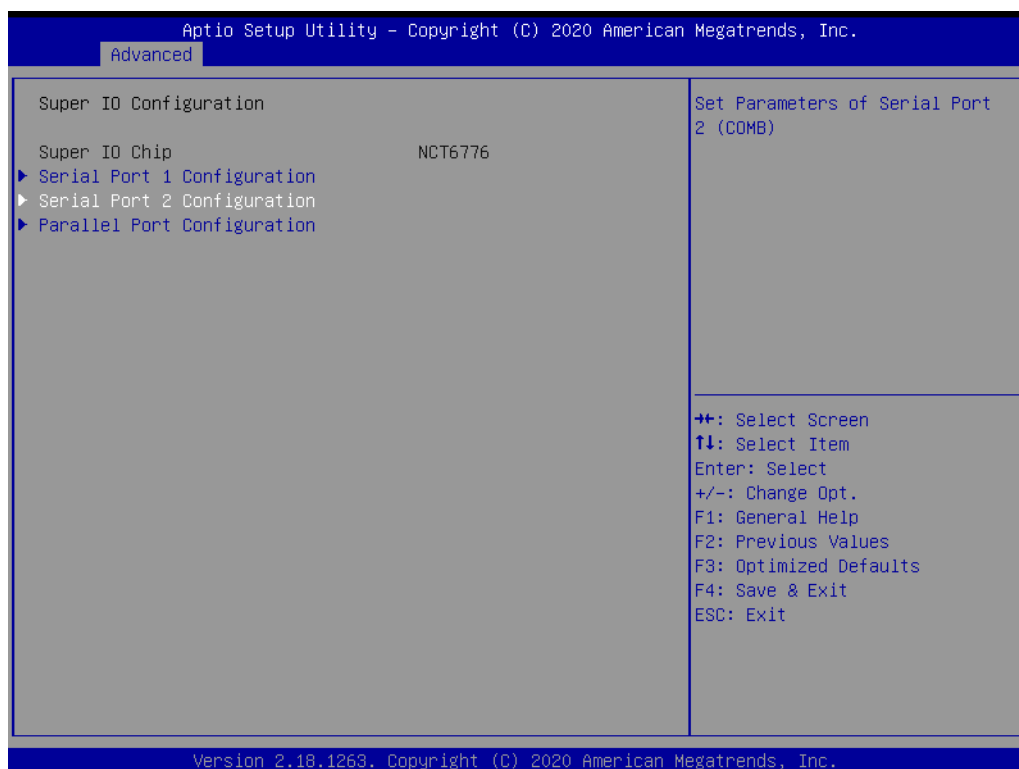


Figure 3.21 Serial Port 2 Configuration setup screen

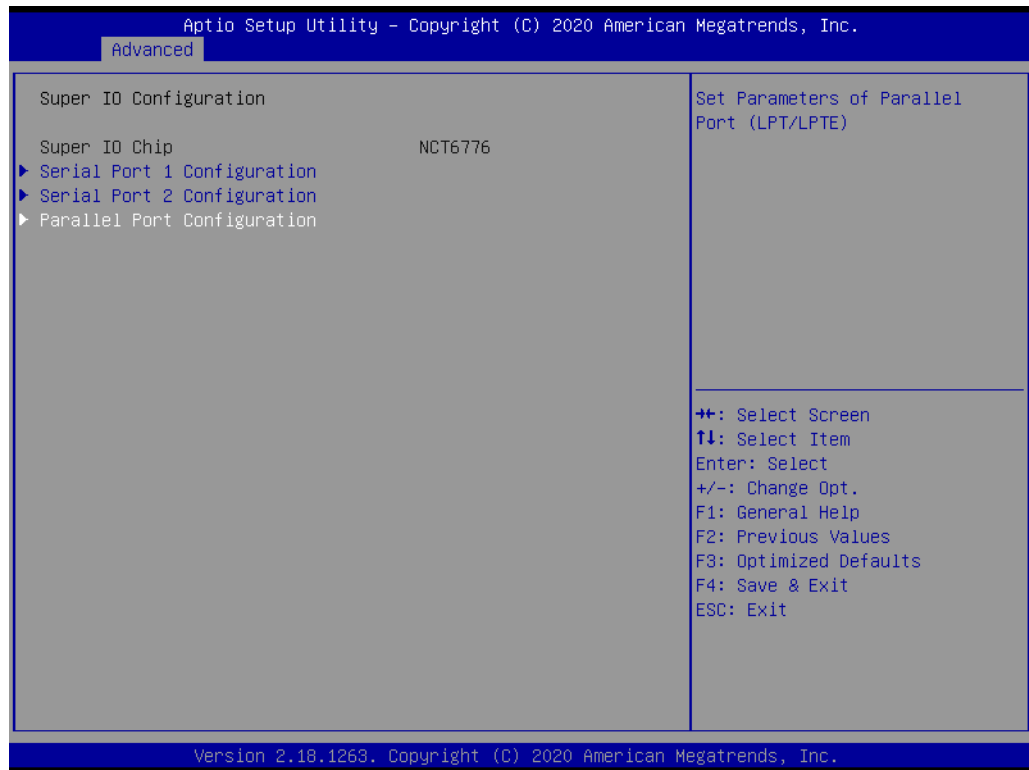


Figure 3.22 Parallel Port Configuration setup screen

- **Serial Port 1 Configuration**
Sets Parameters of Serial Port 1 (COMA).
- **Serial Port 2 Configuration**
Sets Parameters of Serial Port 2 (COMB).
- **Parallel Port1 Configuration**
Sets Parameters of Parallel Port (LPT/LPTE).

3.2.2.7 PC Health Status

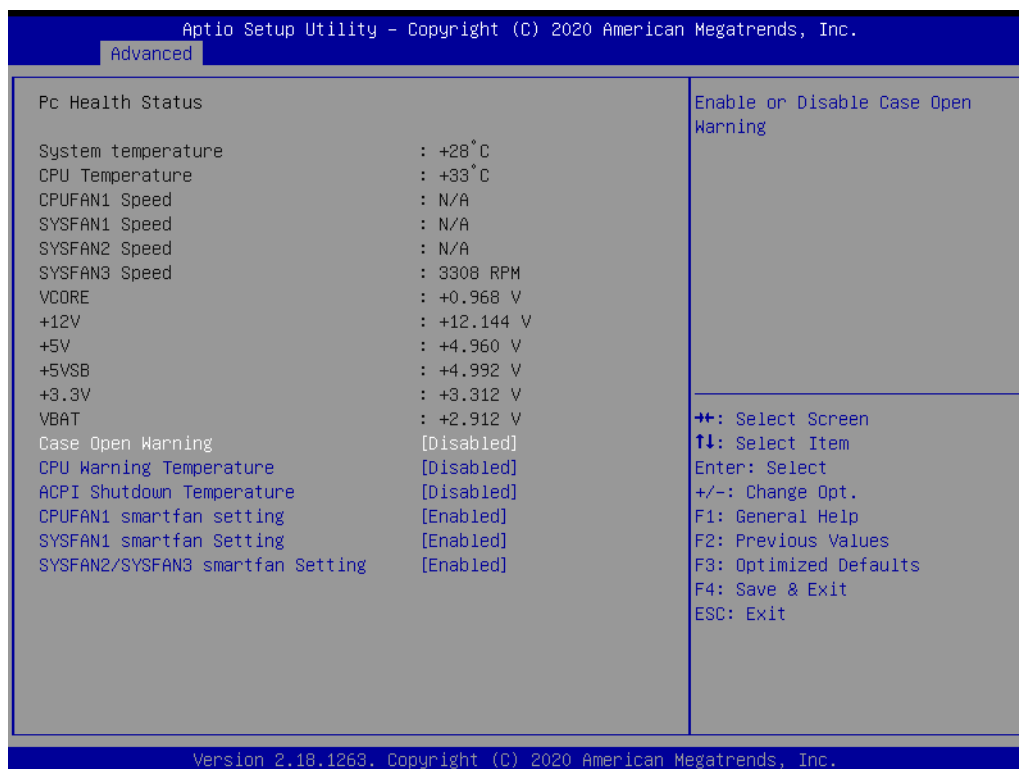


Figure 3.23 Case Open Warning setup screen

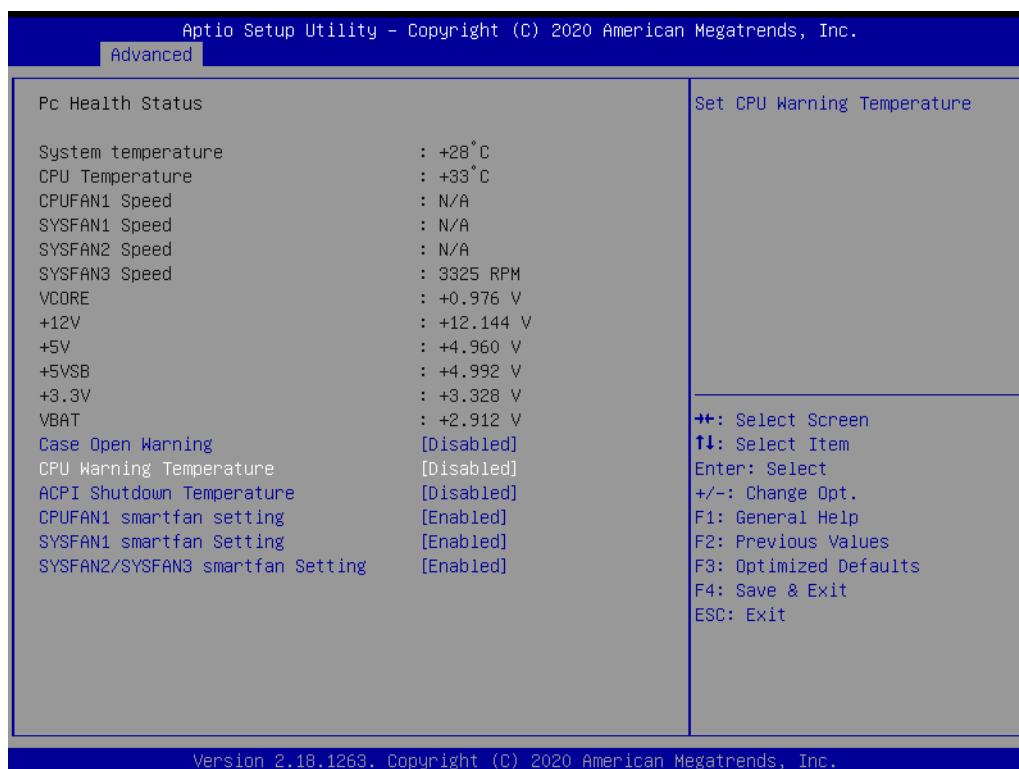


Figure 3.24 CPU Warning Temperature setup screen

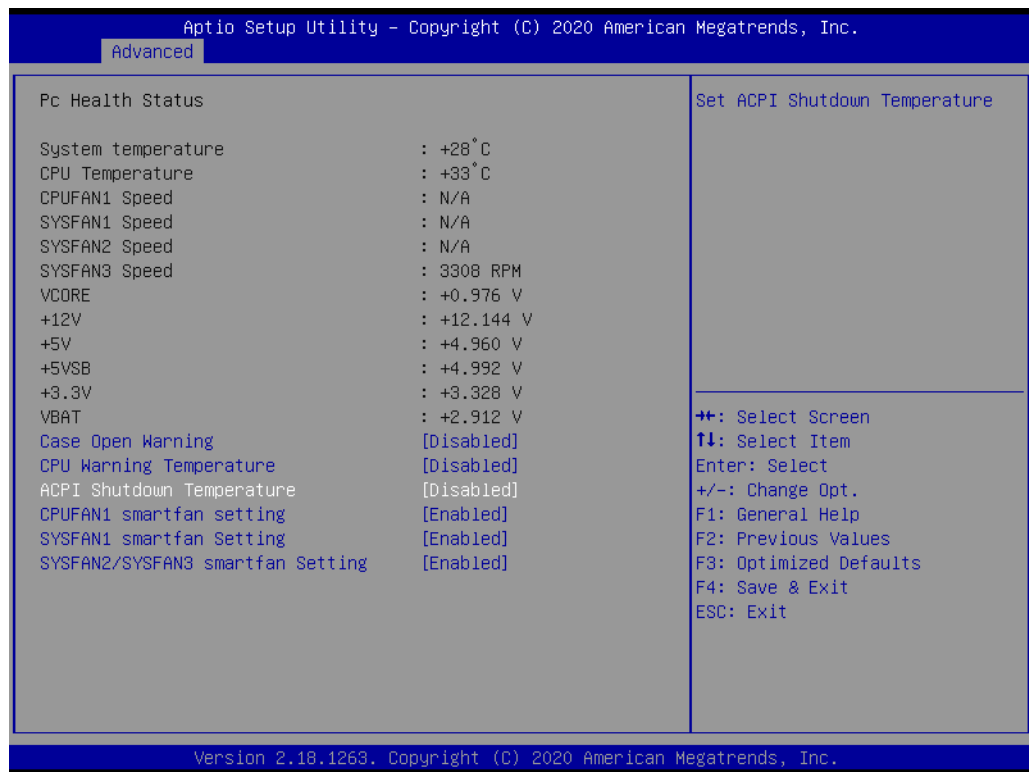


Figure 3.25 ACPI Shutdown Temperature setup screen

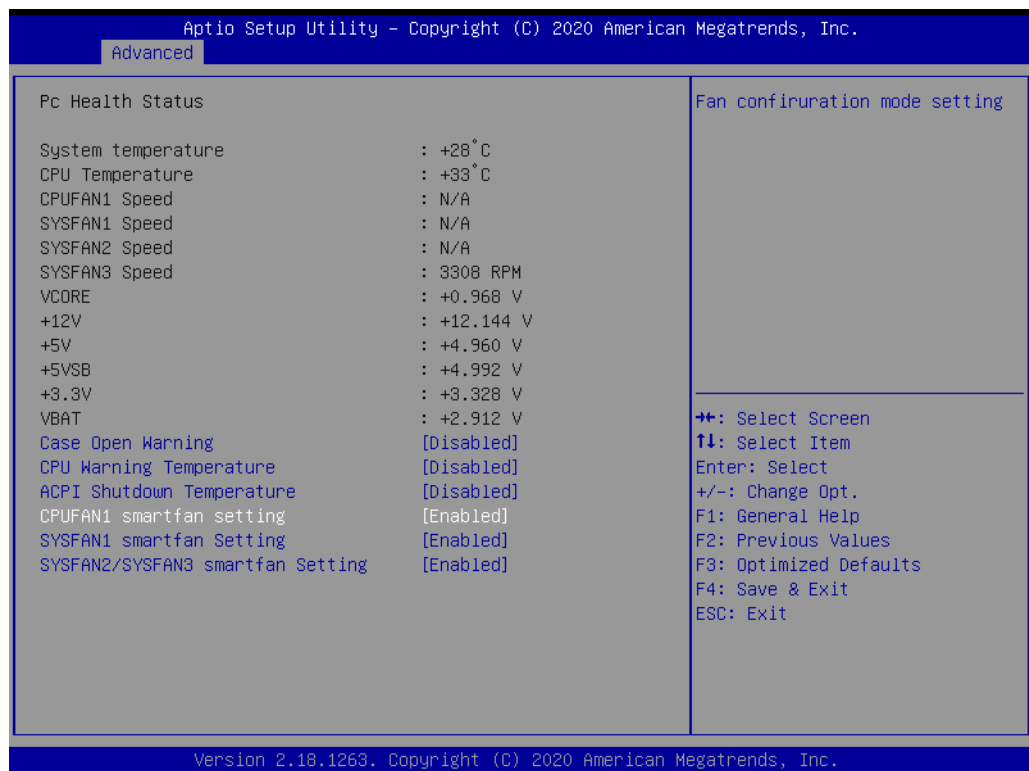


Figure 3.26 CPUFAN1 smartfan setting setup screen

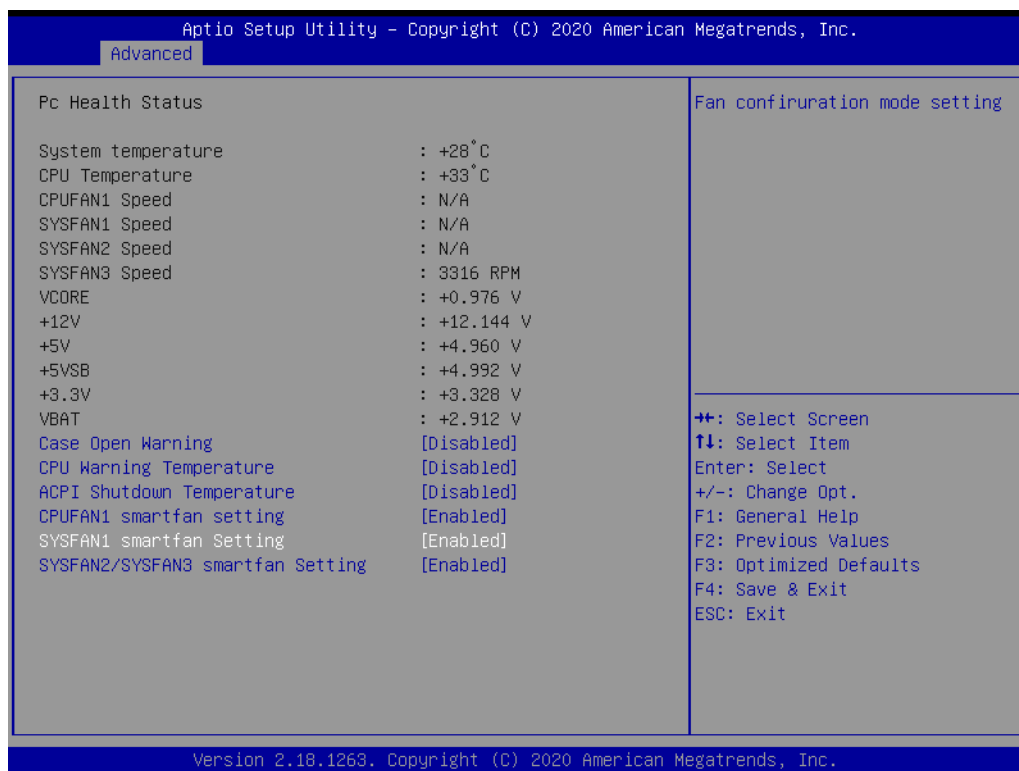


Figure 3.27 SYSFAN1 smartfan setting setup screen

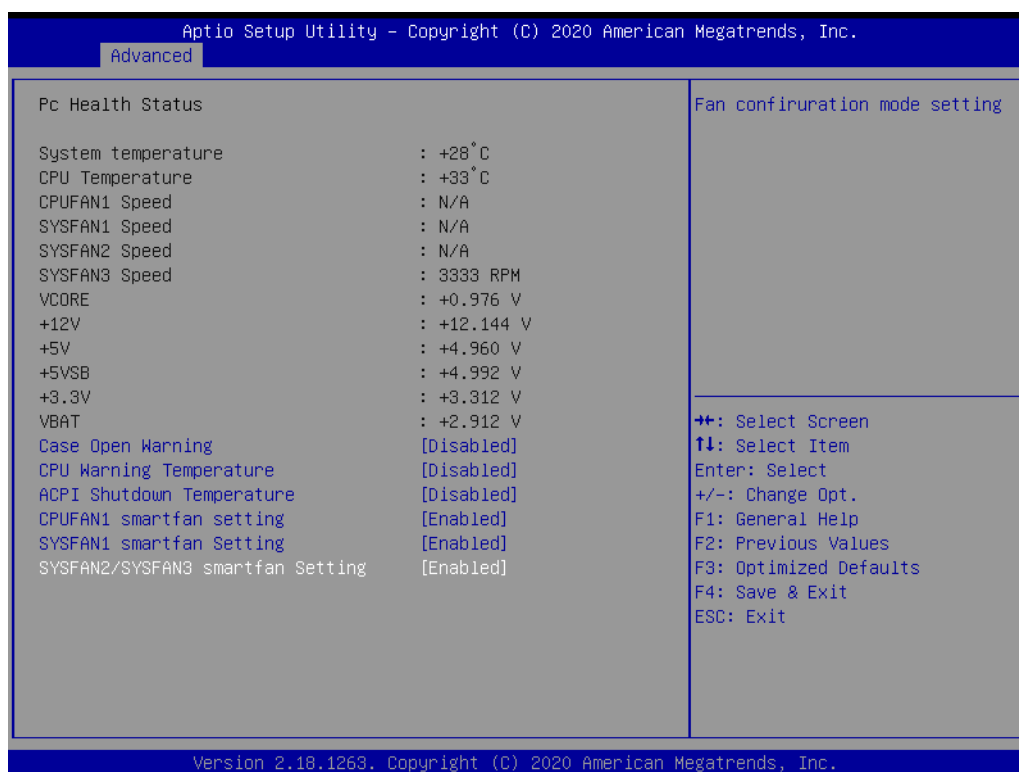


Figure 3.28 SYSFAN2 smartfan setting setup screen

- **Case Open Warning**
Sets ACPI Shutdown Temperature.
- **CPU Warning Temperature**
Sets CPU Warning Temperature.

- **ACPI Shutdown Temperature**
Sets ACPI Shutdown Temperature.
- **CPU/Fan1 smartfan setting**
Fan configuration mode setting.
- **SYSFAN1 smartfan setting**
Fan configuration mode setting.
- **SYSFAN1 smartfan setting**
Fan configuration mode setting.
- **SYSFAN2/SYSFAN3 smartfan Setting**
Fan configuration mode setting.

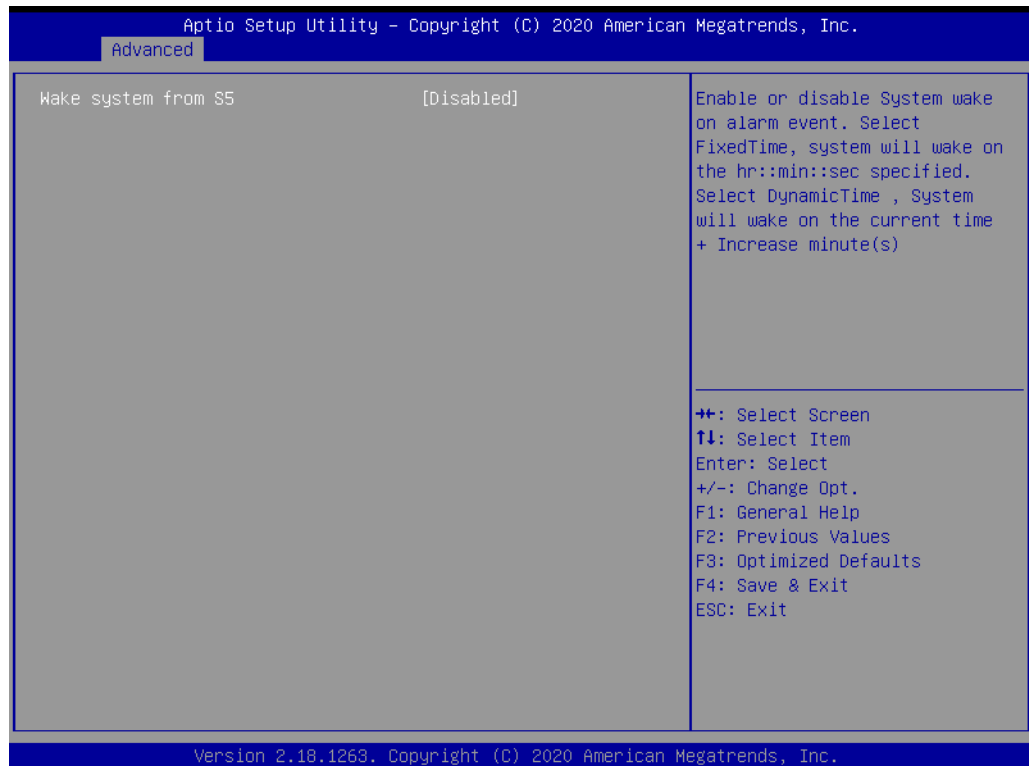


Figure 3.29 Wake system from S5 setup screen

- **Wake from S5**
Enable or disable system wake on alarm event. Select FixedTime, system will wake on the hr:min:spec specified. Select DynamicTime, system will wake on the current time +increase in minutes.

Figure 3.30 Console Redirection setup screen

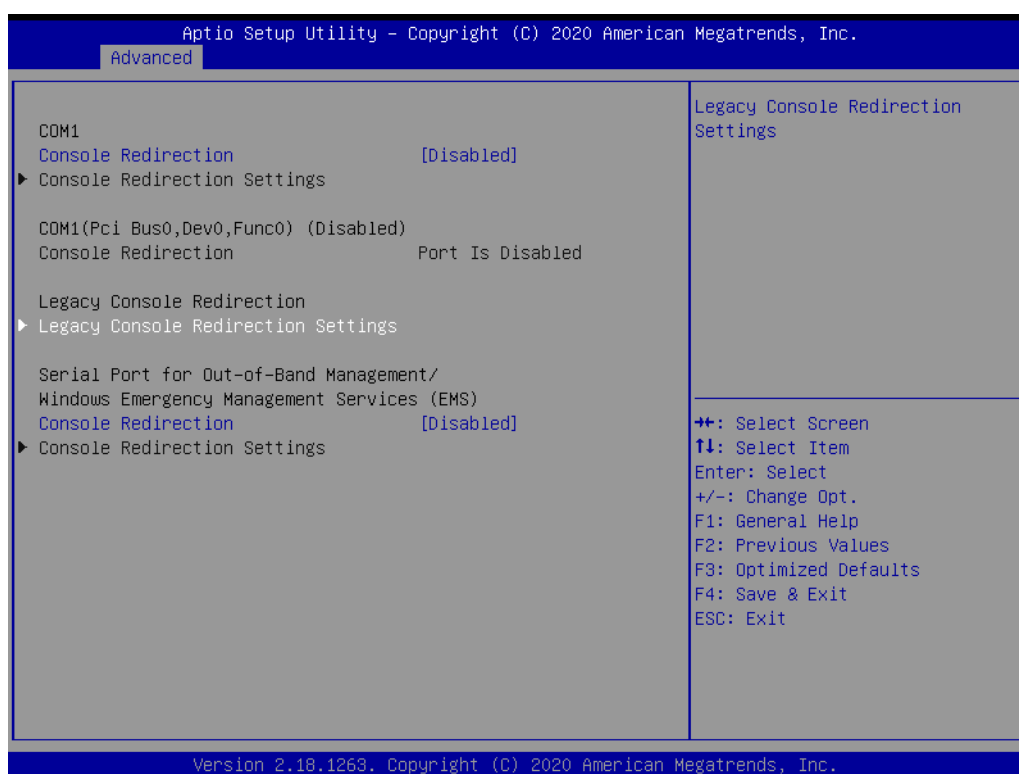


Figure 3.31 Legacy Console Redirection Settings setup screen

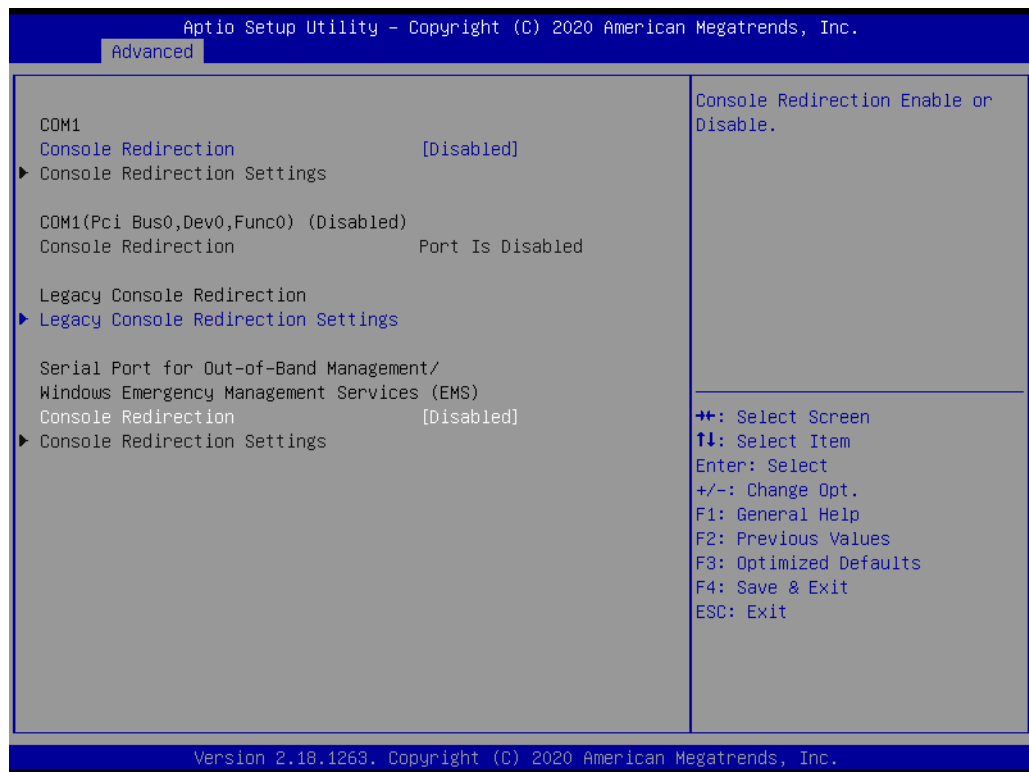


Figure 3.32 Console Redirection setup screen

- **Console Redirection**
Console Redirection Enable or disable
- **Legacy Console Redirection Settings**
Legacy console Redirection Settings
- **Console Redirection**
Console Redirection Enable or disable

3.2.2.8 PCI Bus Driver Versions

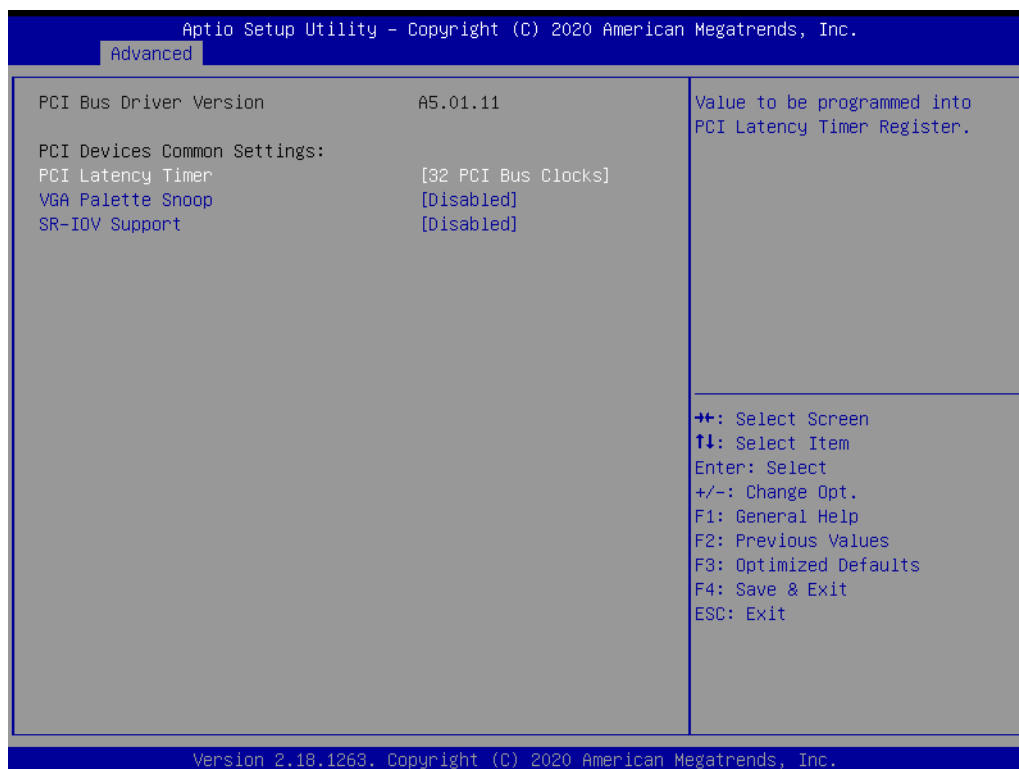


Figure 3.33 PCI Latency Timer setup screen



Figure 3.34 VGA Palette Snoop setup screen

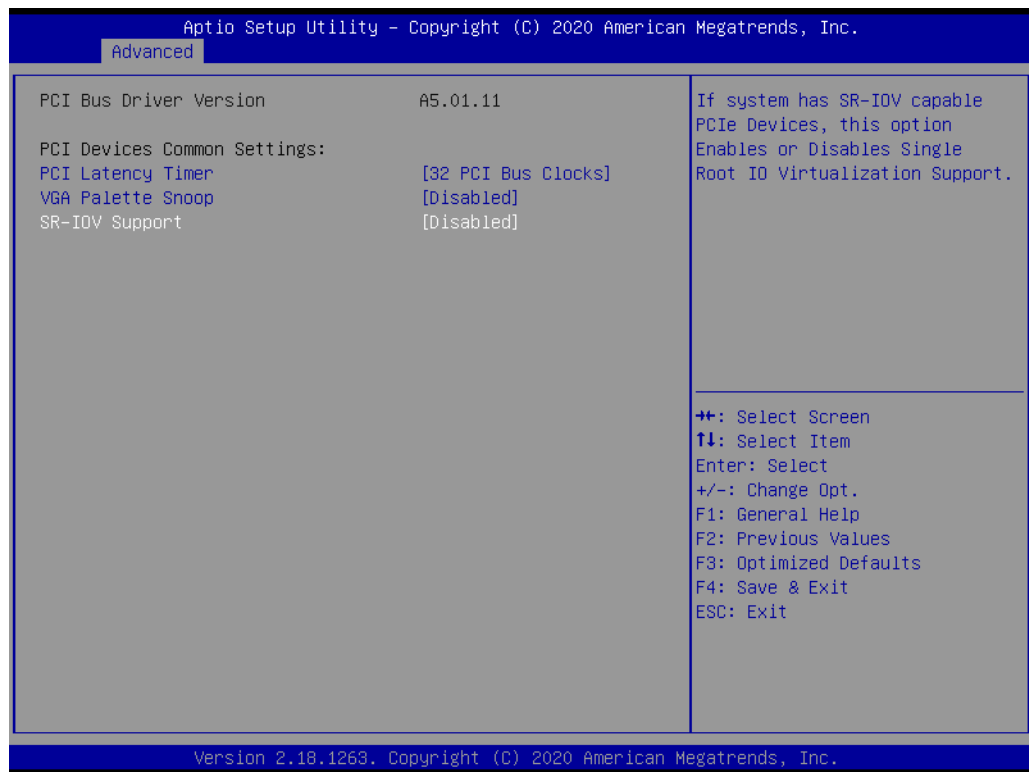


Figure 3.35 SR-IOV Support setup screen

- **PCI Latency Timer**
Value to be programmed into PCI Latency Timer Register
- **VGA Palette Snoop**
Enable or disable VGA Palette Registers Snooping
- **SR-IOV Support**
If system has SR-IOV capable PCIe Devices, this option Enable or disable Single Root IO Virtualization Support.

3.2.2.9 Network Stack



Figure 3.36 Network Stack setup screen

- Enable or disable UEFI Network Stack

3.2.2.10 Compatibility Support Module Configuration

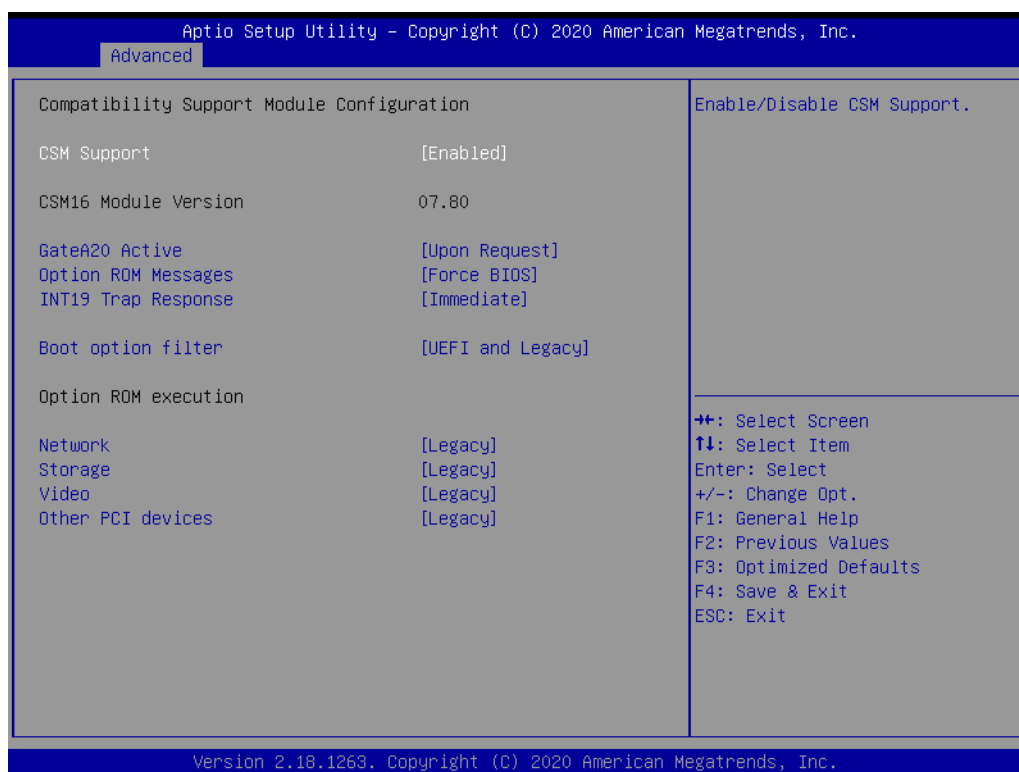


Figure 3.37 CSM Support setup screen

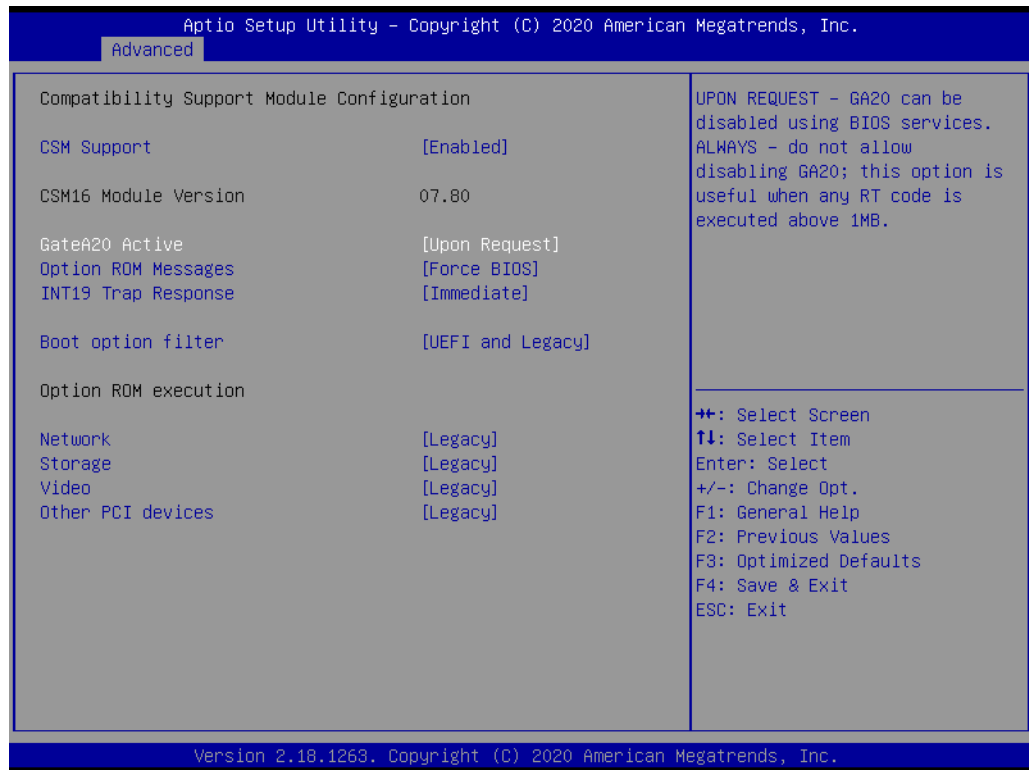


Figure 3.38 GateA20 setup screen

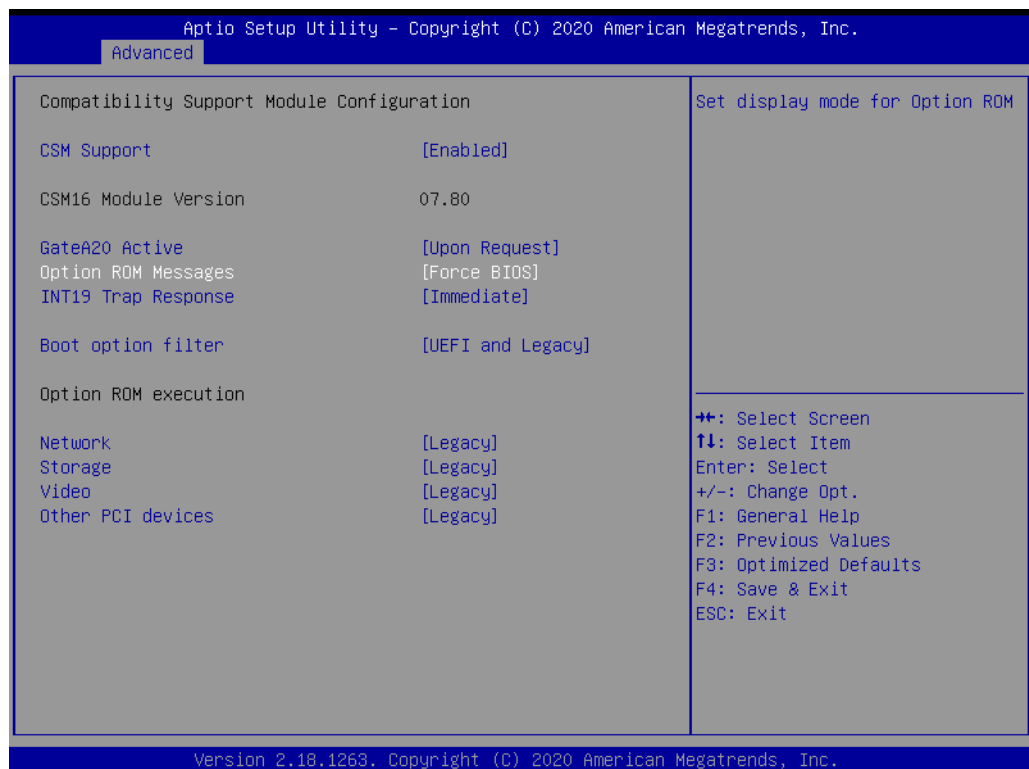


Figure 3.39 Option ROM Message setup screen

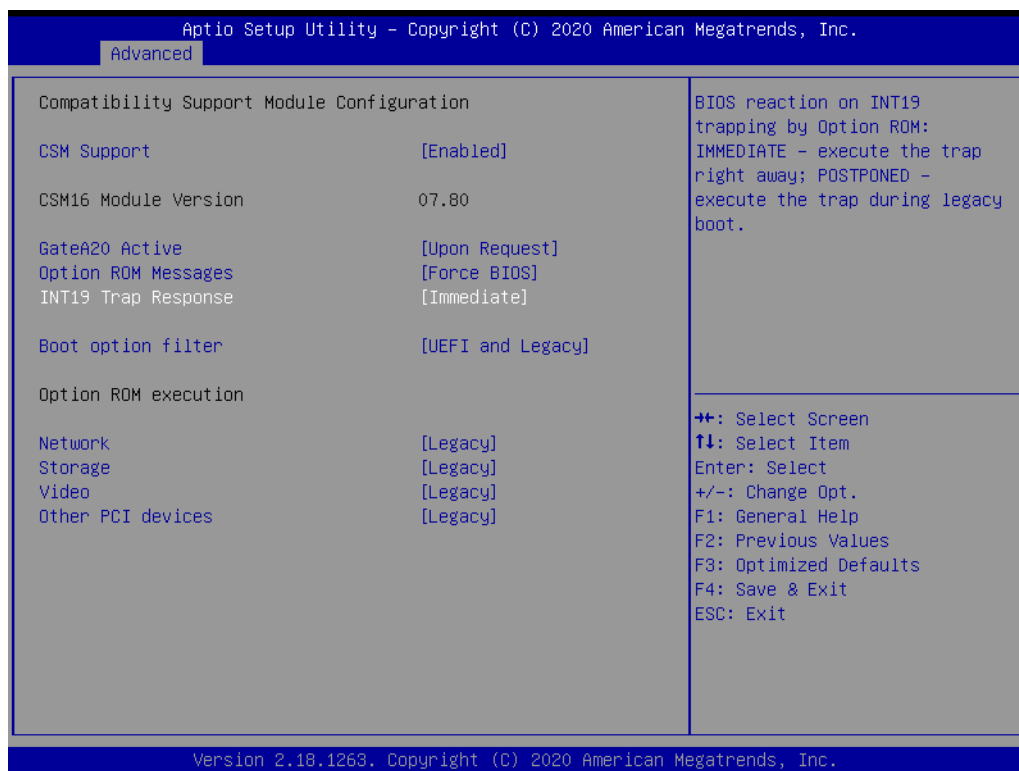


Figure 3.40 INT19 Trap Response setup screen

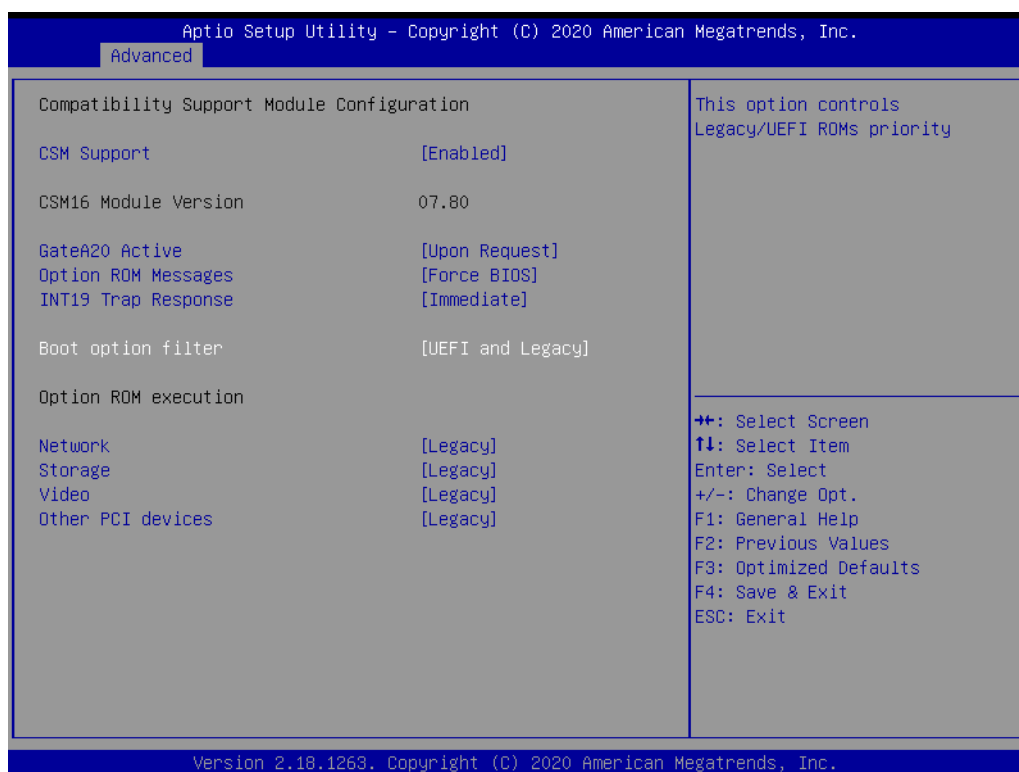


Figure 3.41 Boot option filter setup screen

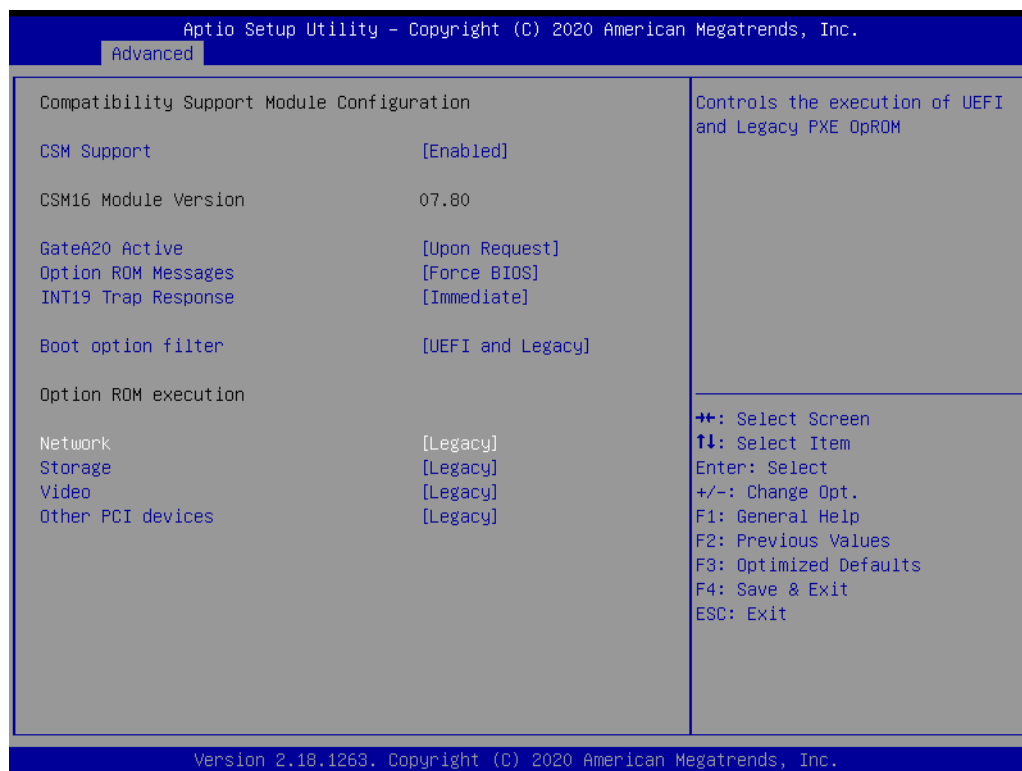


Figure 3.42 Network setup screen

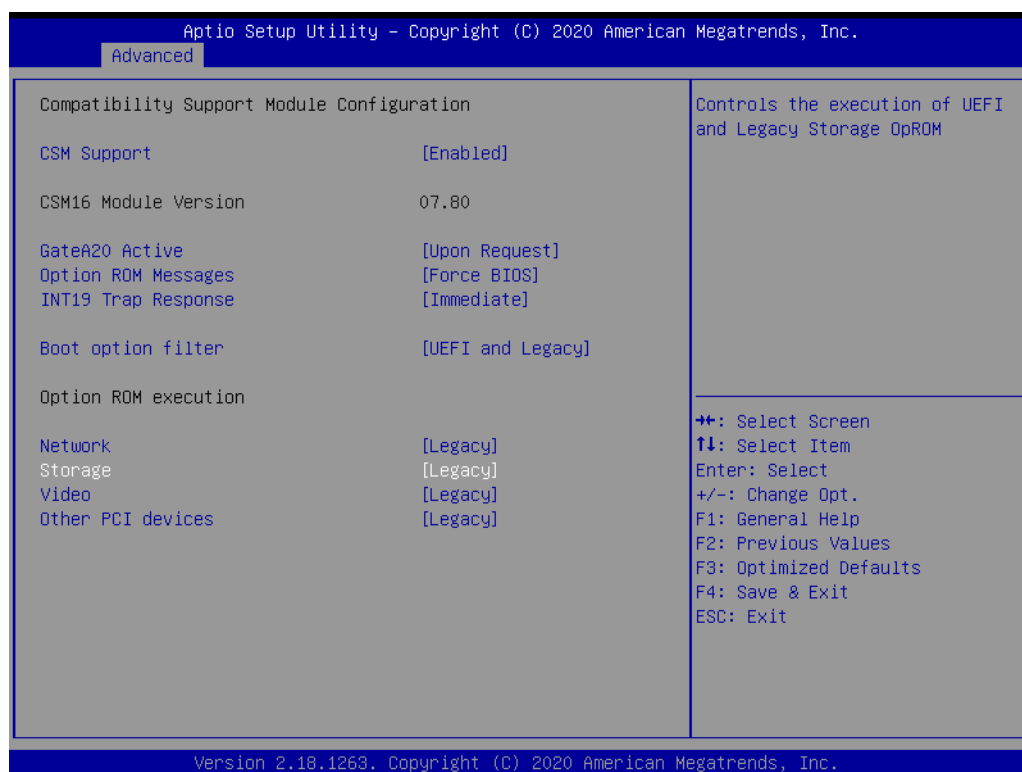


Figure 3.43 Storage setup screen

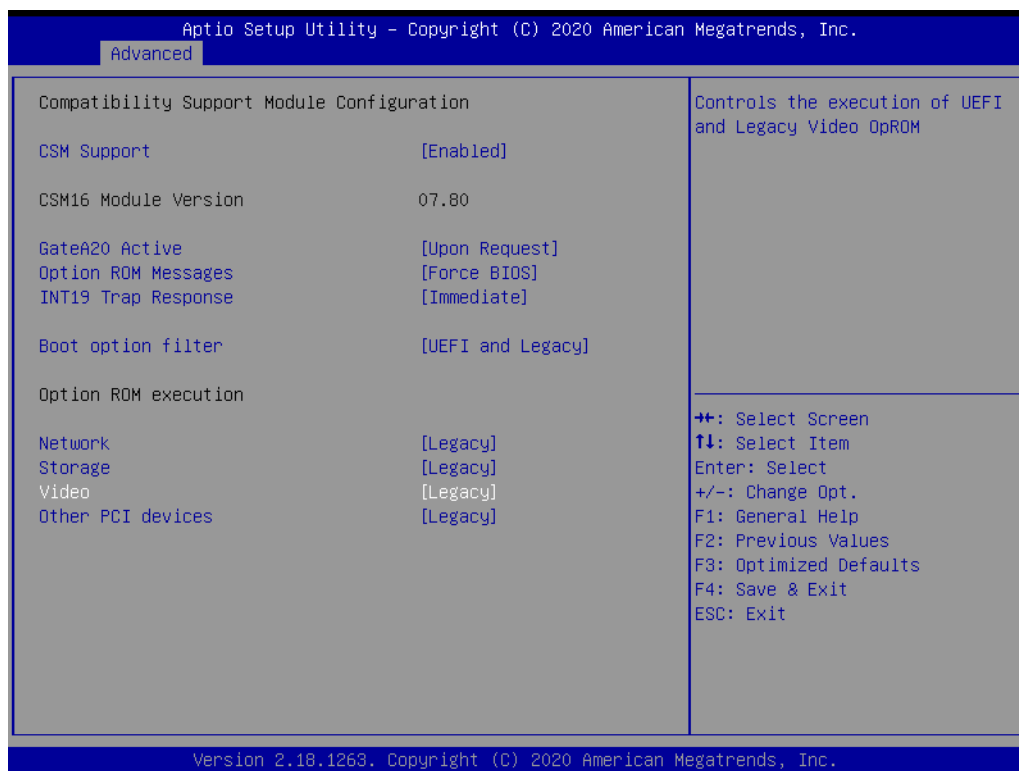


Figure 3.44 Video setup screen



Figure 3.45 Other PCI devices setup screen

- **CSM support**
Enable or disable CSM support

■ **GateA20 Active**

Upon request – GA20 can be disabled using BIOS services.

Always – do not allow disabling of GA20; this option is useful when any RT code is executed above 1MB.

■ **Option ROM Message**

Set disable mode for option ROM

■ **INT19 Trap Response**

BIOS reaction on INT19 trapping by Option ROM: IMMEDIATE - execute the trap right away; POSTONED - execute the trap during legacy boot.

■ **Boot option filter**

This option controls Legacy/UEFI ROMs priority.

■ **Network**

Control the execution of UEFI and Legacy PXE OpROM.

■ **Storage**

Control the execution of UEFI and Legacy Storage OpROM.

■ **Video**

Controls the execution of UEFI and Legacy Video OpROM.

■ **Other PCI devices**

Determines OpROM execution policy for devices other than Network, Storage, or Video.

3.2.2.11 NVMe controller and Drive information



Figure 3.46 NVME controller and Driver information

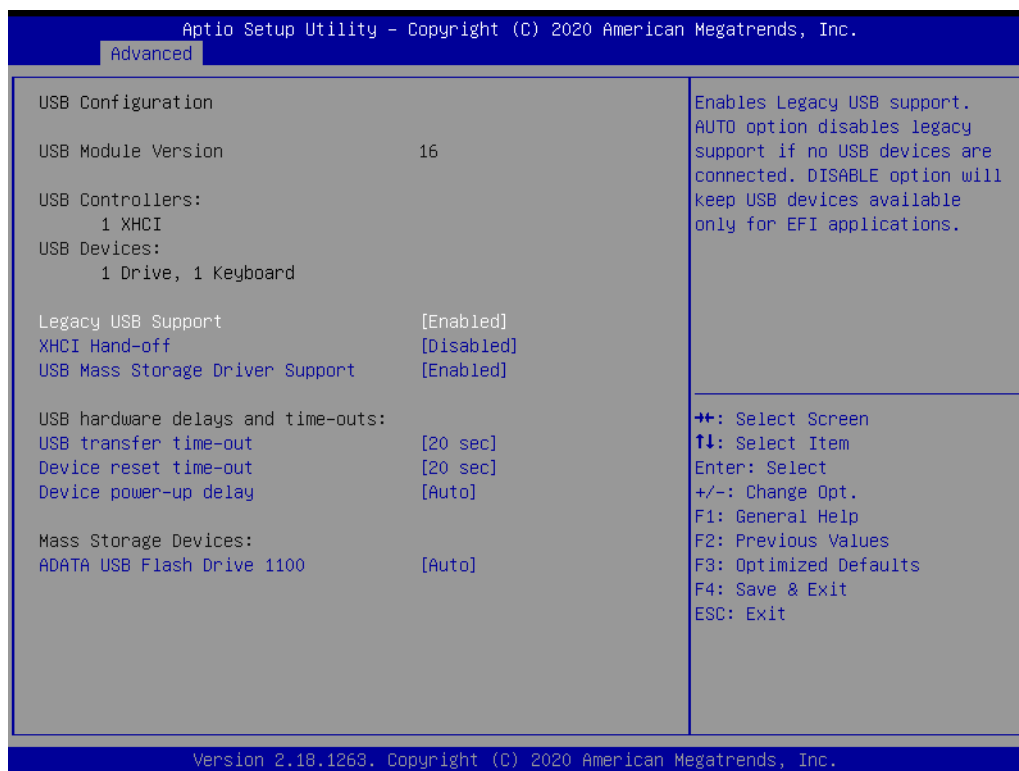


Figure 3.47 Legacy USB Support setup screen

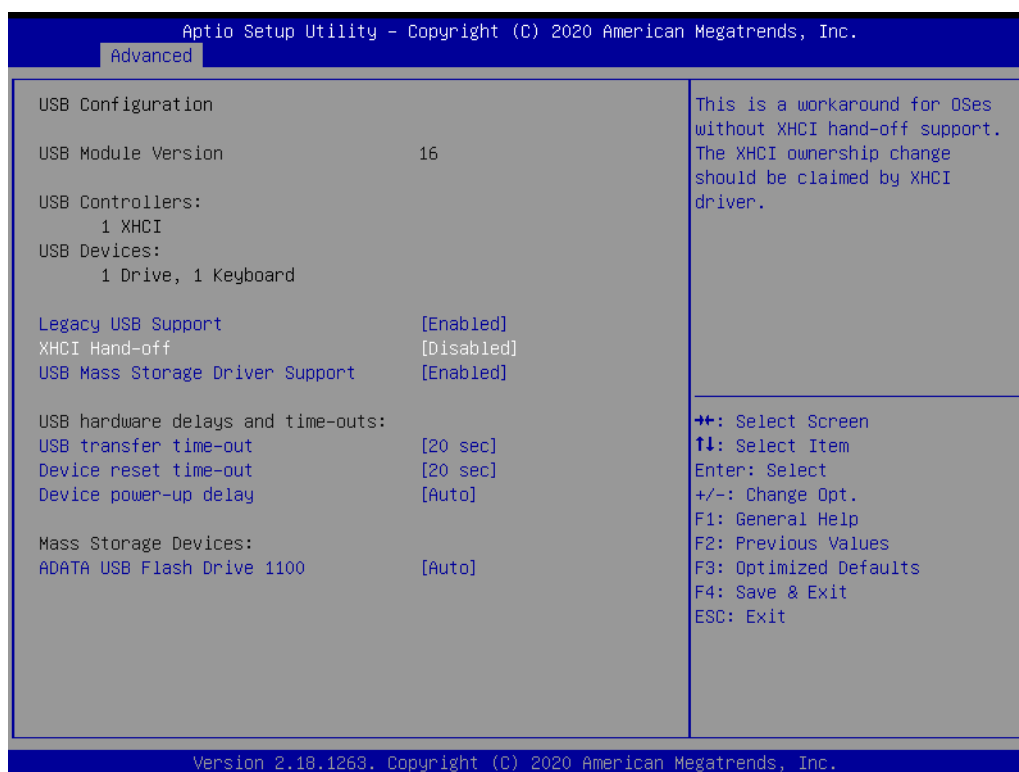


Figure 3.48 XHCI Hand-off setup screen

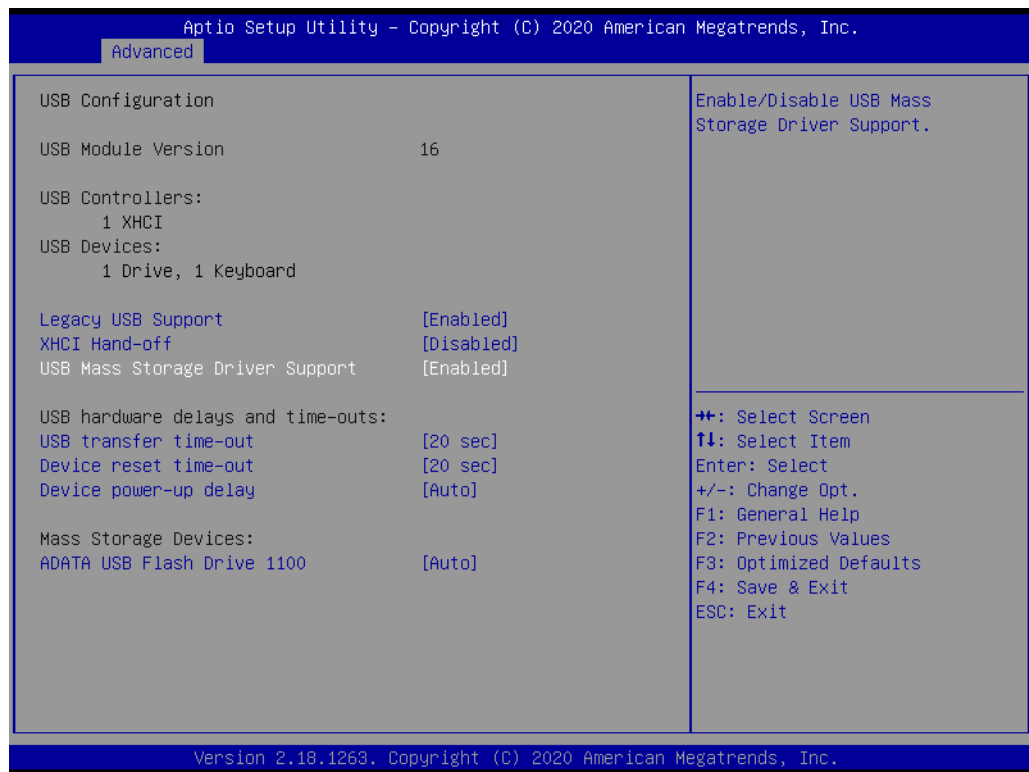


Figure 3.49 USB Mass Storage Driver Support setup screen

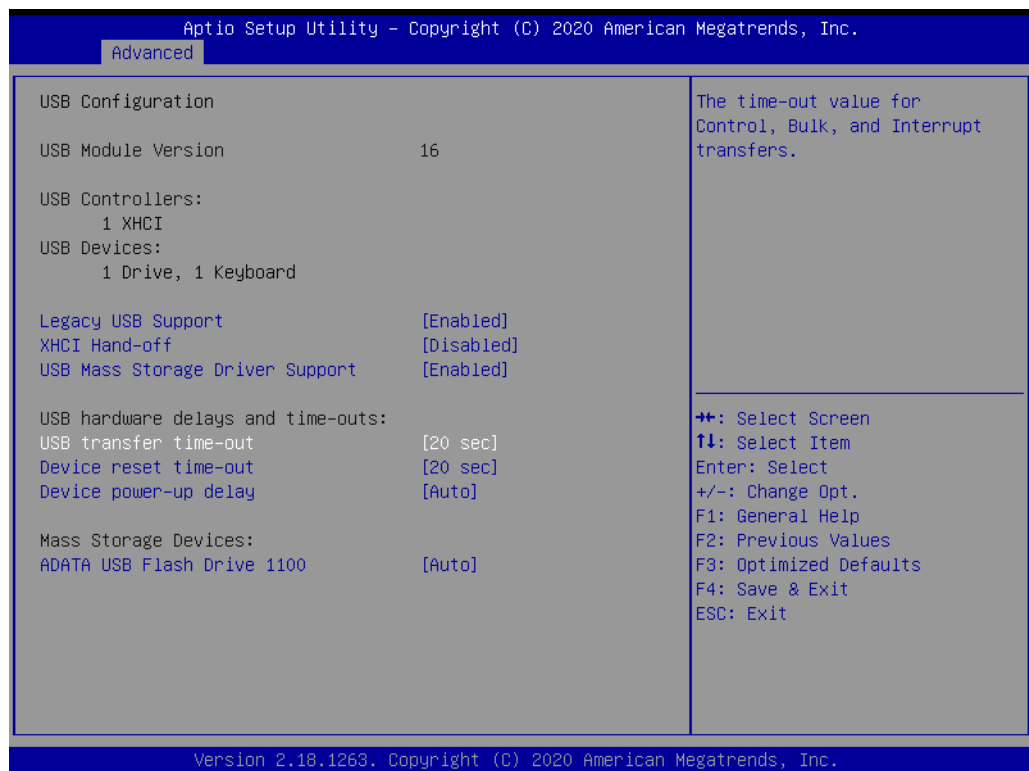


Figure 3.50 USB transfer time-out setup screen

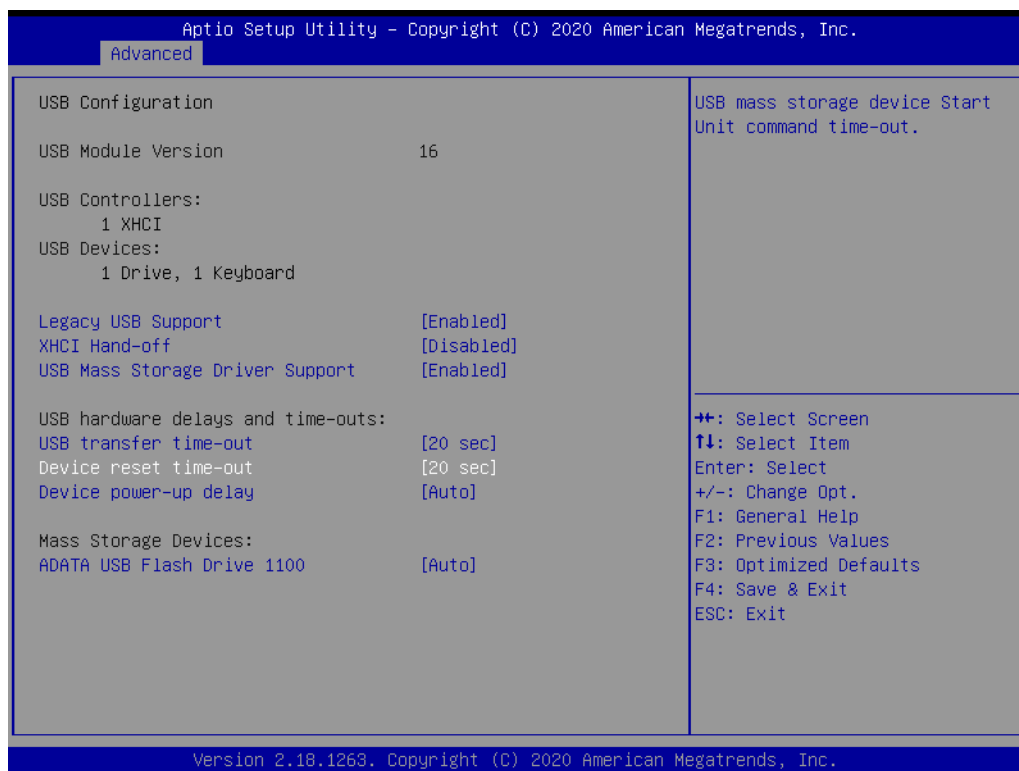


Figure 3.51 Device reset time-out setup screen

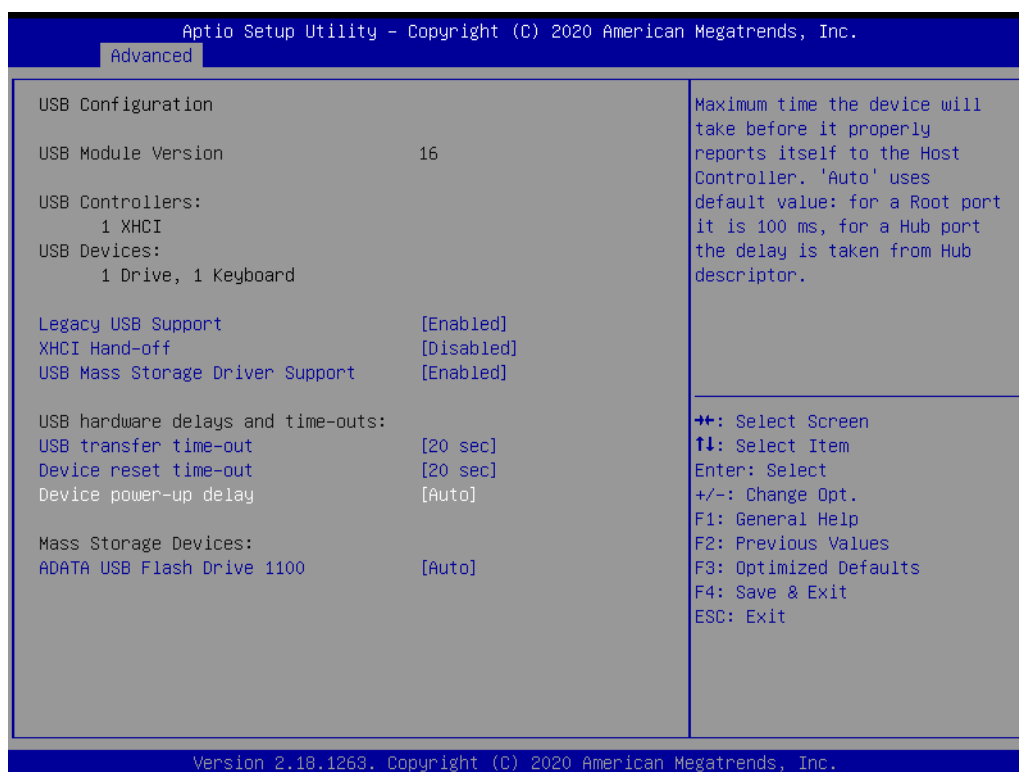


Figure 3.52 Device power-up delay setup screen



Figure 3.53 Mass Storage Devices setup screen

3.2.2.12 Super IO Configuration

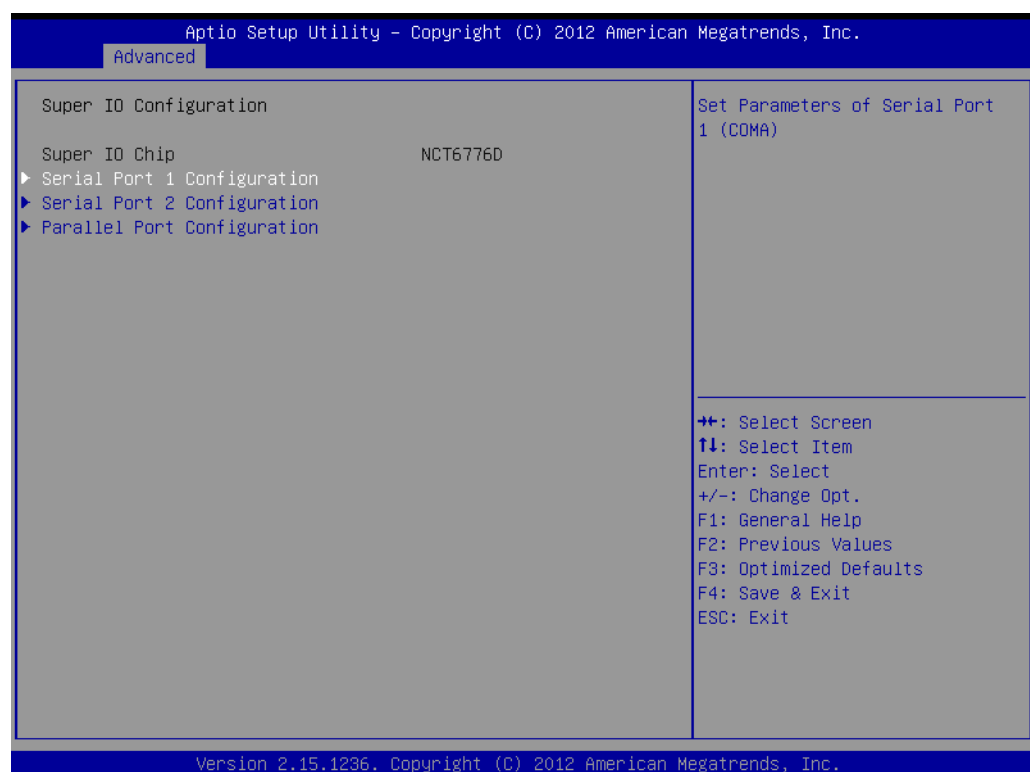


Figure 3.54 Serial Port 1 Configuration setup screen

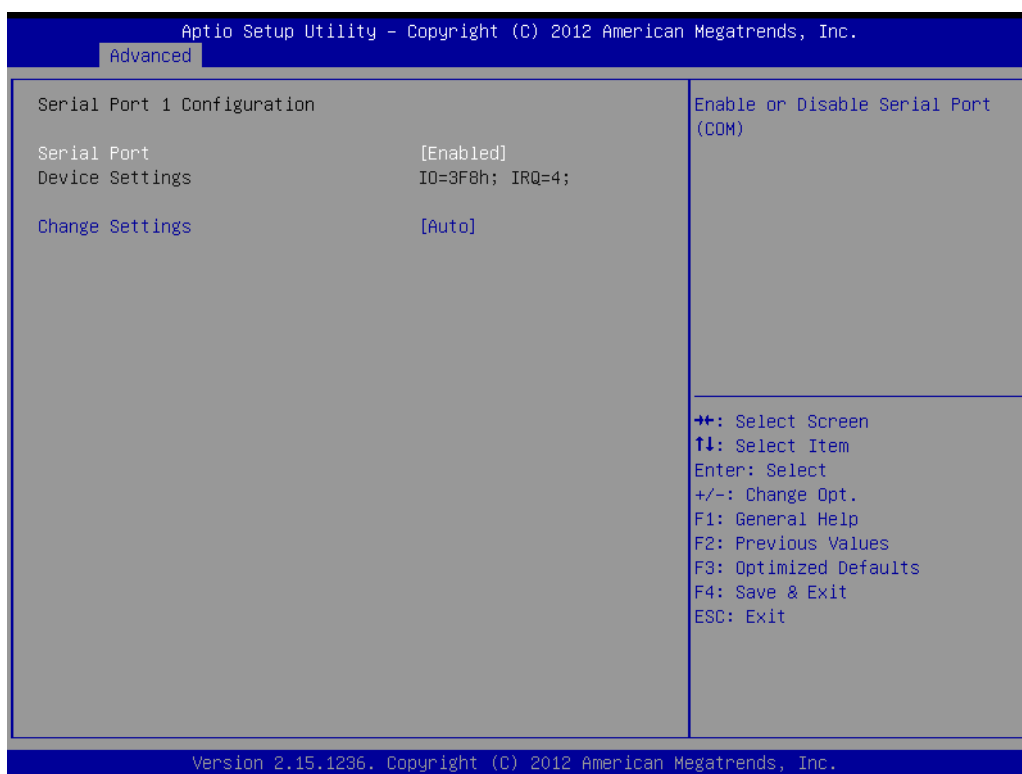


Figure 3.55 Serial Port setup screen-1



Figure 3.56 Serial Port setup screen-2

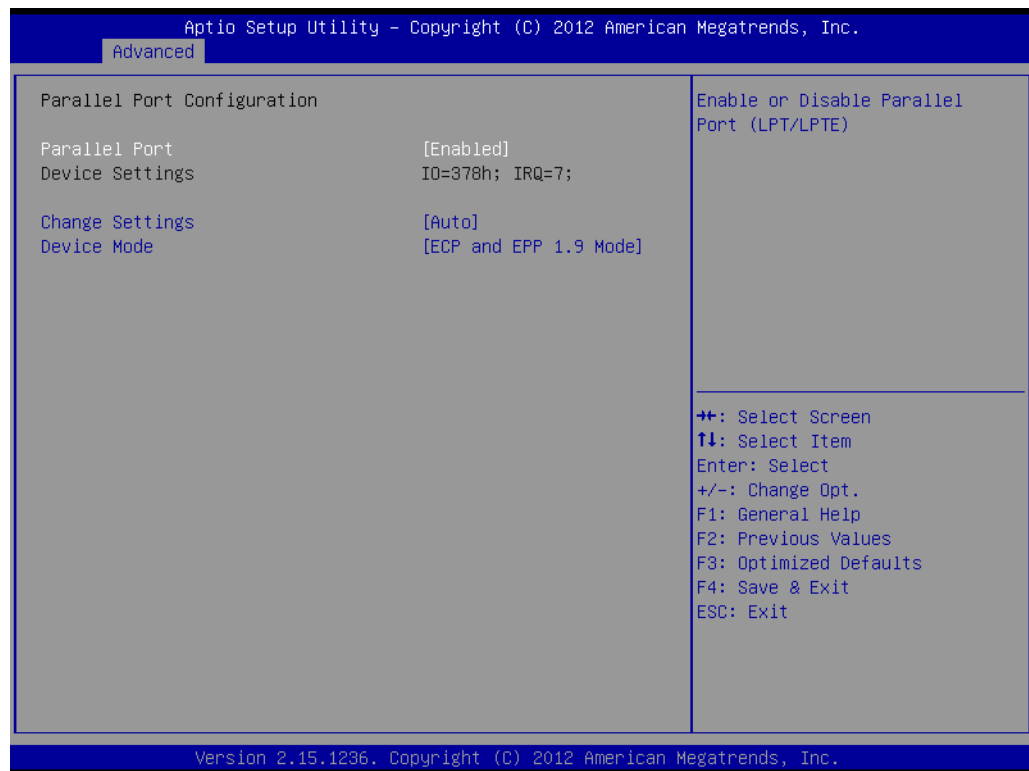


Figure 3.57 Serial Port setup screen-3

- **Serial Port 1 -2 configuration**
Enable or disable serial ports.
- **Parallel Port configuration**
Enable or disable parallel port.

3.2.2.13 H/W Monitor

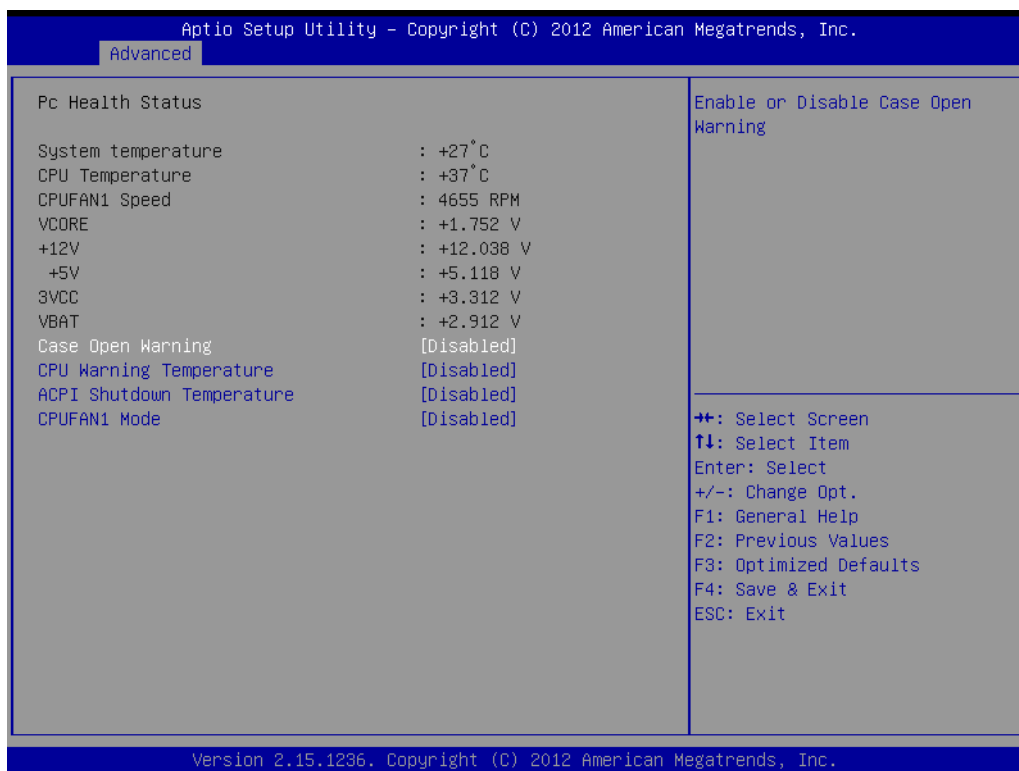


Figure 3.58 Case Open Warning setup screen

- **Case Open Warning**
Enable or disable the Chassis Intrusion monitoring function. When enabled and the case is opened, the speaker beeps.
- **CPU Warning Temperature**
Use this to set the CPU warning temperature threshold. When the system reaches the warning temperature, the speaker will beep.
- **ACPI Shutdown Temperature**
Use this to set the ACPI shutdown temperature threshold. When the system reaches the shutdown temperature, it will be automatically shut down by ACPI OS to protect the system from overheating damage.
- **CPUFAN1 Mode**
Enable or disable the smart fan.

3.2.3 Chipset

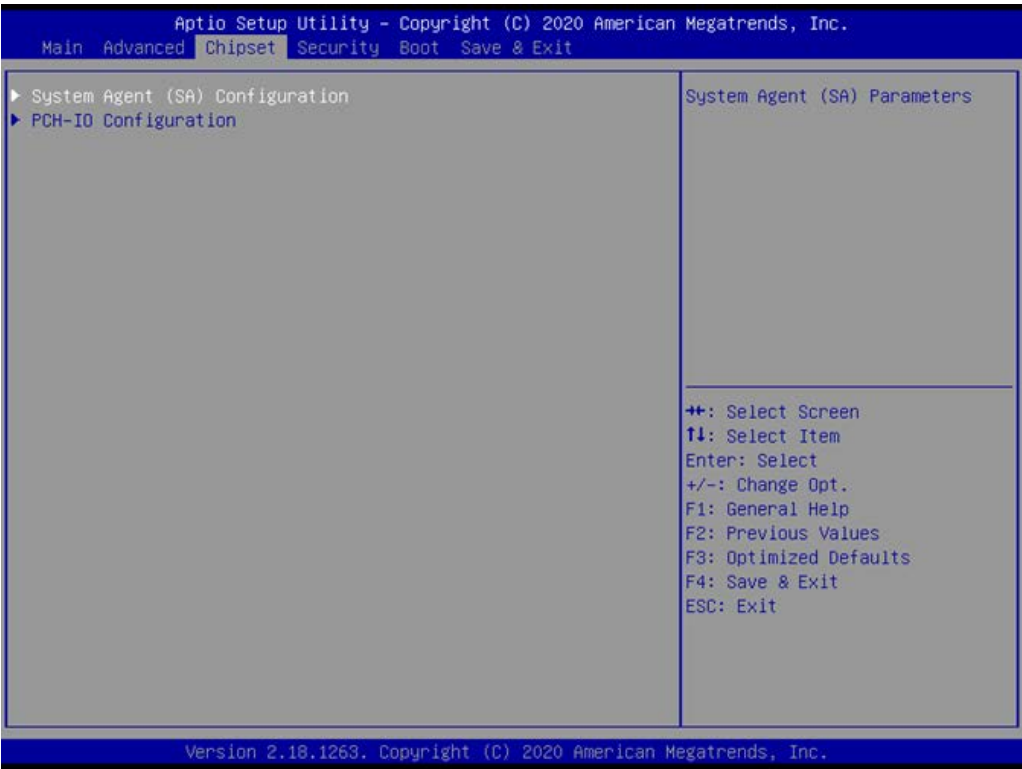


Figure 3.59 System Agent (SA) Configuration setup screen

3.2.3.1 VT-d

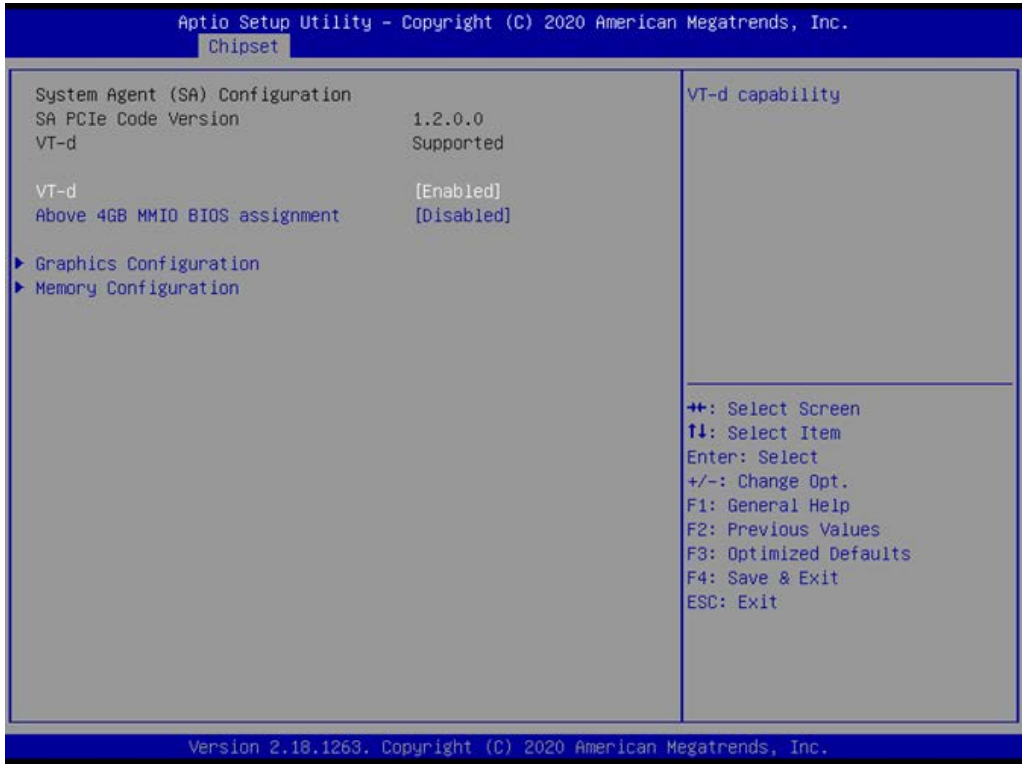


Figure 3.60 VT-d setup screen



Figure 3.61 Above 4GB MMIO BIOS assignment setup screen



Figure 3.62 Graphics Configuration setup screen



Figure 3.63 Memory Configuration setup screen

- **VT-d**
VT-d capability
- **Above 4GB MMIO BIOS assignment**
Enable or disable above 4GB Memory Mapped IO BIOS assignment. This is disabled automatically when Aperture Size is set to 2048MB.
- **Graphics Configuration**
Graphics Configuration
- **Memory Configuration**
Memory Configuration Parameters

3.2.3.2 PCH-IO Configuration

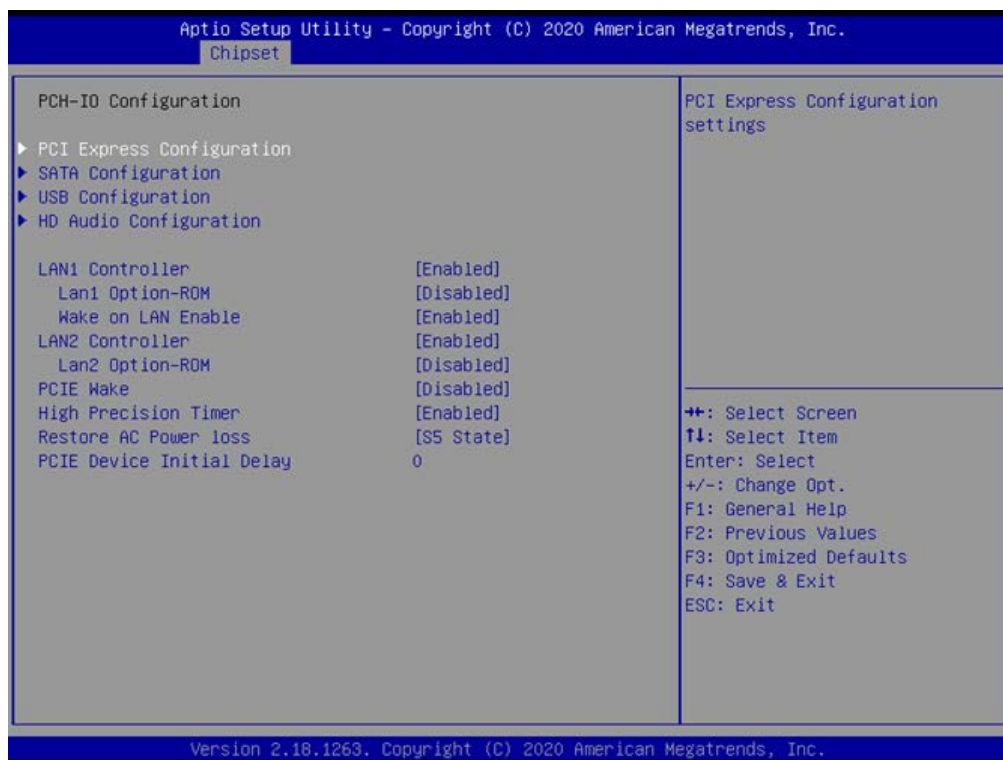


Figure 3.64 PCH-IO Configuration setup screen

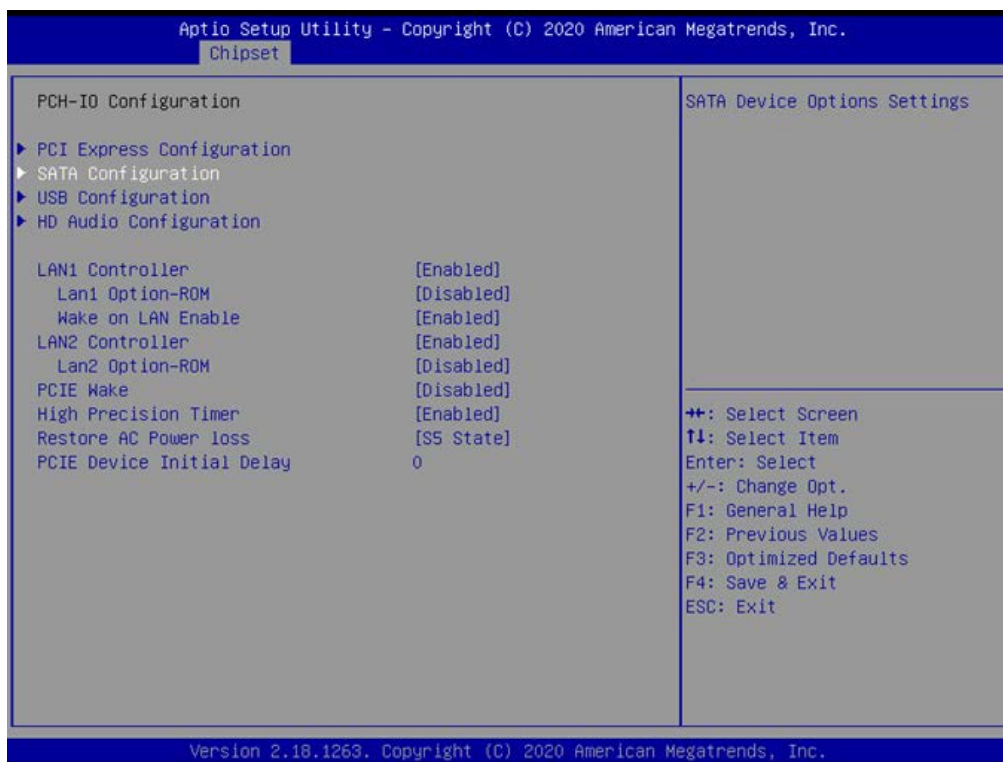


Figure 3.65 SATA Configuration setup screen

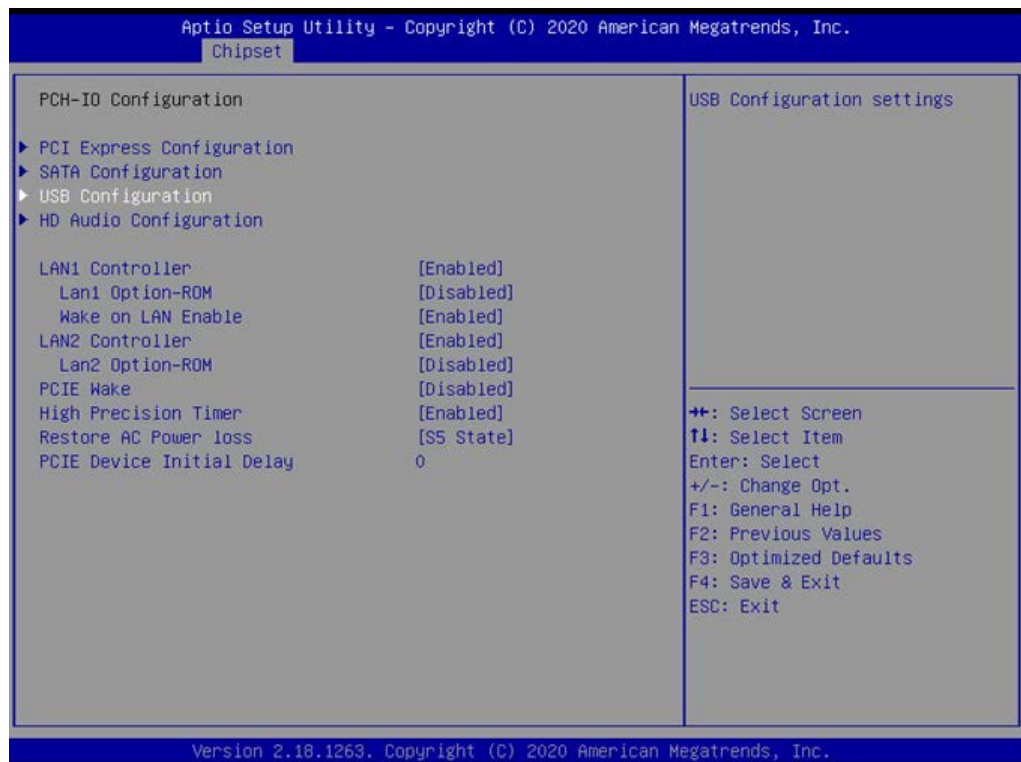


Figure 3.66 USB Configuration setup screen

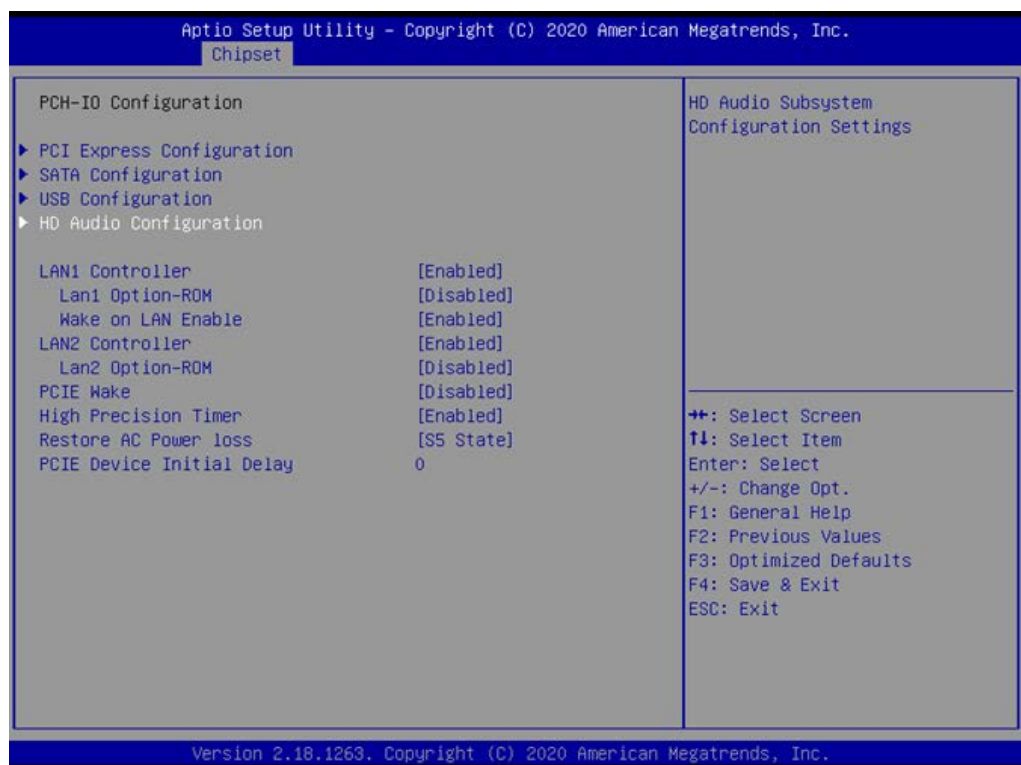


Figure 3.67 HD Audio Configuration setup screen

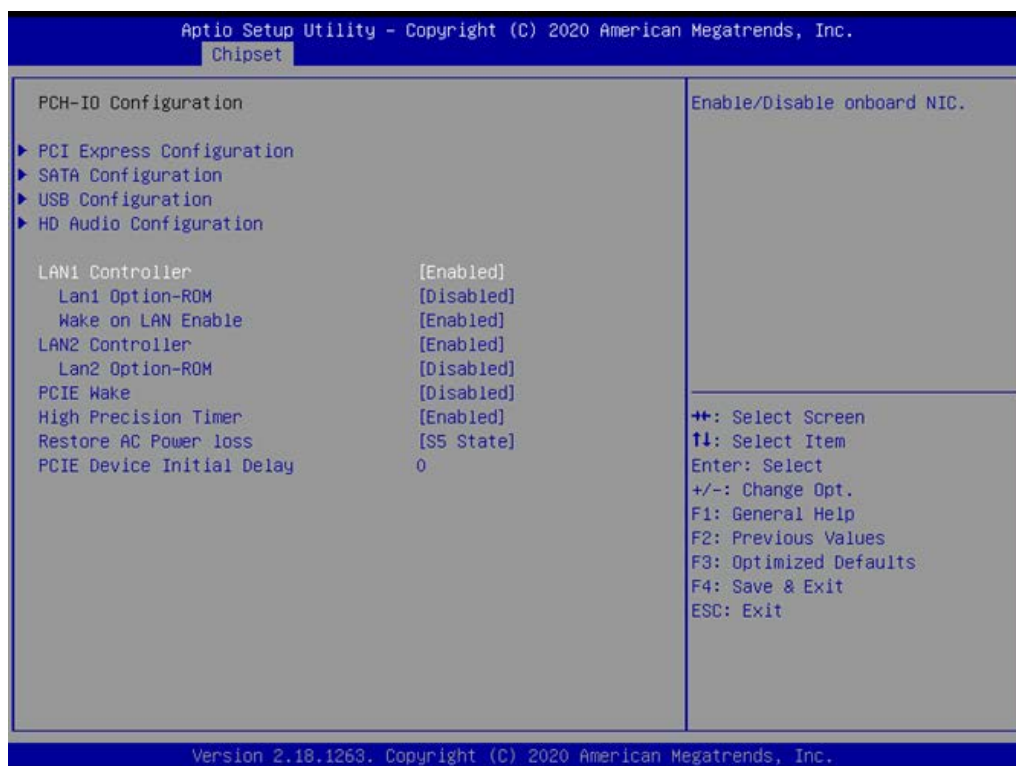


Figure 3.68 LAN1 Controller setup screen

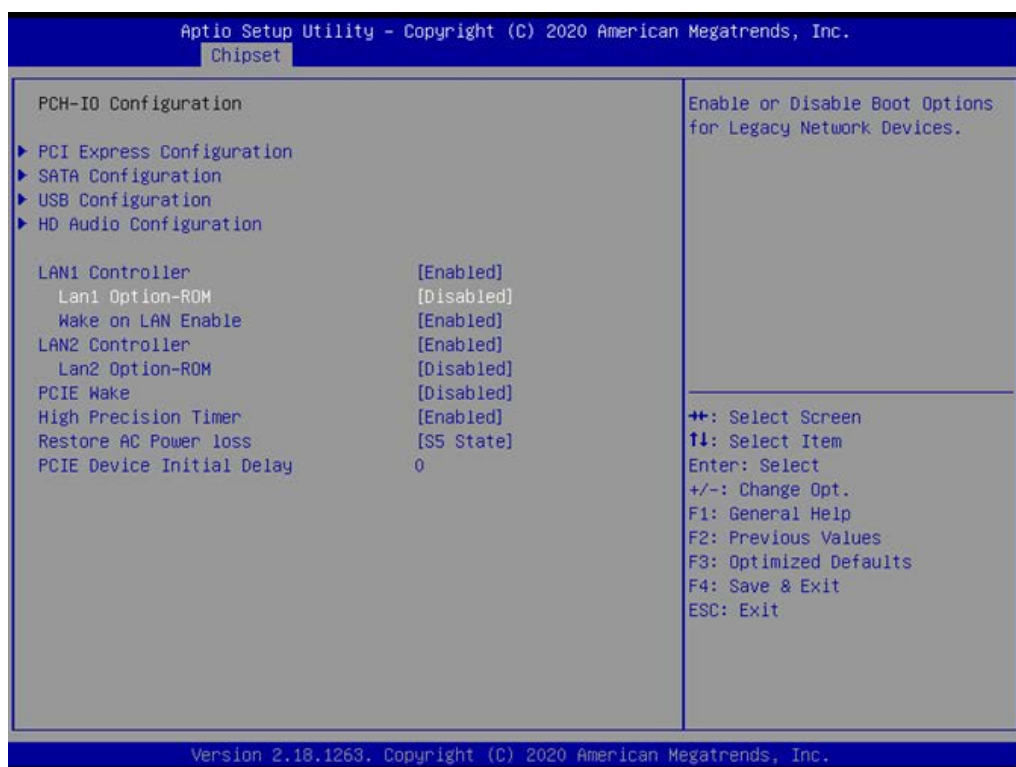


Figure 3.69 LAN1 Option-ROM setup screen

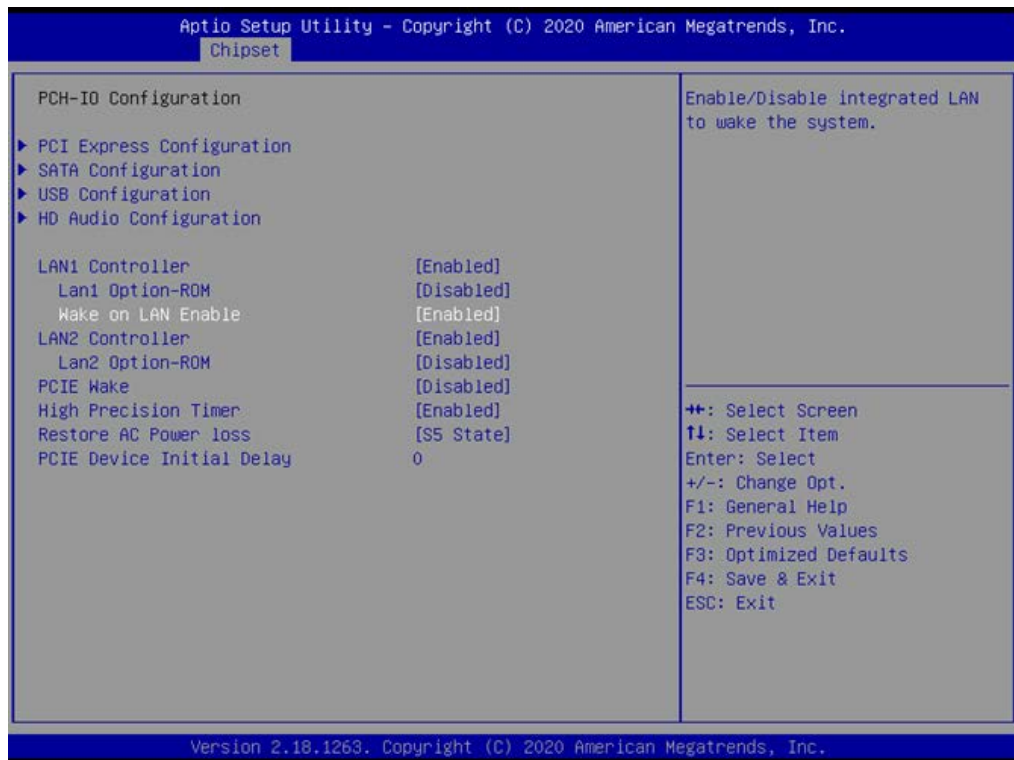


Figure 3.70 Wake on LAN Enable setup screen

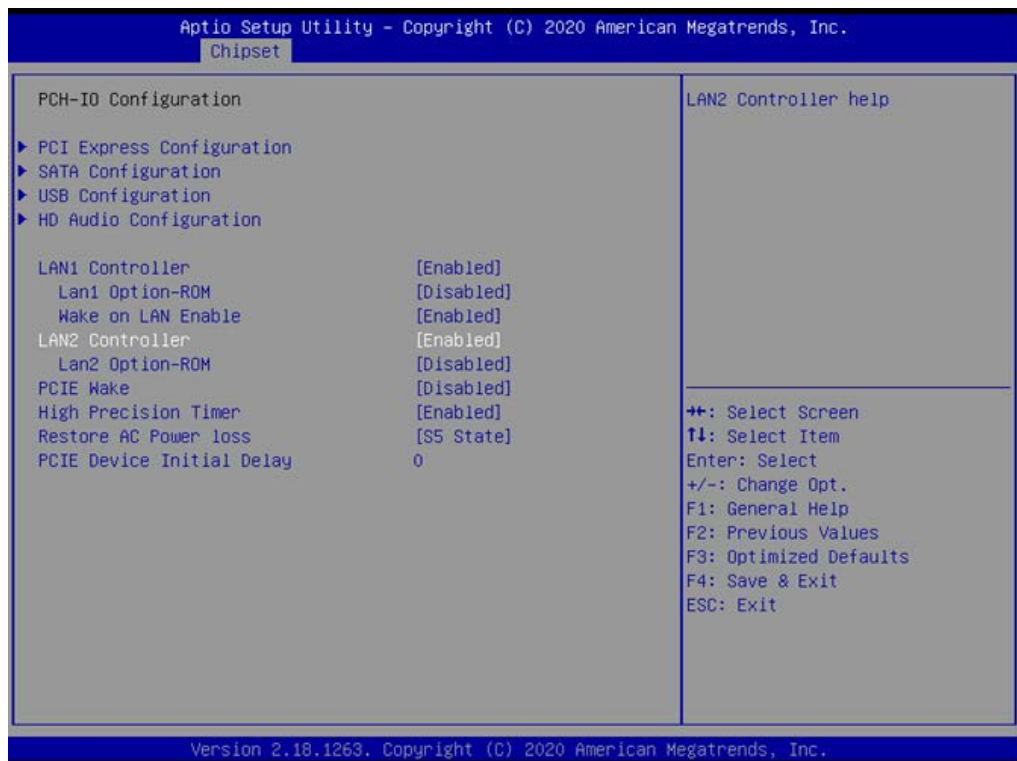


Figure 3.71 LAN2 Controller setup screen

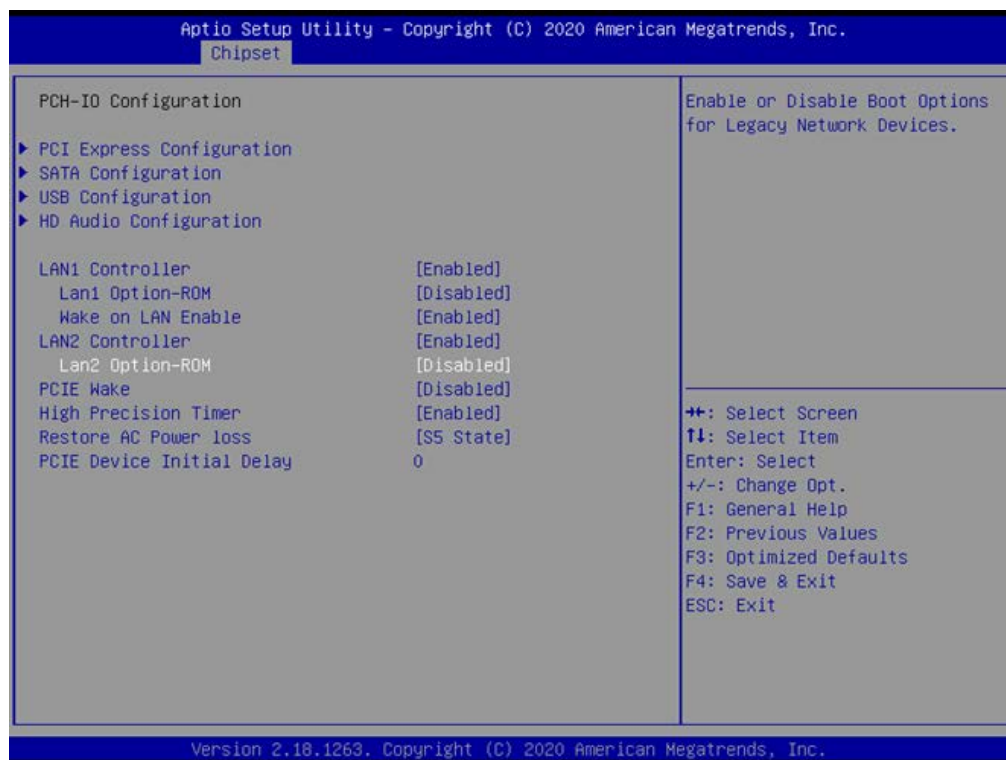


Figure 3.72 LAN2 Option-ROM setup screen

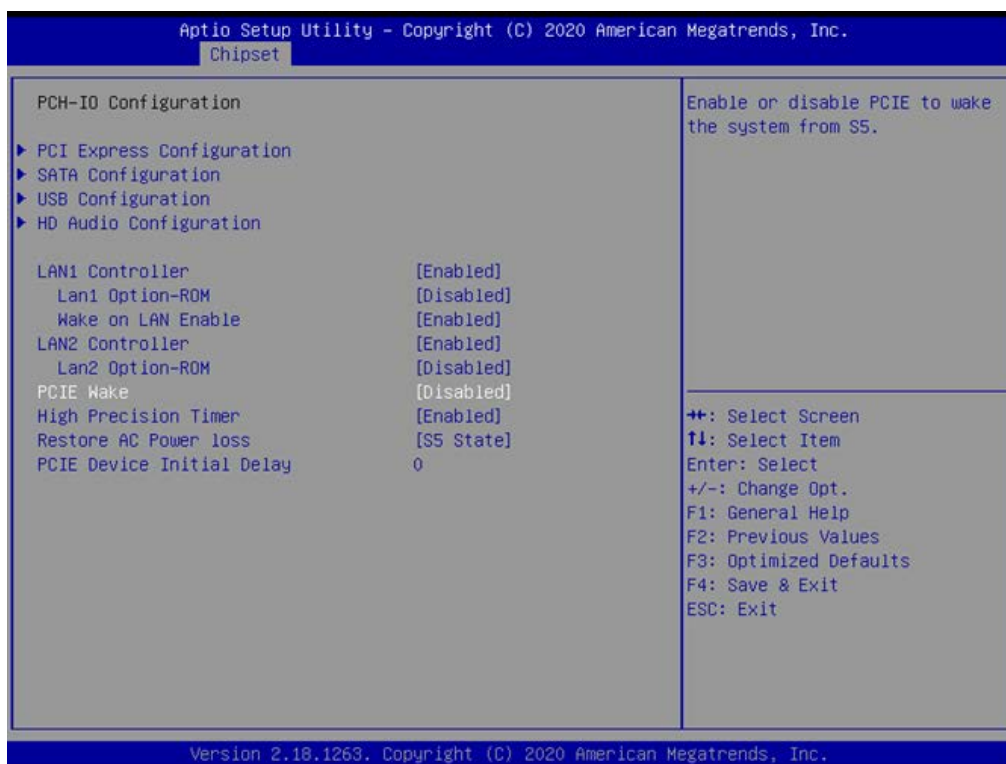


Figure 3.73 PCIE Wake setup screen

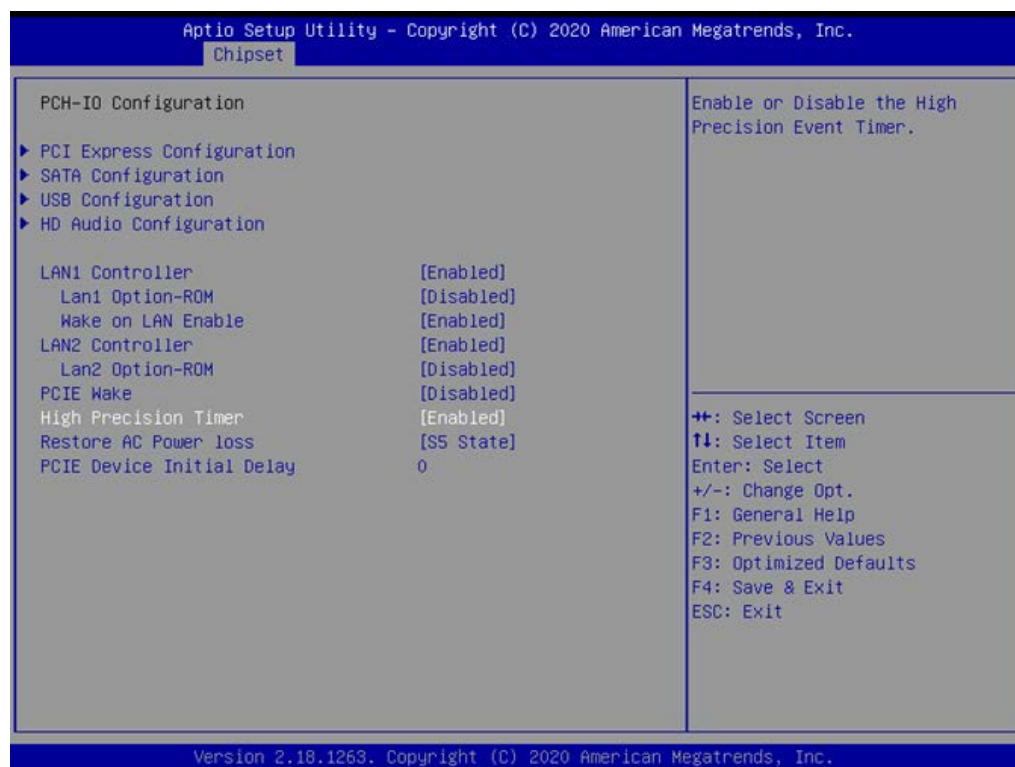


Figure 3.74 High Precision Timer setup screen

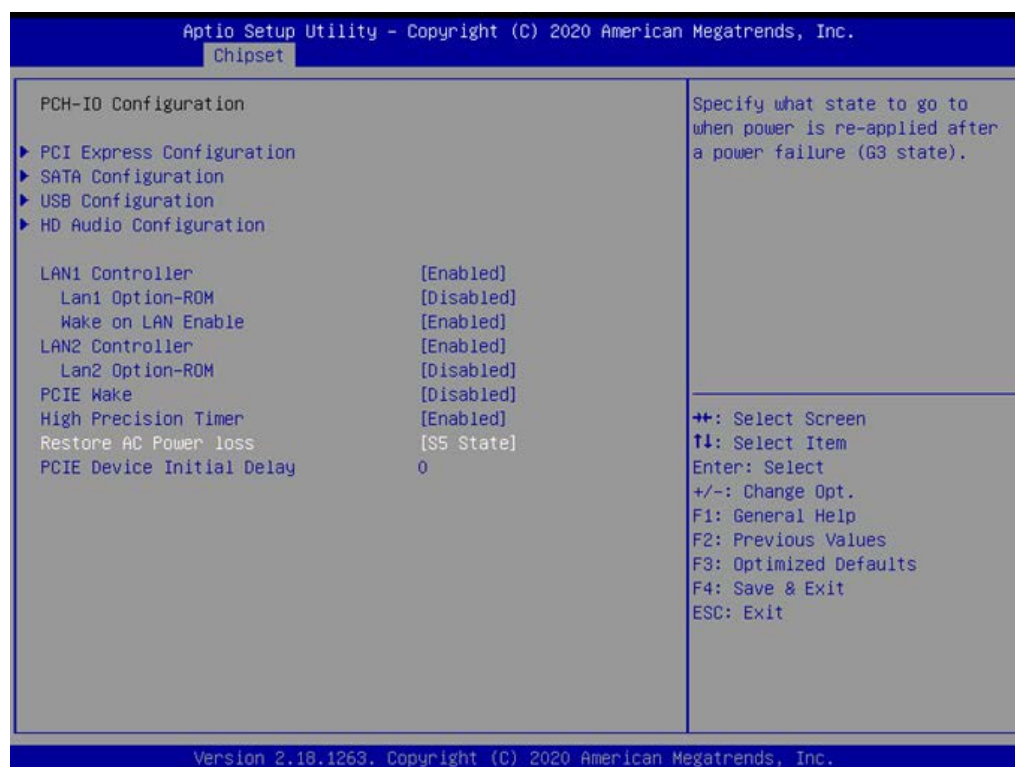


Figure 3.75 Restore AC Power loss setup screen

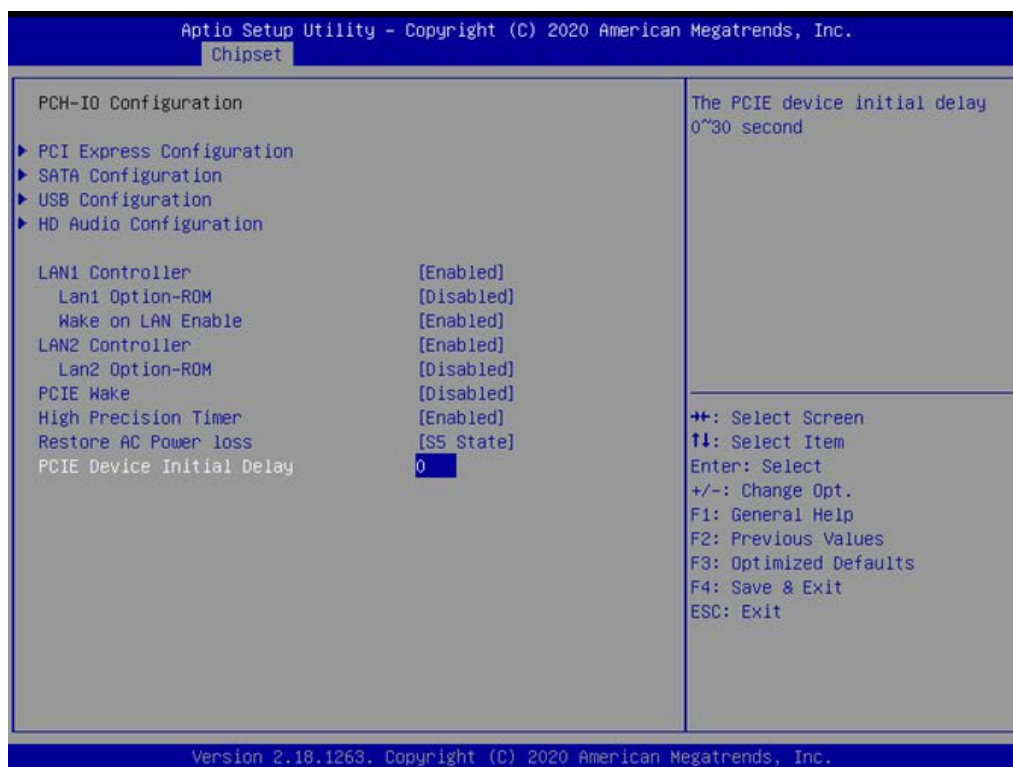


Figure 3.76 PCIE Device Initial Delay setup screen

- **PCI Express Configuration**
PCI Express Configuration Settings.
- **SATA Configuration**
SATA Device Options Settings.
- **USB Configuration**
USB Configuration Settings.
- **HD Audio Configuration**
HD Audio Subsystem Configuration Settings.
- **LAN1 Controller**
Enable or disable onboard NIC.
- **LAN1 Option-ROM**
Enable or disable Boot Options for Legacy Network Devices.
- **Wake on LAN Enable**
Enable or disable integrated LAN to wake the system.
- **LAN2 controller**
LAN2 controller help.
- **LAN2 Option-ROM**
Enable or disable Boot Options for Legacy Network Devices.
- **PCIE Wake**
Enable or disable PCIE to wake the system from S5.
- **High Precision Timer**
Enable or disable the High Precision Event Timer.
- **Restore AC Power Loss**
Specify what state to go to when power is re-applied after a power failure (G3 state).

- **PCIE Device Initial Delay**
The PCIE device initial delay 0~30 second.

3.2.3.3 Password Description

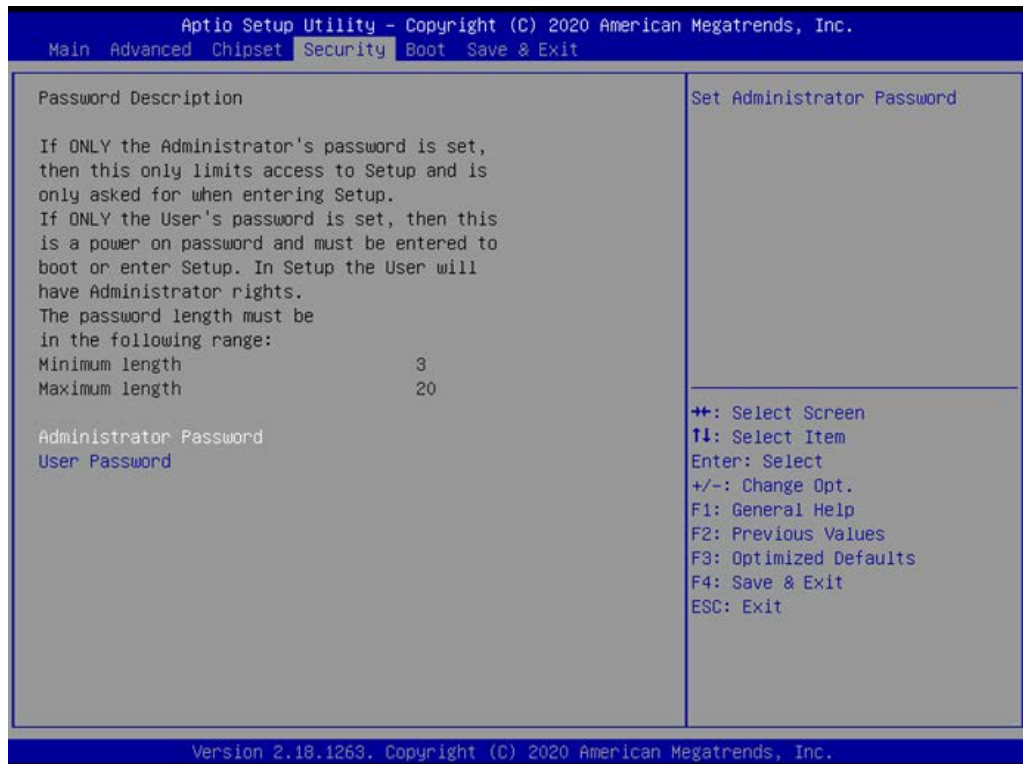


Figure 3.77 Administrator Password setup screen

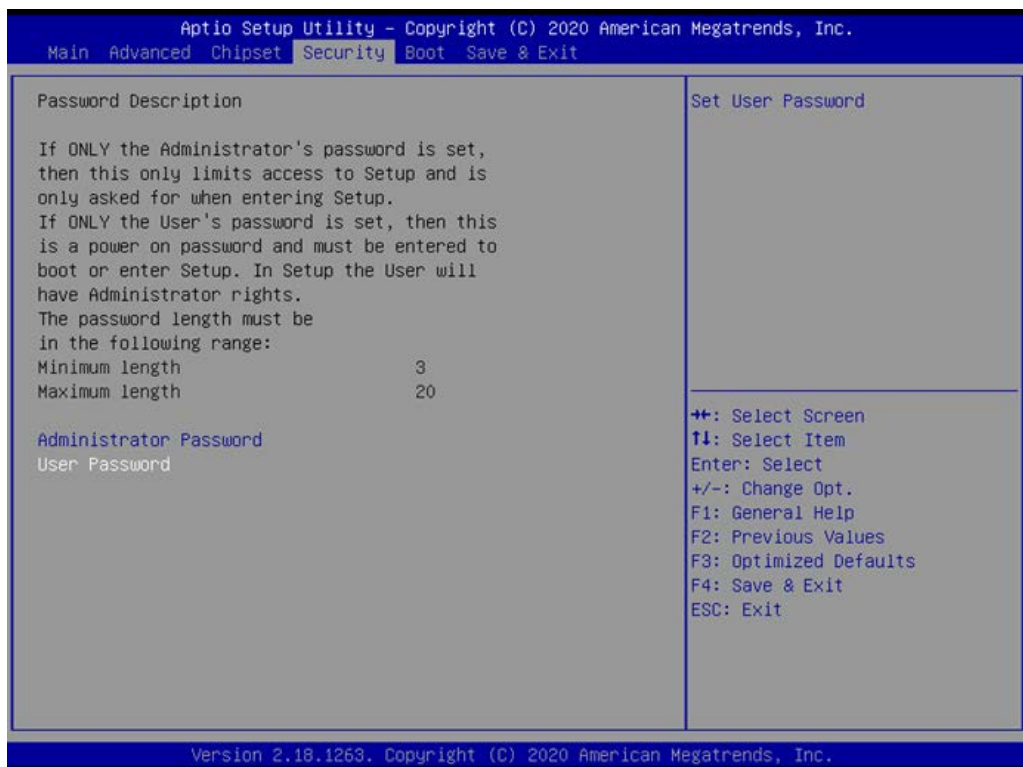


Figure 3.78 User Password setup screen

- **Administrator Password**
Set Administrator Password.

- **User Password**
Set User Password.

3.2.3.4 Boot Configuration

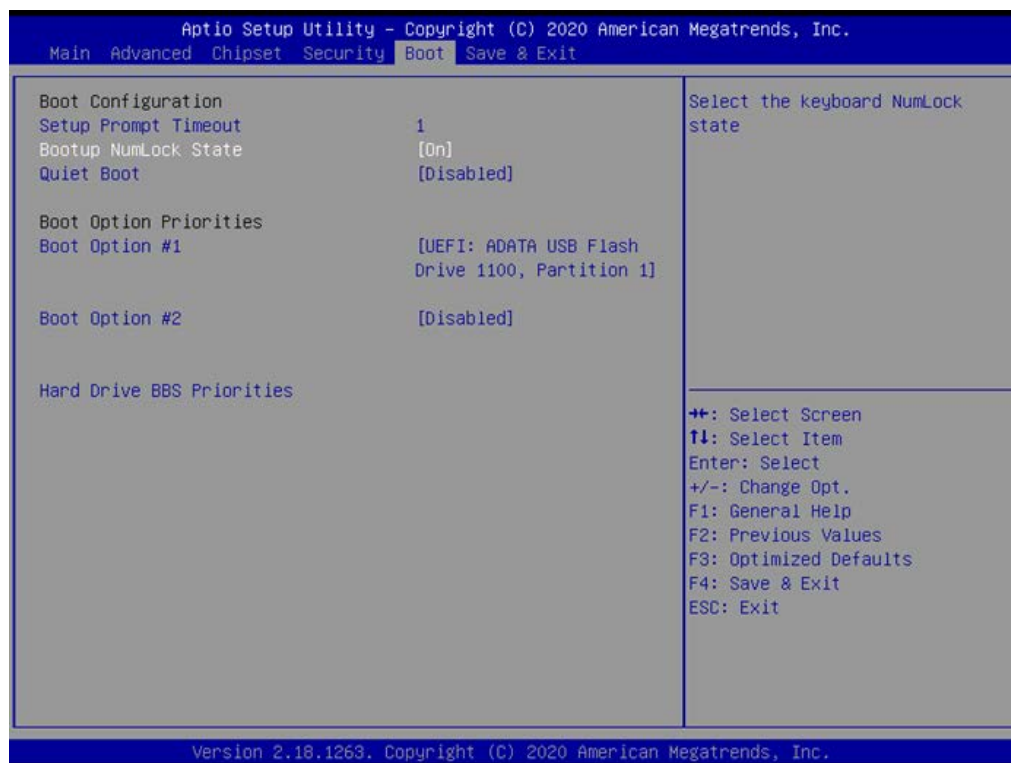


Figure 3.79 Bootup Numlock State setup screen

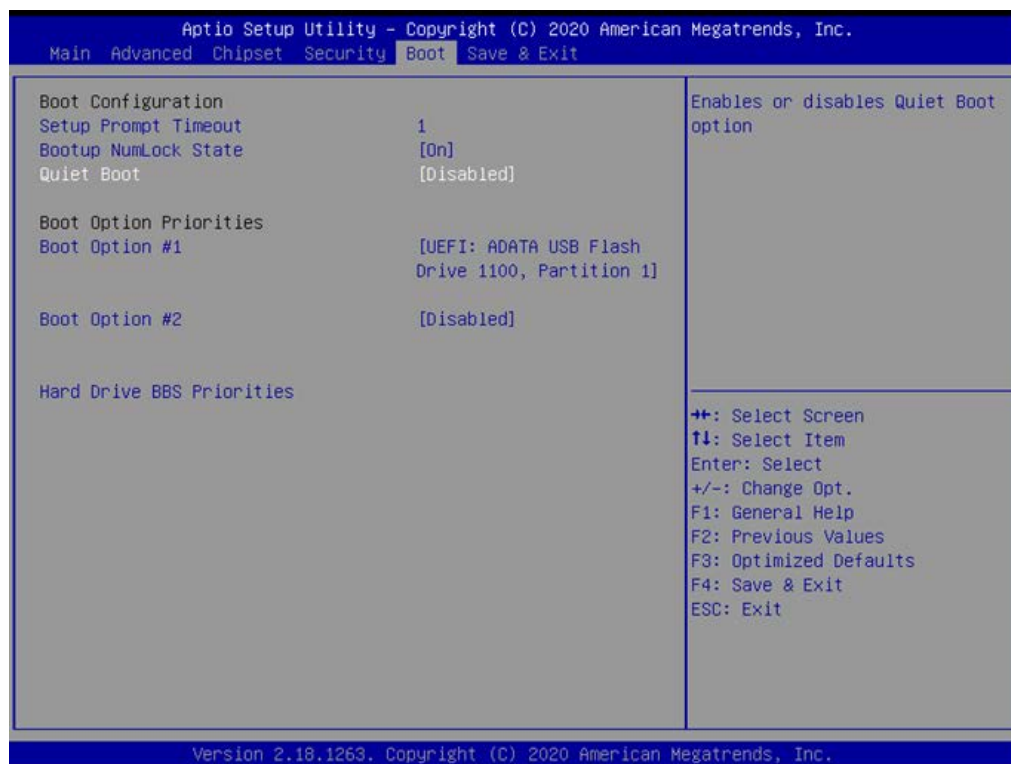


Figure 3.80 Quiet setup screen

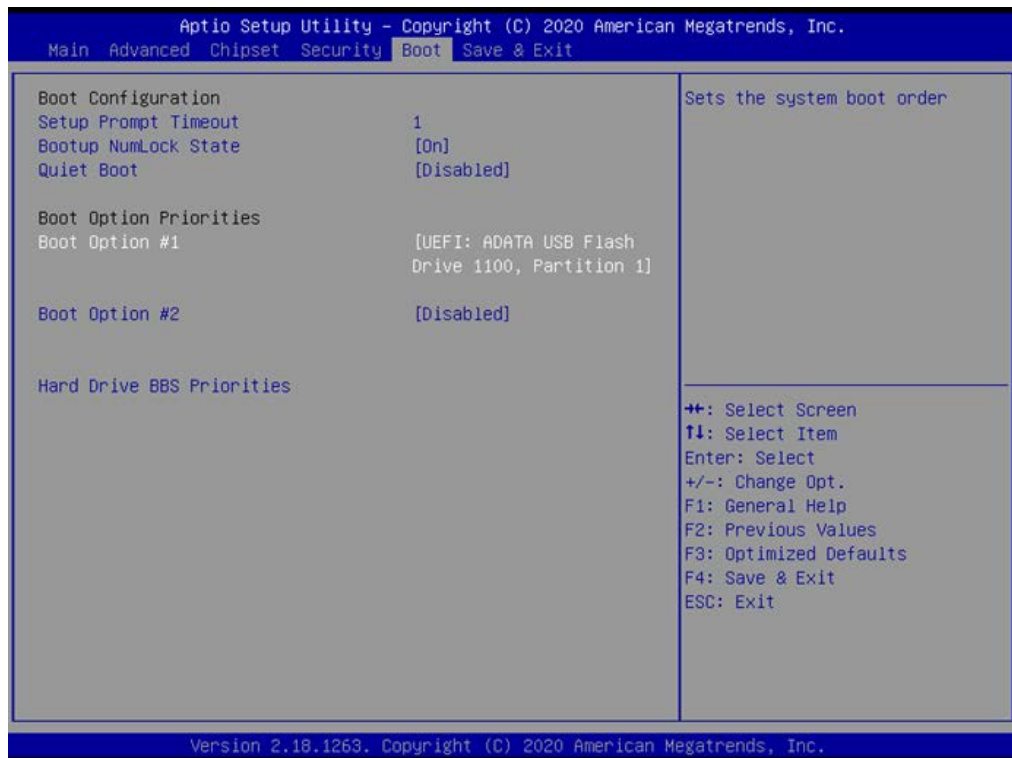


Figure 3.81 Boot Option #1 setup screen



Figure 3.82 Boot Option #2 setup screen

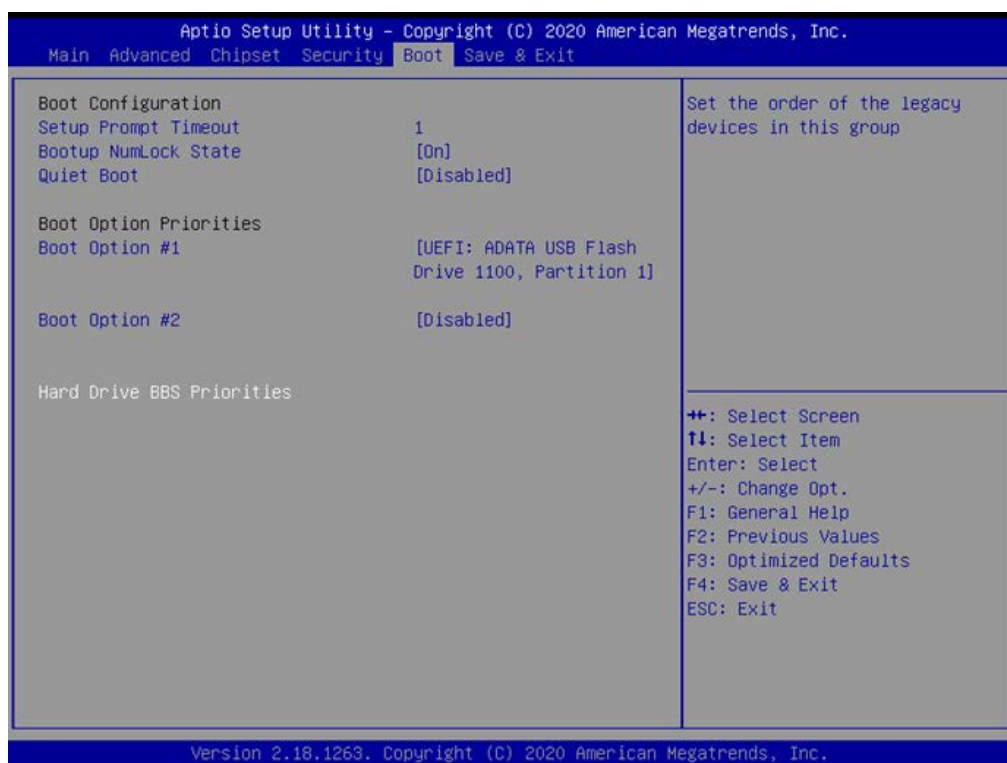


Figure 3.83 Hard Drive BBS Priorities setup screen

- **Boot NumLock State**
Select the keyboard Numlock state.
- **Quiet Boot**
Enable or disable Quiet Boot option.
- **Boot Option #1**
Sets the system boot order.
- **Boot Option #2**
Sets the system boot order.
- **Hard Drive BBS Priorities**
Set the order of the legacy devices in this group.

3.2.3.5 Save Options

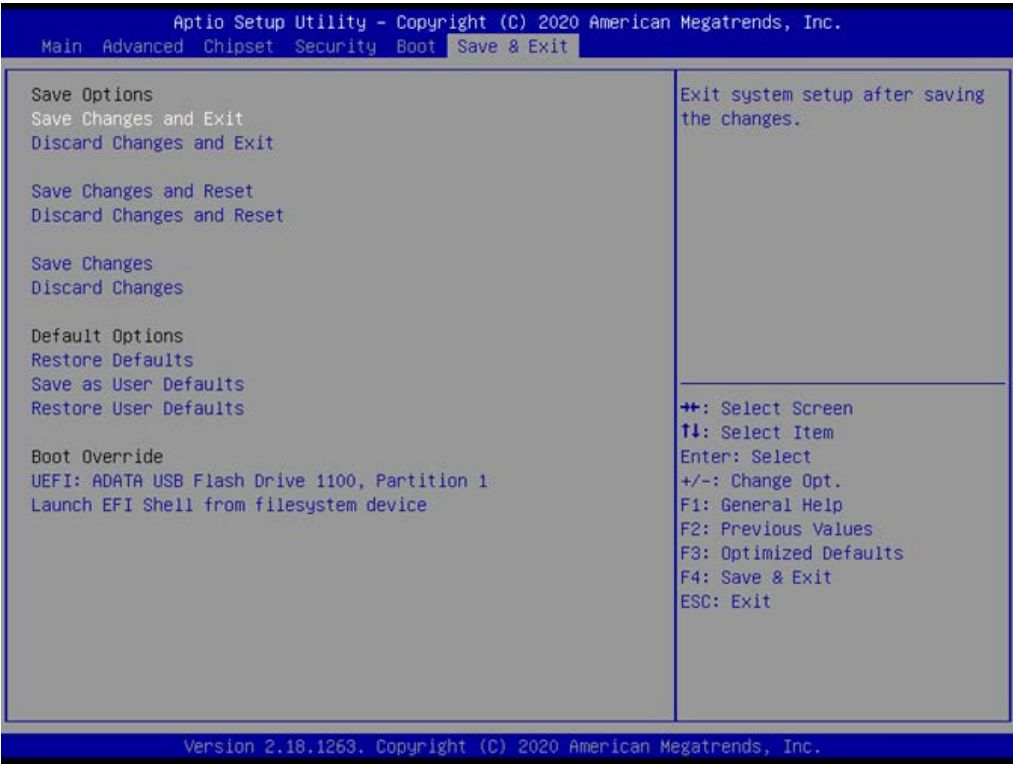


Figure 3.84 Save Changes and Exit setup screen

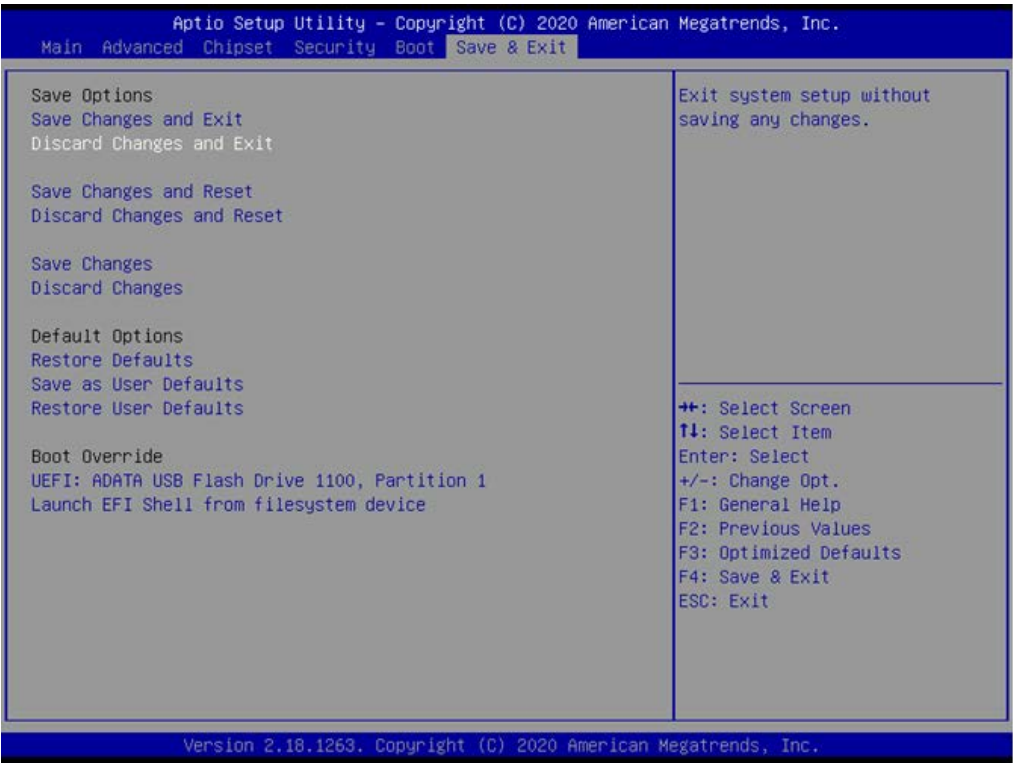


Figure 3.85 Discard Changes and Exit setup screen

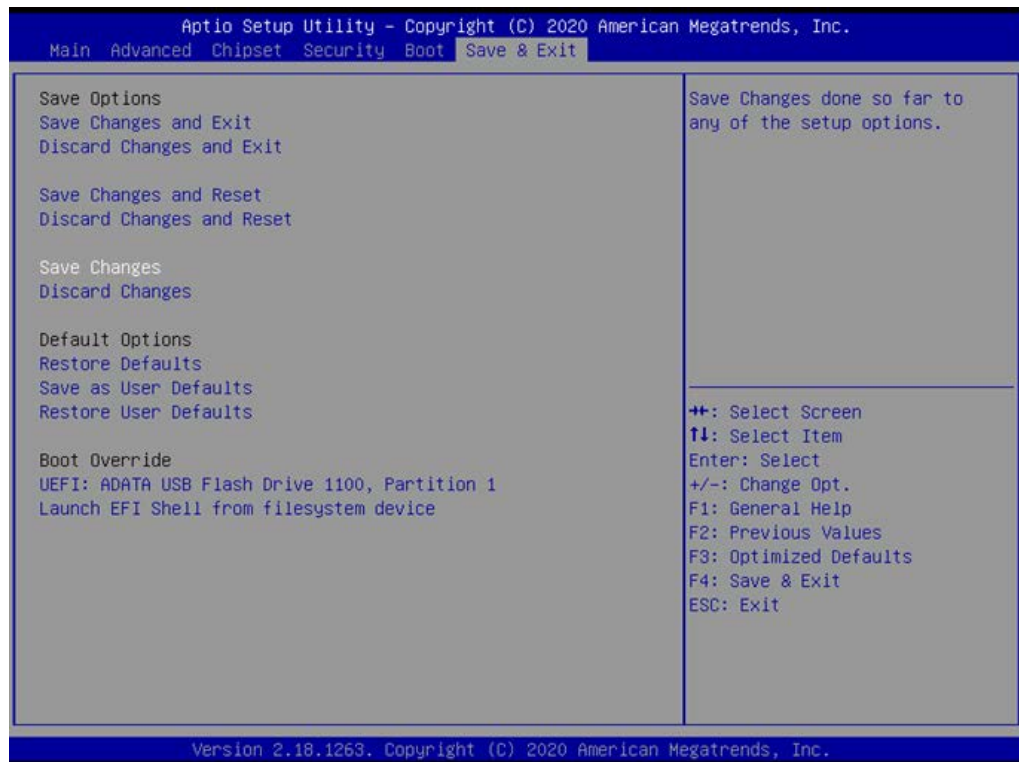


Figure 3.88 Save Changes setup screen

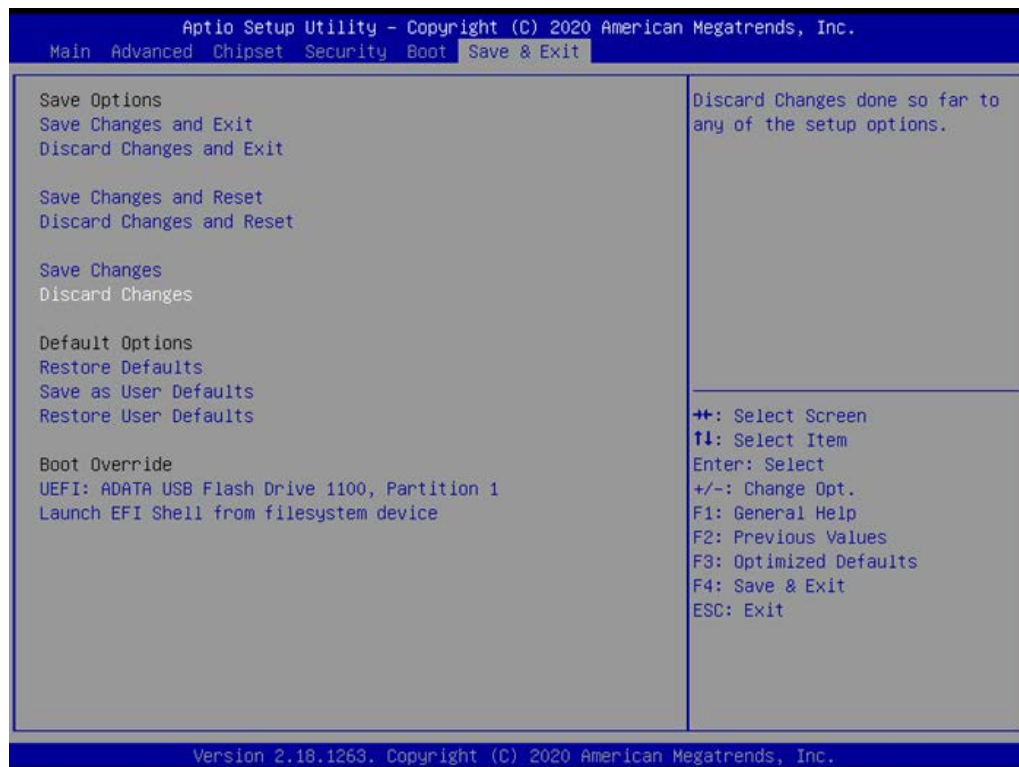


Figure 3.89 Discard Changes setup screen

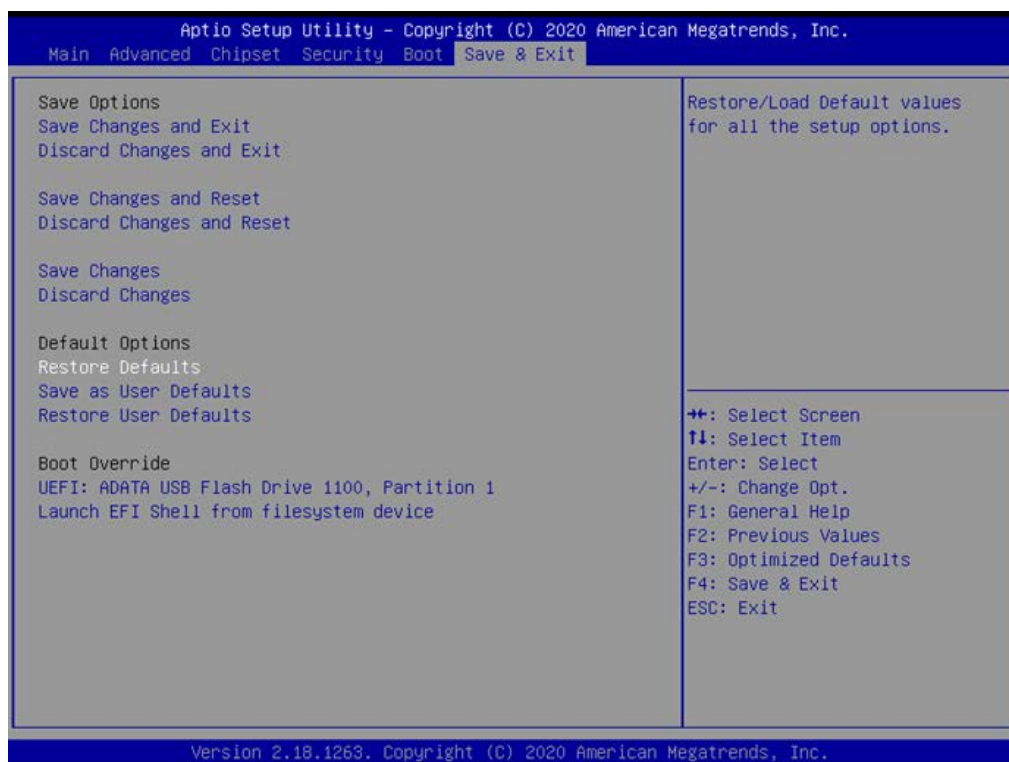


Figure 3.90 Restore setup screen

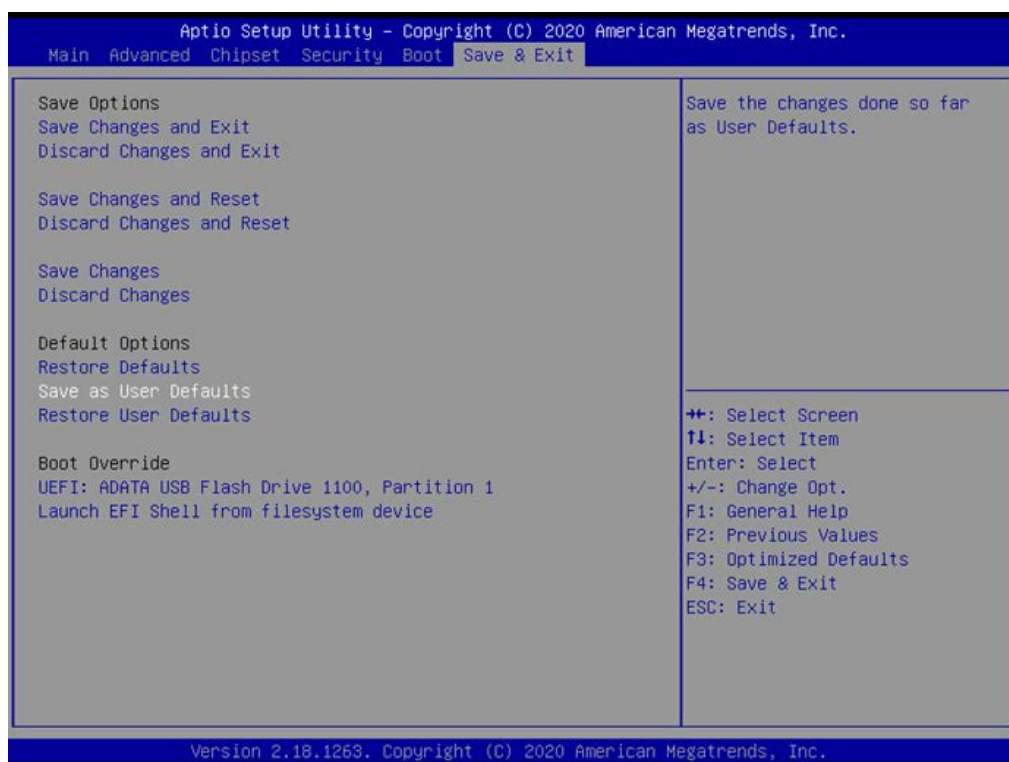


Figure 3.91 Save as User Defaults setup screen

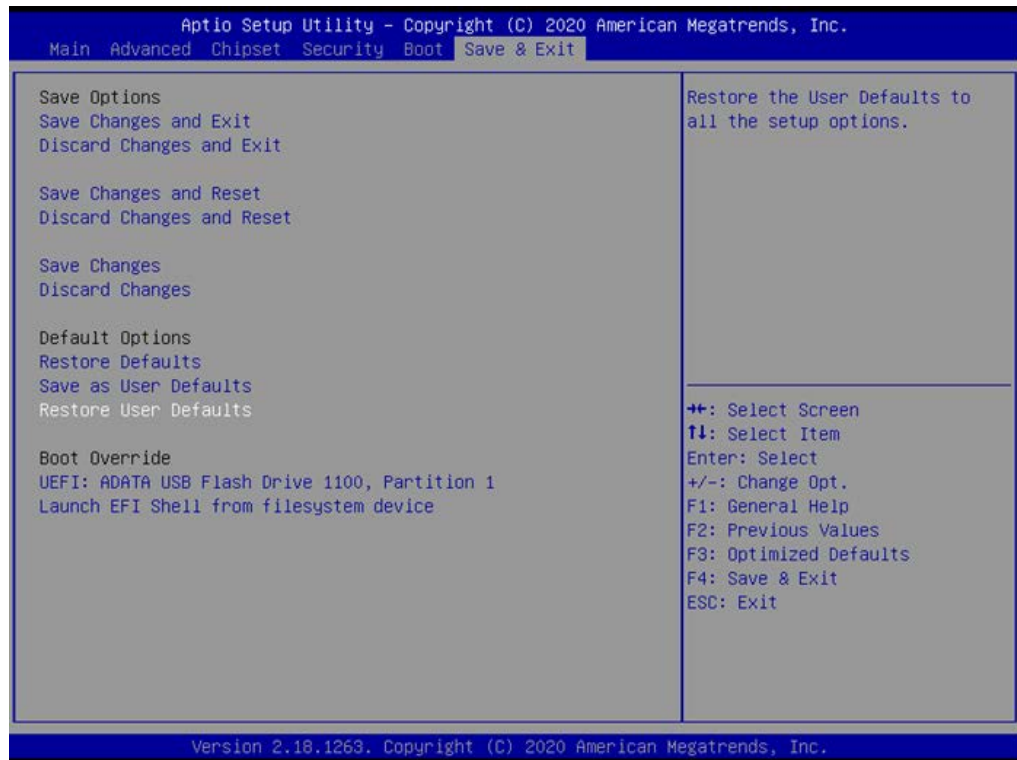


Figure 3.92 Restore User Defaults setup screen

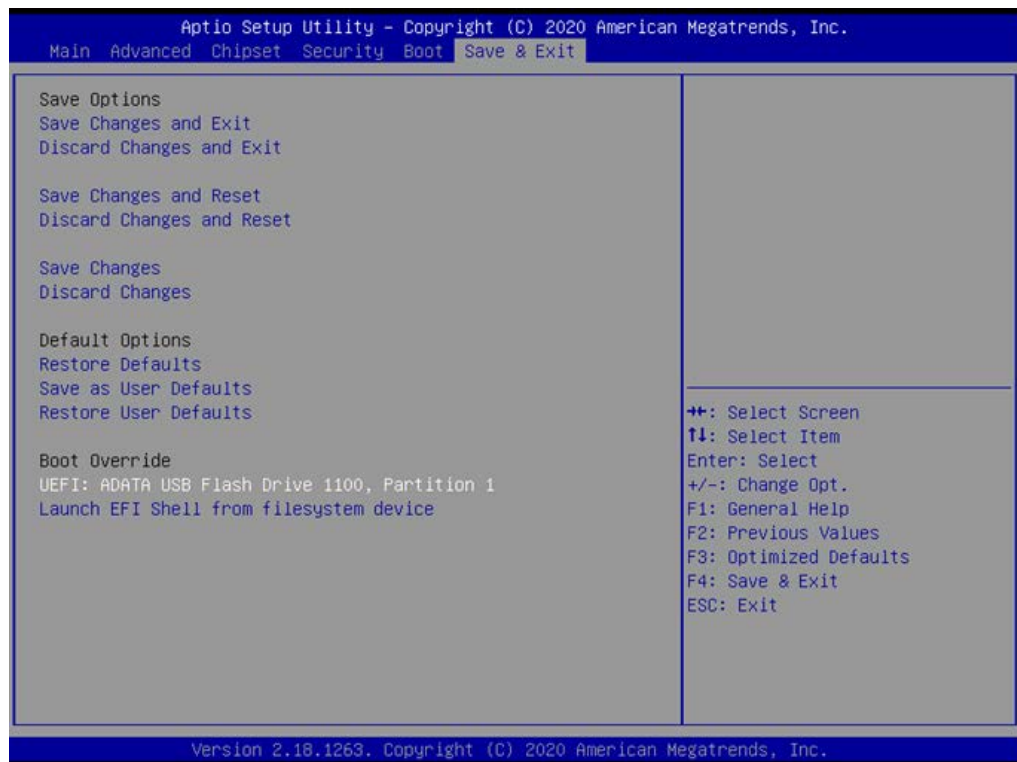


Figure 3.93 UEFI setup screen

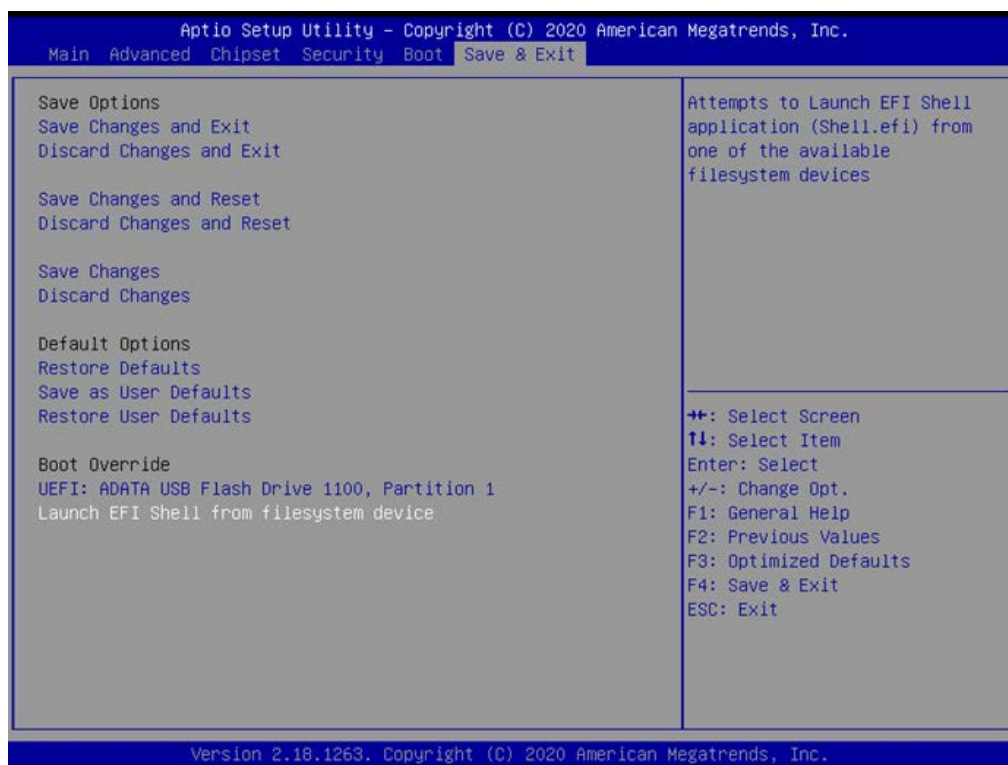


Figure 3.94 Launch EFI Shell from file system setup screen

- **Save Changes and Exit**
Exit system setup after saving the changes.
- **Discard Changes and Exit**
Exit system setup without saving and changes.
- **Save Changes and Reset**
Reset the system after saving the changes.
- **Discard Changes and Exit**
Reset system setup without saving any changes.
- **Save Changes**
Save Changes done so far to any of the setup options
- **Discard Changes**
Discard Changes done so far to any of the setup options.
- **Restore Defaults**
Restore/Load Default values for all the setup options.
- **Save as User Defaults**
Save the changes done so far as User Defaults.
- **Restore User Defaults**
Restore the User Defaults to all the setup options.
- **Launch EFI Shell from filesystem device**
Attempts to Launch EFI Shell application (Shell.efi) from one of the available filesystem devices.

Chapter 4

Value-Added Software Services

4.1 Value-Added Software Services

Software API are interface that define the ways in which an application program may request services from libraries and/or operating systems. They provide not only the underlying drivers required but also a rich set of user-friendly, intelligent and integrated interfaces, which speed development, enhance security and offer add-on value for Advantech platforms. API plays the role of catalyst between developer and solution, and make Advantech embedded platforms easier and simpler to adopt and operate with customer applications. This API and utility is only for XP (32/64 bit), Win7 (32/64bit), and Win8 (32/64bit), so if users needs Linux version API and utility, then contact an Advantech representative for support. For Windows systems, please install the .Net Framework v 3.5 in the driver CD.

4.1.1 Software API

4.1.1.1 Control

GPIO



General Purpose Input/Output is a flexible parallel interface that allows a variety of custom connections. allows users to monitor the level of signal input or set the output status to switch on/off the device. Our API also provides Programmable GPIO, which allows developers to dynamically set the GPIO input or output status.

4.1.1.2 Monitor

Watchdog



A watchdog timer (WDT) is a device that performs a specific operation after a certain period of time if something goes wrong and the system does not recover on its own. A watchdog timer can be programmed to perform a warm boot (restarting the system) after a certain number of seconds.

Hardware Monitor



The Hardware Monitor (HWM) API is a system health supervision API that inspects certain condition indexes, such as fan speed, temperature and voltage.

4.1.2 Software Utility

Monitoring



The Monitoring utility allows the customer to monitor system health, including voltage, CPU and system temperature and fan speed. These items are important to a device; if critical errors happen and are not solved immediately, permanent damage may be caused.

Chapter 5

Chipset Software
Installation Utility

5.1 Before You Begin

To facilitate the installation of the enhanced display drivers and utility software, read the instructions in this chapter carefully. The drivers for the PCA-6029 are located on the official websites.

Before you begin, it is important to note that most display drivers need to have the relevant software application already installed in the system prior to installing the enhanced display drivers. In addition, many of the installation procedures assume that you are familiar with both the relevant software applications and operating system commands. Review the relevant operating system commands and the pertinent sections of your application software's user manual before performing the installation.


5.2 Introduction

The Intel® Chipset Software Installation (CSI) utility installs the Windows INF files that outline to the operating system how the chipset components will be configured. This is needed for the proper functioning of the following features:

- Core PCI PnP services
- Serial ATA interface support
- USB 1.1/2.0/3.0 support
- Identification of Intel® chipset components in the Device Manager
- Integrates superior video features. These include filtered sealing of 720 pixel DVD content, and MPEG-2 motion compensation for software DVD

5.3 Win7/Win8/Win10 Driver Setup

Visit the official website at Advantech, download PCA-6029 drivers from this link: <https://www.advantech.tw/support/details/driver?id=1-1ZEZJR5>

Note!  For 6th Gen. CPU, please choose "Win7/Win8/Win10 driver for PCA-6029_Skylake (6th Intel CPU)".
For 7th Gen. CPU, please choose "Win10 driver for PCA-6029_Kaby-lake (7th Intel CPU)".

Win7/8/10 driver for PCA-6029

2021-02-23 | Driver | Document No.1-4318271681

Related OS:

Win10, Win7, Win8.1

Related Product:

PCA-6029

Solution:

[Win7/Win8/Win10 driver for PCA-6029_Skylake \(6th Intel CPU\)](#)



[Win10 driver for PCA-6029_Kaby-lake \(7th Intel CPU\)](#)



Chapter 6

Integrated Graphic Device Setup

6.1 Introduction

6th/7th generation Intel CPUs have integrated graphics controllers. You need to install the VGA driver to enable this function, which includes the following features:

- **Optimized integrated graphic solution:** Intel Graphics Flexible Display Interface supports versatile display options and 3D graphics engine. Triple independent display, enhanced display modes for widescreen flat panels for extend, twin, and clone dual display mode, and optimized 3D support deliver an intensive and realistic visual experience.

6.2 Win7/Win8/Win10 Driver Setup

Please download graphic driver by clicking "Download".

PCA-6029_Graphic_Win7(32&64bit)_Win8(64bit)_Win10(64bit)
2020-10-20

Download

Chapter 7

LAN Configuration

7.1 Introduction

The PCA-6029 has dual Gigabit Ethernet LANs via dedicated PCI Express x1 lanes (Intel I219-V (LAN1) and I211-AT (LAN2 in G2)) that offer bandwidth of up to 500 MB/sec, eliminating the bottleneck of network data flow and incorporating Gigabit Ethernet at 1000 Mbps.

7.2 Installation

The PCA-6029's Intel I219-V (LAN1) and I211-AT (LAN2 in G2) Gigabit integrated controllers support all major network operating systems. However, the installation procedure varies from system to system. Please find and use the section that provides the driver setup procedure for the operating system you are using.

7.3 Win7/Win8/Win10 Driver Setup (LAN)

Please download LAN graphic driver by clicking "Download".

Note! *Wrong driver installation may cause unexpected system instability.*



PCA-6029_LAN_Win7(32&64bit)_Win8(64bit)_Win10(64bit)

2020-10-20

Download

Chapter 8

Intel ME

8.1 Introduction

The Intel® ME software components that need to be installed depend on the system's specific hardware and firmware features. The installer detects the system's capabilities and installs the relevant drivers and applications.

8.2 Installation

Please download ME driver by clicking "Download".

Note! *If the Intel® Management Engine (Intel® ME) driver has not been successfully installed, you may see an error on a "PCI Simple Communications Controller" in Device Manager.*



PCA-6029_Others_Win7(32&64bit)_Win8(64bit)_Win10(64bit)
2020-12-09

Download

Chapter 9

Intel USB 3.0

9.1 Introduction

PCA-6029 provides Intel® USB 3.0 and the data transfer rates of USB 3.0 (5 Gbps) which is 10 times faster than USB 2.0 (480Mbps).

9.2 Installation

Please download USB driver by clicking Download.

Note!



The Intel USB 3.0 eXtensible Host Controller Driver is not supported natively on Windows 7. USB ports cannot function during standard Windows 7 installation. To workaround this issue, there are 2 solutions:

1. *Use SATA ODD and PS/2 keyboard/mouse to install OS, then install the USB 3.0 driver under OS.*
2. *Use modified OS image or installation media with integrated USB 3.0 driver.*

PCA-6029_USB3.0_Win7(32&64bit)

2020-10-20

Download

Chapter 10

SATA RAID Setup

10.1 Introduction

To support demanding disk I/O, Intel Q87/C226 chipset integrates six Serial ATA controllers with software RAID 0, 1, 5, 10 capabilities.

RAID 0 striping increases the storage performance and is designed to speed up data transfer rates for disk-intensive applications.

RAID 1 mirroring protects valuable data that might be lost in the event of a hard drive failure.

RAID 5 array contains three or more hard drives where the data is divided into manageable blocks called strips. Parity is a mathematical method for recreating data that was lost from a single drive, which increases fault-tolerance. The data and parity are striped across all the hard drives in the array. The parity is striped in a rotating sequence to reduce bottlenecks associated with the parity calculations.

RAID 10 array uses four hard drives to create a combination of RAID levels 0 and 1. The data is striped across a two-drive array forming the RAID 0 component. Each of the drives in the RAID 0 array is then mirrored by a RAID 1 component.

10.2 SATA RAID Driver and Utility Setup

The driver is in the CD's "07_Others/Raid/" folder. Go to the directory and follow Intel's installation guide to install the driver and utility.

Note!



For the detailed installation instructions for the SATA RAID driver and utility, please check the User Guide in the driver CD. Path: "07_Others/Raid/".

Note!



Before you install the Intel Rapid Storage Technology, please read the "readme.txt" which is in the folder "07_Others/Raid/".

Appendix **A**

Programming the Watchdog Timer

A.1 Introduction

The PCA-6029's watchdog timer can be used to monitor system software operation and take corrective action if the software fails to function within the programmed period. This section describes the operation of the watchdog timer and how to program it.

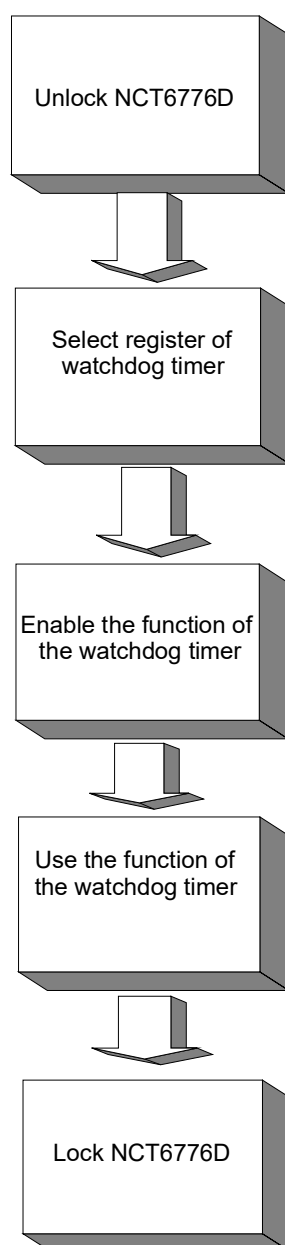
A.1.1 Watchdog timer overview

The watchdog timer is built in to the NCT6776D super I/O controller. It provides the following user programmable functions:

- Can be enabled and disabled via user's program
- Timer can be set from 1 to 255 seconds
- Generates a reset signal if the software fails to reset the timer before time-out

A.1.2 Programming the watchdog timer

The I/O port address of the watchdog timer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. You must first write an address value into address port 2E (hex), then write/read data to/from the assigned register through data port 2F (hex).


Table A.1: Watchdog timer registers

Address of register (2E)	Attribute Read/Write	Value (2F)& description
87 (hex)	----	Write this address to I/O address port 2E (hex) twice to unlock the NCT6776D
07 (hex)	write	Write 08 (hex) to select register of watchdog timer.
30 (hex)	write	Write 01 (hex) to enable the function of the watchdog timer. Disabled is set as default.
F5 (hex)	write	<p>Set seconds or minutes as units for the timer. Write 0 to bit 3: set second as counting unit. [default]. Write 1 to bit 3: set minutes as counting unit</p> <p>Write 1 to bit 4: Watchdog timer count mode is 1000 times faster.</p> <p>If bit 3 is 0, the count mode is 1/1000 seconds mode. If bit 3 is 1, the count mode is 1/1000 minutes mode.</p>

F6 (hex)	write	0: stop timer [default] 01~FF (hex): The amount of the count, in seconds or minutes, depends on the value set in register F5 (hex). This number decides how long the watchdog timer waits for strobe before generating an interrupt or reset signal. Writing a new value to this register can reset the timer to count with the new value.
F7 (hex)	read/write	Bit 6: Write 1 to enable keyboard to reset the timer, 0 to disable.[default] Bit 5: Write 1 to generate a timeout signal immediately and automatically return to 0. [default=0] Bit 4: Read status of watchdog timer, 1 means timer is "timeout".
AA (hex)	-----	Write this address to I/O port 2E (hex) to lock the NCT6776D.

A.1.3 Example program

1. Enable watchdog timer and set 10 sec. as timeout interval

```

;-----
Mov dx,2eh          ; Unlock NCT6776D
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h          ; Select registers of watchdog timer
Out dx,al
Inc dx
Mov al,08h
Out dx,al
;-----
Dec dx              ; Enable the function of watchdog timer
Mov al,30h
Out dx,al
Inc dx
In al,dx
Or al,01h
Out dx,al
;-----
Dec dx              ; Set second as counting unit
Mov al,0f5h
Out dx,al
Inc dx
In al,dx
And al,not 08h
Out dx,al
;-----
Dec dx              ; Set timeout interval as 10 seconds and start counting
Mov al,0f6h

```

```

Out    dx,al
Inc     dx
Mov     al,10      ; 10 seconds
Out     dx,al
;-----

```

```

Dec dx          ; Lock NCT6776D
Mov     al,0aah
Out     dx,al

```

2. Enable watchdog timer and set 5 minutes as timeout interval

```

;-----
Mov dx,2eh      ; Unlock NCT6776D
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h      ; Select registers of watchdog timer
Out     dx,al
Inc     dx
In      al,dx
Or      al,08h
Out     dx,al
;-----
Dec dx          ; Enable the function of watchdog timer
Mov     al,30h
Out     dx,al
Inc     dx
Mov     al,01h
Out     dx,al
;-----
Dec dx          ; Set minute as counting unit
Mov     al,0f5h
Out     dx,al
Inc     dx
In      al,dx
Or      al,08h
Out     dx,al
;-----
Dec dx          ; Set timeout interval as 5 minutes and start counting
Mov     al,0f6h
Out     dx,al
Inc     dx
Mov     al,5      ; 5 minutes
Out     dx,al
;-----

```

```
Dec dx          ; Lock NCT6776D
Mov    al,0aah
Out    dx,al
```

3. Enable watchdog timer to be reset by mouse

```
;-----
Mov dx,2eh      ; Unlock NCT6776D
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h      ; Select registers of watchdog timer
Out    dx,al
Inc    dx
Mov    al,08h
Out    dx,al
;-----
Dec dx          ; Enable the function of watchdog timer
Mov    al,30h
Out    dx,al
Inc    dx
In     al,dx
Or     al,01h
Out    dx,al
;-----
Dec dx          ; Enable watchdog timer to be reset by mouse
Mov    al,0f7h
Out    dx,al
Inc    dx
In     al,dx
Or    al,80h
Out    dx,al
;-----
Dec dx          ; Lock NCT6776D
Mov    al,0aah
Out    dx,al
```

4. Enable watchdog timer to be reset by keyboard

```
;-----
Mov dx,2eh      ; Unlock NCT6776D
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h      ; Select registers of watchdog timer
```

```

Out    dx,al
Inc     dx
Mov     al,08h
Out     dx,al
;-----
Dec dx          ; Enable the function of watchdog timer
Mov     al,30h
Out     dx,al
Inc     dx
Mov     al,01h
Out     dx,al
;-----
Dec dx          ; Enables watchdog timer to be strobe reset by keyboard
Mov     al,0f7h
Out     dx,al
Inc     dx
In      al,dx
Or      al,40h
Out     dx,al
;-----
Dec dx          ; Lock NCT6776D
Mov     al,0aah
Out     dx,al

```

5. Generate a time-out signal without timer counting

```

;-----
Mov dx,2eh      ; Unlock NCT6776D
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h      ; Select registers of watchdog timer
Out     dx,al
Inc     dx
Mov     al,08h
Out     dx,al
;-----
Dec dx          ; Enable the function of watchdog timer
Mov     al,30h
Out     dx,al
Inc     dx
Mov     al,01h
Out     dx,al
;-----
Dec dx          ; Generate a time-out signal

```

```
Mov    al,0f7h
Out     dx,al      ;Write 1 to bit 5 of F7 register
Inc     dx
In      al,dx
Or      al,20h
Out     dx,al
;-----
Dec dx      ; Lock NCT6776D
Mov     al,0aah
Out     dx,al
```

Appendix **B**

I/O Pin Assignments

B.1 Parallel Port Connector (LPT1)

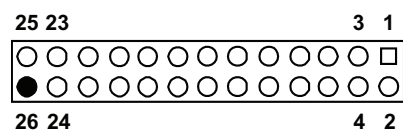


Table B.1: Parallel port connector (LPT1)

Pin	Signal	Pin	Signal
1	STROBE*	2	AUTOFD*
3	D0	4	ERR
5	D1	6	INIT*
7	D2	8	SLCTINI*
9	D3	10	GND
11	D4	12	GND
13	D5	14	GND
15	D6	16	GND
17	D7	18	GND
19	ACK*	20	GND
21	BUSY	22	GND
23	PE	24	GND
25	SLCT	26	N/C

* low active

B.2 VGA Connector (VGA1)

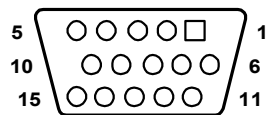


Table B.2: VGA connector (VGA1)

Pin	Signal	Pin	Signal
1	RED	9	VCC
2	GREEN	10	GND
3	BLUE	11	N/C
4	N/C	12	SDT
5	GND	13	H-SYNC
6	GND	14	V-SYNC
7	GND	15	SCK
8	GND		

B.3 RS 232 Serial Port (COM12)

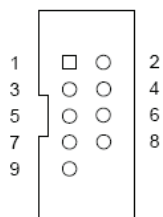


Table B.3: RS-232 serial port (COM2)

Pin	Signal
1	DCD
2	DSR
3	SIN
4	RTS
5	SOUT
6	CTS
7	DTR
8	RI
9	GND

B.4 USB 2.0 Header (USB2H1, USB2H2, USB2H3)

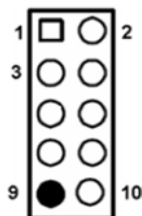


Table B.4: USB Header (USB2H1, USB2H3)

Pin	Signal	Pin	Signal
1	USB1_VCC5	6	USB2_D+
2	USB2_VCC5	7	GND
3	USB1_D-	8	GND
4	USB2_D-	9	Key
5	USB1_D+	10	NC

Table B.5: USB Header (USB2H2)

Pin	Signal	Pin	Signal
1	USB1 VCC5	6	USB2 D+
2	USB2_VCC5	7	GND
3	USB1 D-	8	GND
4	USB2 D-	9	NC
5	USB1 D+	10	Key

B.5 USB3.0 Header (USB3H1)

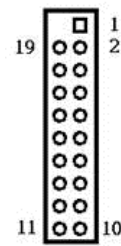


Table B.6: USB 3.0 Header (USB12)

Pin	Signal	Pin	Signal
1	USB1_VCC5	11	USB_P+_P2
2	USB3.0_RXN_P1	12	USB_P-_P2
3	USB3.0_RXP_P1	13	GND
4	GND	14	USB3.0_TXP_P2
5	USB3.0_TXN_P1	15	USB3.0_TXN_P2
6	USB3.0_TXP_P1	16	GND
7	GND	17	USB3.0_RXP_P2
8	USB_P-_P1	18	USB3.0_RXN_P2
9	USB_P+_P1	19	USB2_VCC5
10	Reserve		

B.6 PS/2 Keyboard/Mouse Connector (KBMS1)

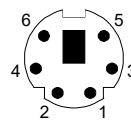


Table B.7: PS/2 keyboard/mouse connector (KBMS1)

Pin	Signal
1	KB DATA
2	MS DATA
3	GND
4	VCC
5	KB CLOCK
6	MS CLOCK

B.7 External Keyboard Connector (KBMS2)

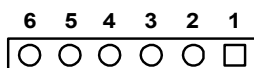


Table B.8: External keyboard connector (KBMS2)

Pin	Signal
1	KBCLK
2	KBDAT
3	MSDAT
4	GND
5	MSVCC
6	MSCLK

B.8 CPU Fan Power Connector (CPUFAN1)

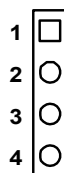


Table B.9: CPU fan power connector (CPUFAN1)

Pin	Signal
1	GND
2	+12V
3	Detect
4	PWM

B.9 Power LED and Keyboard Lock Connector (JFP3 / PWR_LED & KEY LOCK)

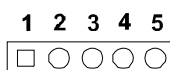


Table B.10: Power LED and keyboard lock connector (JFP3 / PWR_LED & KEY LOCK)

Pin	Signal
1	LED power (+3.3 V)
2	NC
3	GND
4	KEYLOCK#
5	GND

B.10 External Speaker Connector (JFP2 / SPEAKER)

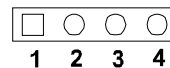


Table B.11: External speaker connector (JFP2 / SPEAKER)

Pin	Signal
1	SPK_CN17P1
2	SPK_CN17P2
3	SPK_CN17P3
4	SPK_CN17P4

B.11 Reset Connector (JFP1 / RESET)

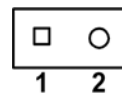


Table B.12: Reset connector (JFP1 / RESET)

Pin	Signal
1	RESET #
2	GND

B.12 HDD LED (JFP2 / HDDLED)

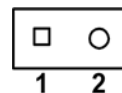


Table B.13: HDD LED (JFP2 / HDDLED)

Pin	Signal
1	HDD LED
2	SATA LED

B.13 ATX Soft Power Switch (JFP1 / PWR_SW)

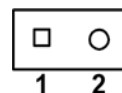


Table B.14: ATX soft power switch (JFP1 / PWR_SW)

Pin	Signal
1	3.3 VSB
2	PWR-BTN

B.14 Hi-definition Audio Link Connector (HDAUD1)

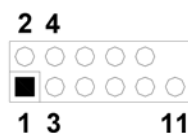


Table B.15: Hi-definition audio link connector (HDAUD1)

Pin	Signal	Pin	Signal
1	ACZ_VCC	2	GND
3	ACZ_SYNC	4	ACZ_BITCLK
5	ACZ_SDOUT	6	ACZ_SDIN0
7	ACZ_SDIN1	8	-ACZ_RST
9	ACZ_12V	10	GND
11	GND	12	N/C

B.15 SM Bus Connector (JFP2 / SNMP)

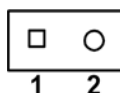


Table B.16: SM bus connector (JFP2 / SNMP)

Pin	Signal
1	SMB_DATA
2	SMB_CLK

B.16 LAN1 and LAN2 LED Connector (LANLED1)

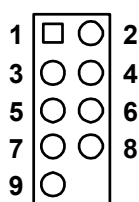


Table B.17: LAN1 and LAN2 LED connector (LANLED1)

Pin	Signal
1	#LAN1_ACT
2	#LAN2_ACT
3	V33_AUX
4	V33_AUX
5	#LAN1_LINK1000
6	#LAN2_LINK1000
7	#LAN1_LINK100
8	#LAN2_LINK100
9	V33_AUX

B.17 GPIO Header (GPIO1)

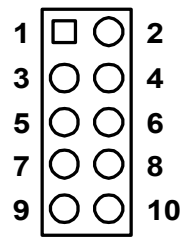


Table B.18: GPIO header (GPIO1)

Pin	Signal
1	SIO_GPIO0
2	SIO_GPIO4
3	SIO_GPIO1
4	SIO_GPIO5
5	SIO_GPIO2
6	SIO_GPIO6
7	SIO_GPIO3
8	SIO_GPIO7
9	VCC_GPIO
10	GND

B.18 Fixed I/O Ranges Decoded by Intel PCH

Table B.19: Fixed I/O Ranges Decoded by PCH

I/O Address	Read Target	Write Target	Internal Unit
00h–1Fh	RESERVED	RESERVED	Not Decoded
20h–21h	Interrupt Controller	Interrupt Controller	Interrupt
24h–25h	Interrupt Controller	Interrupt Controller	Interrupt
28h–29h	Interrupt Controller	Interrupt Controller	Interrupt
2Ch–2Dh	Interrupt Controller	Interrupt Controller	Interrupt
2Eh–2Fh	LPC SIO	LPC SIO	Forwarded to LPC
30h–31h	Interrupt Controller	Interrupt Controller	Interrupt
34h–35h	Interrupt Controller	Interrupt Controller	Interrupt
38h–39h	Interrupt Controller	Interrupt Controller	Interrupt
3Ch–3Dh	Interrupt Controller	Interrupt Controller	Interrupt
40h–42h	Timer/Counter	Timer/Counter	PIT (8254)
43h	RESERVED	Timer/Counter	PIT
4Eh–4Fh	LPC SIO	LPC SIO	Forwarded to LPC
50h–52h	Timer/Counter	Timer/Counter	PIT
53h	RESERVED	Timer/Counter	PIT
60h	Microcontroller	Microcontroller	Forwarded to LPC
61h	NMI Controller	NMI Controller	Processor I/F
62h	Microcontroller	Microcontroller	Forwarded to LPC
63h	NMI Controller	NMI Controller	Processor I/F

64h	Microcontroller	Microcontroller	Forwarded to LPC
65h	NMI Controller	NMI Controller	Processor I/F
66h	Microcontroller	Microcontroller	Forwarded to LPC
67h	NMI Controller	NMI Controller	Processor I/F
70h	RESERVED1	NMI and RTC Controller	RTC
71h	RTC Controller	RTC Controller	RTC
72h	RTC Controller	NMI and RTC Controller	RTC
73h	RTC Controller	RTC Controller	RTC
74h	RTC Controller	NMI and RTC Controller	RTC
75h	RTC Controller	RTC Controller	RTC
76h	RTC Controller	NMI and RTC Controller	RTC
77h	RTC Controller	RTC Controller	RTC
80h	LPC or PCIe2	LPC or PCIe2	Forwarded to LPC or PCIe
81h–83h	RESERVED	RESERVED	Not Decoded
84h–86h	RESERVED	LPC or PCIe	Forwarded to LPC or PCIe
87h	RESERVED	RESERVED	Not Decoded
88h	RESERVED	LPC or PCIe2	Forwarded to LPC or PCIe
89h–8Bh	RESERVED	RESERVED	Not Decoded
8Ch–8Eh	RESERVED	LPC or PCIe2	Forwarded to LPC or PCIe
8Fh	RESERVED	RESERVED	Not Decoded
90h	Alias to 80h	Alias to 80h	Forwarded to LPC
91h	RESERVED	RESERVED	Not Decoded
92h	Reset Generator	Reset Generator	Processor I/F
93h–9Fh	RESERVED	RESERVED	Forwarded to LPC
A0h–A1h	Interrupt Controller	Interrupt Controller	Interrupt
A4h–A5h	Interrupt Controller	Interrupt Controller	Interrupt
A8h–A9h	Interrupt Controller	Interrupt Controller	Interrupt
ACh–ADh	Interrupt Controller	Interrupt Controller	Interrupt
B0h–B1h	Interrupt Controller	Interrupt Controller	Interrupt
B2h–B3h	Power Management	Power Management	Power Management
B4h–B5h	Interrupt Controller	Interrupt Controller	Interrupt
B8h–B9h	Interrupt Controller	Interrupt Controller	Interrupt
BCh–BDh	Interrupt Controller	Interrupt Controller	Interrupt
C0h–DFh	RESERVED	RESERVED	Not Decoded
F0h	FERR# / Interrupt Controller	FERR# / Interrupt Controller	Processor I/F
170h–177h	SATA Controller, PCI, or PCIe	SATA Controller, PCI, or PCIe	SATA
1F0h–1F7h	SATA Controller, PCI, or PCIe	SATA Controller, PCI, or PCIe	SATA
200h–207h	Gameport Low	Gameport Low	Forwarded to LPC
208h–20Fh	Gameport High	Gameport High	Forwarded to LPC
376h	SATA Controller, PCI, or PCIe	SATA Controller, PCI, or PCIe	SATA
3F6h	SATA Controller, PCI, or PCIe	SATA Controller, PCI, or PCIe	SATA

4D0h–4D1h	Interrupt Controller	Interrupt Controller	Interrupt
CF9h	Reset Generator	Reset Generator	Processor I/F

B.19 System I/O Ports

Table B.20: System I/O Ports

I/O Address (Hex)	Device
290h-29Fh	H/W Monitor
2F8h-2FFh	Communication Port (COM2)
378h-37Fh	ECP Printer Port(LPT1)
3B0h-3BBh	Graphics
3C0h-3DFh	Graphics
3F8h-3FFh	Communication Port (COM1)
400h-47Fh	PMBASE
500h-57Fh	GPIOBASE
600h-67Fh	PCA-COM485 Module I/O used
778h-77Fh	ECP Printer Port(LPT1)
C80h-C9Fh	Communication port (COM3-6) for PCA-COM232 module
CA0h-CBFh	Communication port (COM8-11) for PCA-COM485 module

B.20 Interrupt Assignments

Table B.21: Interrupt Assignments

Interrupt#	Interrupt source
IRQ0	System timer
IRQ1	Keyboard
IRQ2	Interrupt from controller 2 (cascade)
IRQ3	Communication port (COM2)
IRQ4	Communication port (COM1)
IRQ5	Available
IRQ6	Communication port (COM8-11) for PCA-COM485 module
IRQ7	Available
IRQ8	System COMS/Real-time clock
IRQ9	Available
IRQ10	Intel 8/C220 series SMBus Controller
IRQ11	Communication port (COM3-6) for PCA-COM232 module
IRQ12	PS/2 mouse
IRQ13	Numeric data processor
IRQ14	Available
IRQ15	Available

B.21 1 MB Memory Map

Table B.22: 1 MB memory map

Address Range	Device
E8000h - FFFFFh	BIOS
D0000h - E7F00	Unused
C0000h - CFF00	VGA BIOS
A0000h - BFFFFh	Video Memory
00000h - 9FFFFh	Base memory

B.22 PCI Bus Map

Table B.23: PCI Bus Map

Signal	IDSEL	INT#PIN	GNT	REQ
PCI Slot 1	AD31	INT B, C, D, A	GNT A	REQ A
PCI Slot 2	AD30	INT C, D, A, B	GNT B	REQ B
PCI Slot 3	AD29	INT D, A, B, C	GNT C	REQ C
PCI Slot 4	AD28	INT A, B, C, D	GNT D	REQ D

Appendix **C**

Programming the GPIO

C.1 Supported GPIO Register

Below are the detailed descriptions of the GPIO addresses and a programming sample.

C.2 GPIO Registers

Bank	Offset	Description
09h	30h	Write 1 to bit 7 to enable GPIO
07h	E0h	GPIO I/O Register When set to a '1', respective GPIO port is programmed as an input port. When set to a '0', respective GPIO port is programmed as an output port.
07h	E1h	GPIO Data Register If a port is programmed to be an output port, then its respective bit can be read/written. If a port is programmed to be an input port, then its respective bit can only be read.
07h	E2h	GPIO Inversion Register When set to a '1', the incoming/outgoing port value is inverted. When set to a '0', the incoming/outgoing port value is the same as in data register.

C.3 GPIO Example Program-1

Enter the extended function mode, interruptible double-write

```
MOV DX,2EH
MOV AL,87H
OUT DX,AL
OUT DX,AL
```

Configure logical device, configuration register CRE0,CRE1,CRE2

```
MOV DX,2EH
MOV AL,09H
OUT DX,AC
DEC DX
MOV AL,30H
OUT DX,AL
INC DX
IN AL,DX
OR AL,10000000B
DEC DX
MOV AL,07H
OUT DX,AL
```

```

INC DX
MOV AL,07H ; Select logical device 7
OUT DX,AL ;
DEC DX
MOV AL,E0H
OUT DX,AL
INC DX
MOV AL,00H ; 1:Input 0:output for GPIO respective
OUT DX,AL
DEC DX
MOV AL,E2H ;
OUT DX,AL
INC DX
MOV AL,00H ;Set GPIO is normal not inverter
OUT DX,AL;
DEC DX
MOV AL,E1H
OUT DX,AL
INC DX
MOV AL,??H ; Put the output value into AL
OUT DX,AL

```

Exit extended function mode |

```

MOV DX,2EH
MOV AL,AAH
OUT DX,AL

```




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