

EMS-TGL-Marine

11th Generation Intel® Core™ i7/i5/i3 Marine Fanless Rugged
Embedded System

Quick Reference Guide

1st Ed –7 November 2023

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THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

- (1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.
- (2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRE OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

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1. Getting Started

1.1 Safety Precautions

Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

1.2 Packing List

- 1 x EMS-TGL-Marine 11th Gen Intel®Core™ Processor i7/i5/i3 Fanless Rugged Embedded System
- 1 x Terminal block to lockable DC Jack cable
- 1 x DP to VGA Converter
- 1 x Screw driver for chassis
- 1 x Wire tie for HDMI
- 1 x 60W/120W adapter (optional)
- 1 x Power cord (optional)
- 1 x Thermal pad for M.2 NVMe SSD



If any of the above items is damaged or missing, contact your retailer.

1.3 System Specifications

System Information	
Processor	Intel® Core™ i7-1185GRE (15W, 12M Cache, up to 2.80 GHz), WT Intel® Core™ i5-1145GRE (15W, 8M Cache, up to 2.60 GHz), WT Intel® Core™ i3-1115GRE (15W, 6M Cache, up to 3.00 GHz), WT
System Memory	2 x 260-pin SODIMM socket Max. up to 64GB DDR4 3200 MT/s
I/O Chipset	EC ITE IT8528E
BIOS Information	AMI uEFI BIOS 256 Mbit SPI Flash ROM
Watchdog Timer	H/W Reset, 1sec. ~ 65535sec.
H/W Status Monitor	CPU & system temperature monitoring and Voltages monitoring
TPM	TPM 2.0
iAMT	iAMT 15.0 (Above i5 CPU)
SBC	EBM-TGLS, EPM-1722, AUX-M07
Expansion	
M.2 (Key-X, Size, Signal)	1 x M.2 Key-B 2242/3042/3052 support SATA3/ PCIeIII x1/ USB3.1 and SIM slot1 1 x M.2 Key-E 2230 for Wi-Fi & BT Module (CNVi)
Storage	
M.2 (Key-X, Size, Signal)	1 x M.2 Key-M 2242/2280, support PCIe Gen. III x 4 (NVMe SSD) 1 x M.2 Key-B 2242, support SATA (share with expansion slot)
Edge I/O (Front)	
COM Port	2 x COM RS232/422/485 (COM1/2 mode select via BIOS setup and JCOM_SEL1/2 jumper setting, auto flow control via HW)
Power Button	1 x Push Button for Power on/off
Reset Button	1 x Push Button for Reset
Wire-Control Power On/Off	1 x 2-Pin Terminal Block for wire-control power on/off
LED Indicator	1 x Power LED (Blue) 1 x Storage LED (Yellow)- M.2 B-key SATA/ M.2 M-key PCIe Storage 1 x LTE LED (Green)- M.2 B-key PCIe/USB3 1 x Wifi LED (Green)- M.2 E key
Digital I/O	1 x 8-bit GPIO (DB9)
SIM Slot	1 x Internal SIM slot
Antenna	2 x Antenna with dust cover
Edge I/O (Rear)	
USB Port	2 x USB 3.2 Gen.2 (10Gbp/s) 2 x USB 3.2 Gen.1 (5Gbp/s) 2 x USB 2.0

EMS-TGL-Marine

COM Port	4 x COM RS232/422/485 w/ 2.5KV Isolation																																								
HDMI	1 x HDMI 2.0b																																								
DP	1 x DP++ 1.4																																								
Audio	1 x Mic-In 1 x Line-Out																																								
RJ-45	2 x RJ-45																																								
Antenna	2 x Antenna with dust cover																																								
Edge I/O (Right)																																									
Antenna	2 x Antenna with dust cover																																								
Edge I/O (Left)																																									
Antenna	2 x Antenna with dust cover																																								
Display																																									
Graphic Chipset	Intel® Iris® Xe Graphics (i7-1185GRE/i5-1145GRE) Intel® UHD Graphics for 11th Gen Intel® Processors (i3-1115GRE)																																								
Resolution	DP++ 1.4: 4096x2304 @ 60Hz HDMI 2.0b: Max. resolution 4096x2304 @ 60Hz																																								
Audio																																									
Audio Codec	Realtek ALC888 HD codec																																								
Ethernet																																									
LAN Chipset	Standard Sku: 1 x Intel I225-LM, w/ TSN (need additional SW support) 1 x Intel I219-LM, vPro support (only with i5 and i7 CPU)																																								
Specification	1x 10/100/1000/2.5G Base-Tx GbE compatible 1x 10/100/1000Base-Tx GbE compatible																																								
LED Indicator	2.5G LAN Port (i225-LM)<table><tr><th colspan="2">ACT/LINK</th><th colspan="2">SPEED</th></tr><tr><th>LED</th><th>Definition</th><th>LED</th><th>Definition</th></tr><tr><td>Light Off</td><td>No Link</td><td>Solid Orange</td><td>2.5G</td></tr><tr><td>Solid Yellow</td><td>Connection</td><td>Solid Green</td><td>1G/100M</td></tr><tr><td>Flashing</td><td>Activity</td><td>Light Off</td><td>10M</td></tr></table> 1G LAN Port (i219-LM)<table><tr><th colspan="2">ACT/LINK</th><th colspan="2">SPEED</th></tr><tr><th>LED</th><th>Definition</th><th>LED</th><th>Definition</th></tr><tr><td>Light Off</td><td>No Link</td><td>Solid Orange</td><td>1G</td></tr><tr><td>Solid Yellow</td><td>Connection</td><td>Solid Green</td><td>100M</td></tr><tr><td>Flashing</td><td>Activity</td><td>Light Off</td><td>10M</td></tr></table>	ACT/LINK		SPEED		LED	Definition	LED	Definition	Light Off	No Link	Solid Orange	2.5G	Solid Yellow	Connection	Solid Green	1G/100M	Flashing	Activity	Light Off	10M	ACT/LINK		SPEED		LED	Definition	LED	Definition	Light Off	No Link	Solid Orange	1G	Solid Yellow	Connection	Solid Green	100M	Flashing	Activity	Light Off	10M
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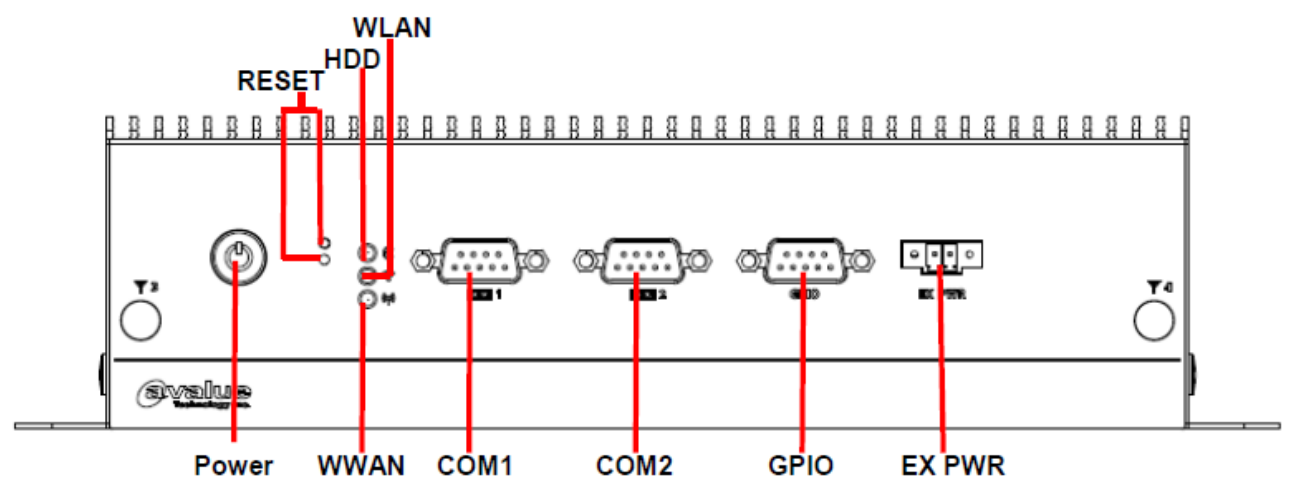
Power Requirement	
DC Input	+9.9Vdc~25.2Vdc w/1.5KV Isolation TVS component for surge protection Reverse current/voltage protection
DC Input Connector	3-Pin Terminal Block (V+, V-, GND)
ACPI	● Single power ATX Support S0,S3, S4, S5 ● ACPI 5.0 Compliant
Power Mode	AT/ATX (ATX is default setting)
Mechanical & Environment	
Operating Temp.	-40°C ~ 60°C (w/SSD) ambient w/ air flow
Storage Temp.	-30°C ~ 70°C (-22°F ~ 158°F)
Operating Humidity	40°C @ 95% Relative Humidity, Non-condensing
Dimension (W*L*H)	240mm x 150mm x 69 mm (w/ IET module)
Weight	● 2.4 kg (System) ● 3.4 kg (w/ Package)
Vibration Test	1. PSD: 0.0505G²/Hz , 5 Grms 2. Operation mode 3. Test Frequency : 5-500Hz 4. Test Axis : X,Y and Z axis 5. 30 minutes per each axis 6. IEC 60068-2-64 Test:Fh 7. Storage : SSD
Shock Test	1. Wave form : Half Sine wave 2. Acceleration Rate : 50g for operation mode 3. Duration Time : 11ms 4. No. of Shock : +/- XYZ axis 18 times 5. Operation mode 6. Reference IEC 60068-2-27 Testing procedures Test Eb : Shock Test
Drop Test	1. One corner , three edges, six faces 2. ISTA 2A, IEC-60068-2-32 Test:Ed
IP Rating	IP 50
Mounting Kit	● Wall mount kit (standard) ● DIN RAIL (optional)
Software Support	
OS Information	Win 10 64bit/ Win11 / Linux



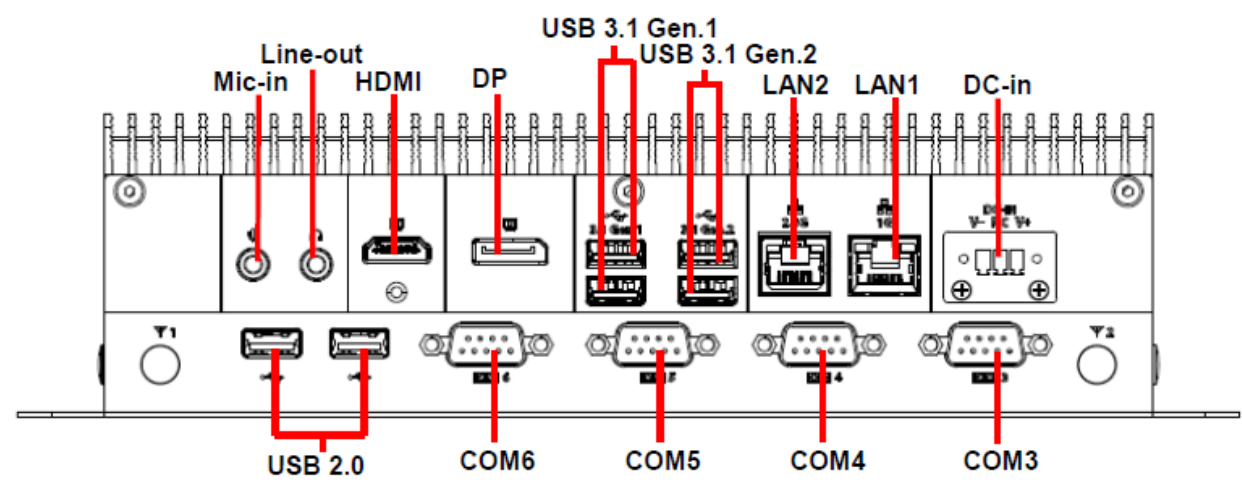
Note: Specifications are subject to change without notice.

1.4 System Overview

1.4.1 Front View



1.4.2 Rear View



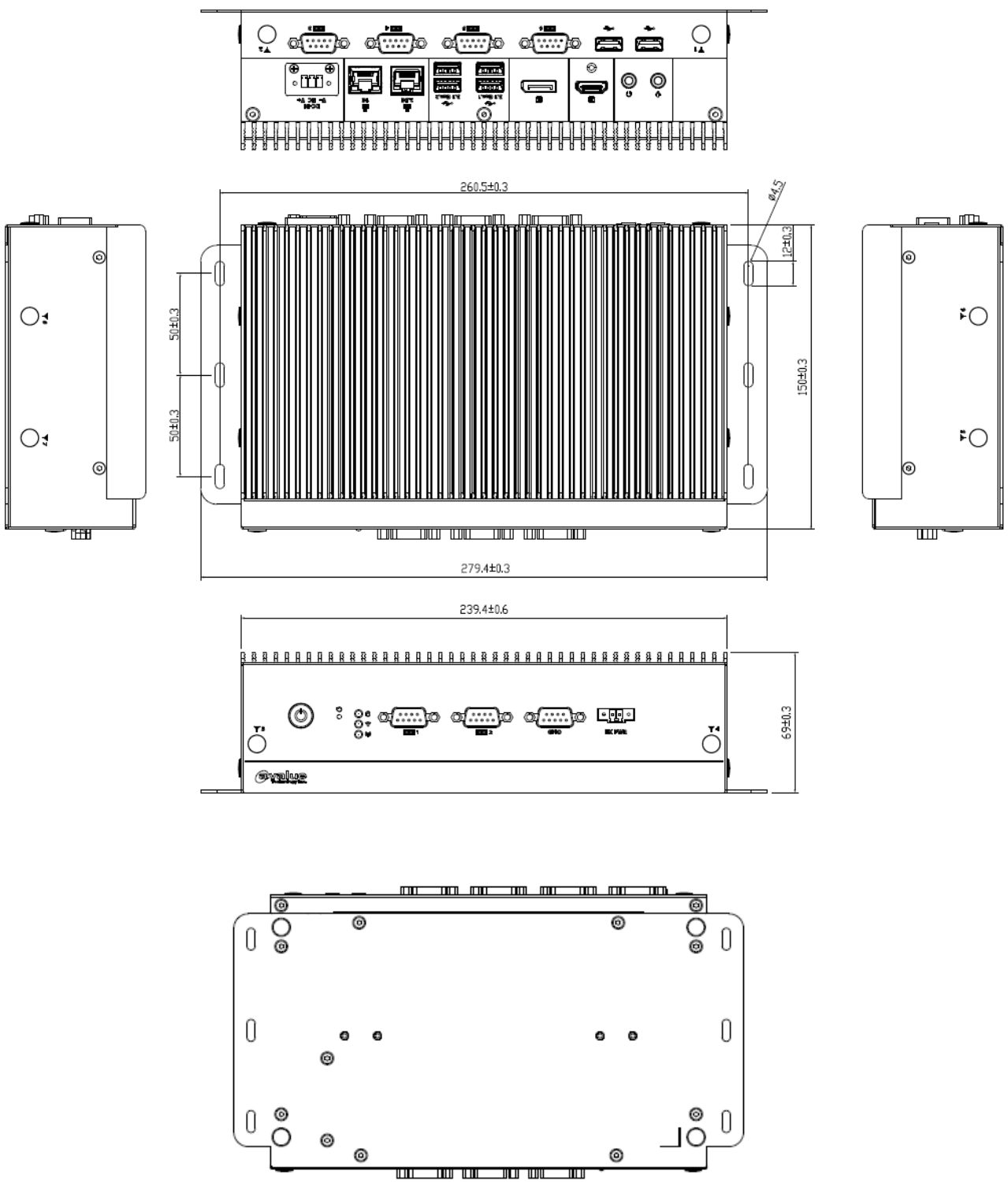
Connectors

Label	Function	Note
Power	System power indicator	
Reset	Reset button	
HDD	HDD indicator	
WWAN	WWAN Indicator	
WLAN	WLAN Indicator	
COM1/2/3/4/5/6	Serial port connector 1/2/3/4/5/6	

GPIO	General purpose I/O connector
EX PWR	Power on button
Mic-in	Mic-in audio jack
Line-out	Line-out jack
HDMI	HDMI connector
DP	DP connector
USB 3.1 Gen.1	2 x USB 3.1 Gen.1 connector
USB 3.1 Gen.2	2 x USB 3.1 Gen.2 connector
USB 2.0	2 x USB 2.0 connector
LAN1/2	RJ-45 Ethernet 1/2
DC-in	DC power-in connector

1.5 System Dimensions

1.5.1 Front & Top view



(Unit: mm)

2. Hardware Configuration

Jumper and Connector Setting, Driver and BIOS Installing

For advanced information, please refer to:

- 1- EBM-TGLS, AUX-M07 and EPM-1722 included in this manual.

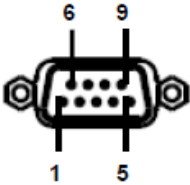
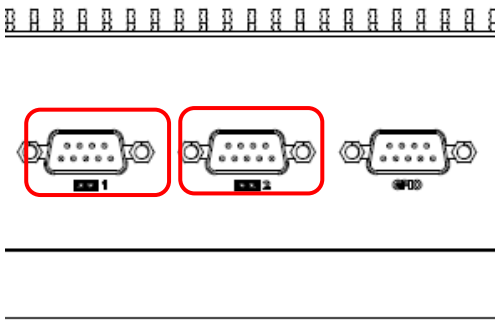


Note: If you need more information, please visit our website:

<http://www.avalue.com.tw>

2.1 EMS-TGL-Marine connector mapping

2.1.1 Serial port connector 1/2 (COM1/2)



In RS-232 Mode

Signal	PIN	PIN	Signal
NDCD#	1	6	NDSR#
NRXD	2	7	NRTS#
NTXD	3	8	NCTS#
NDTR#	4	9	NRI#
GND	5		

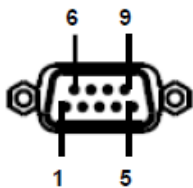
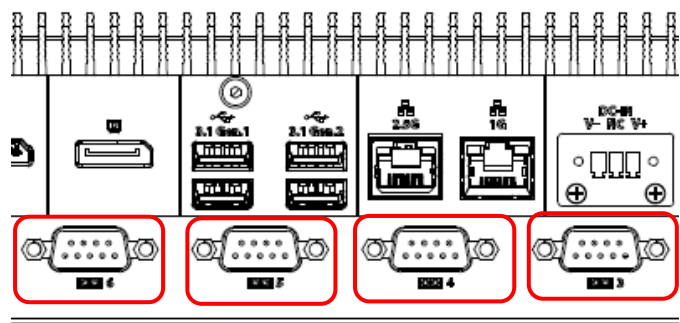
In RS-422 Mode

Signal	PIN	PIN	Signal
TxD1-	1	6	NC
TxD1+	2	7	NC
RxD1+	3	8	NC
RxD1-	4	9	NC
GND	5		

In RS-485 Mode

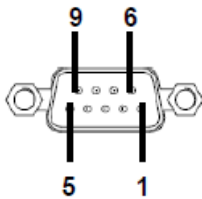
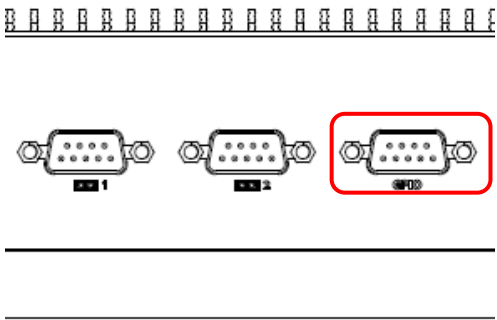
Signal	PIN	PIN	Signal
DATA1-	1	6	NC
DATA1+	2	7	NC
NC	3	8	NC
NC	4	9	NC
GND	5		

2.1.2 Serial port 3/4/5/6 connector (COM3/4/5/6)



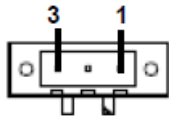
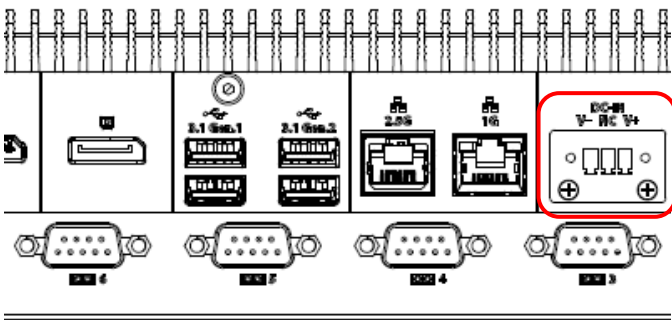
Signal	PIN	PIN	Signal
NDCD#	1	6	NDSR#
NRXD	2	7	NRTS#
NTXD	3	8	NCTS#
NDTR#	4	9	NRI#
GND	5		

2.1.3 General purpose I/O connector (GPIO)



Signal	PIN	PIN	Signal
DIO_GP20	1	6	DIO_GP10
DIO_GP21	2	7	DIO_GP11
DIO_GP22	3	8	DIO_GP12
DIO_GP23	4	9	DIO_GP13
GND	5		

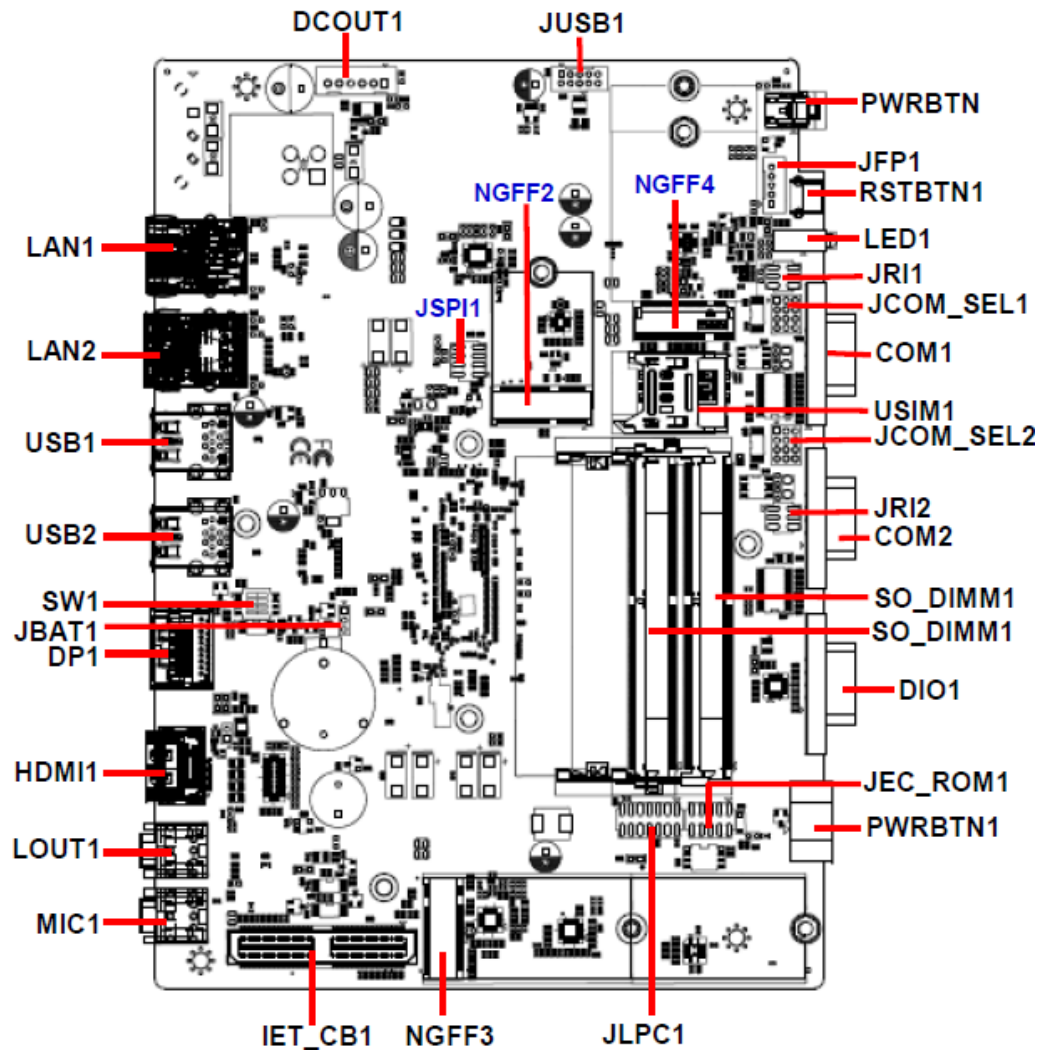
2.1.4 DC power-in connector (DC-in)



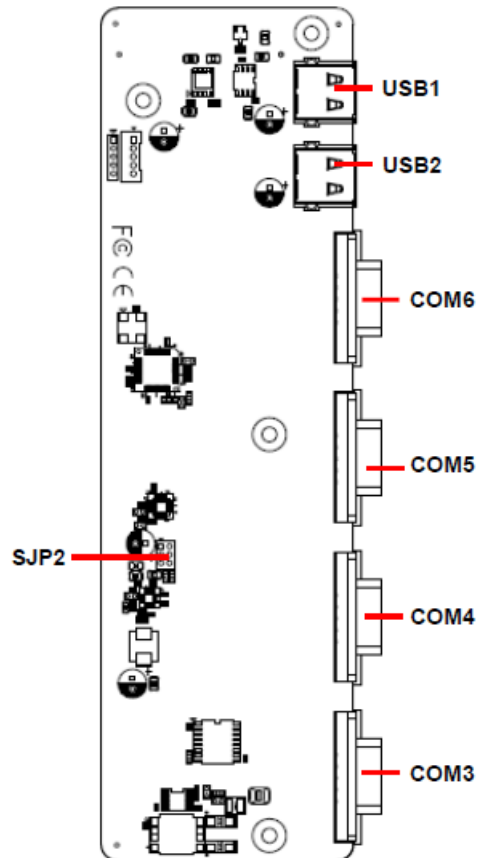
Signal	PIN
VIN + (BAT+)	1
CHASSIS_GND	2
VIN- (BAT-)	3

2.2 EBM-TGLS, AUX-M07 and EPM-1722 Overviews

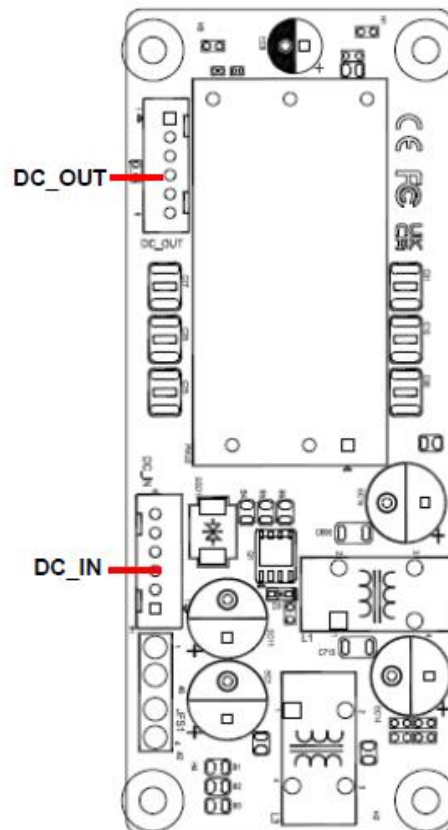
2.2.1 EBM-TGLS



2.2.2 AUX-M07



2.2.3 EPM-1722



2.3 EBM-TGLS Jumper & Connector list

Jumpers

Label	Function	Note
JBAT1	Clear CMOS	3 x 1 header, pitch 2.00 mm
JRI1/2	Serial port 1/2 pin 9 signal select	3 x 2 header, pitch 2.00 mm
JCOM_SEL1/2	Serial port 1/2 – RS232/422/485 mode select	4 x 3 header, pitch 2.00 mm
SW1	Multi-function select	SMT switch 4pin

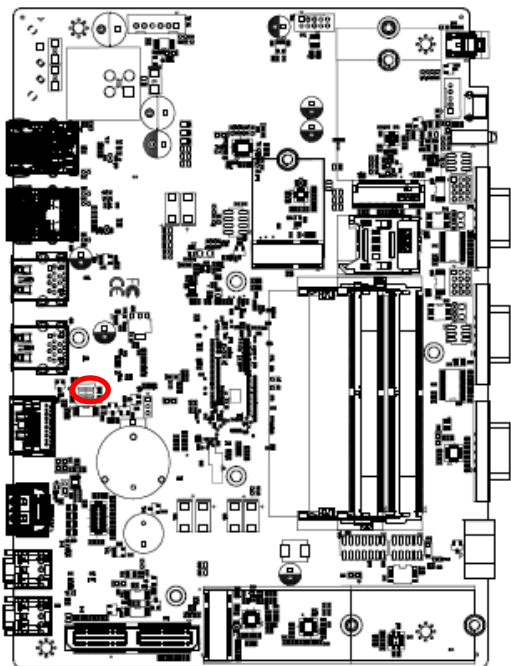
Connectors

Label	Function	Note
USB1	2 x USB3.1 Gen.2 connector	
USB2	2 x USB3.1 Gen.1 connector	
JUSB1	On-board header for USB2.0	5 x 2 wafer, pitch 2.00 mm
DIO1	General purpose I/O connector	
LAN1/2	RJ-45 Ethernet 1/2	
LOUT1	Audio line-out connector	
MIC1	Audio mic-in connector	
COM1/2	Serial port connector 1/2	
USIM1	SIM card slot	
JFP1	Front Panel Connector	5 x 1 wafer, pitch 2.00 mm
PWRBTN1	Power on/off connector	1 x 2 terminal block, pitch 3.50 mm
PWRBTN	Power on/off button	
RSTBTN1	Reset button	
LED1	WWAN 、WIFI & Storage	
RSTBTN1	Reset button	
HDMI1	HDMI connector	
SO_DIMM1/2	2 x DDR4 SODIMM connector	
IET_CB1	IET Expansion slot	
JLPC1	LPC port connector	7 x 2 header, pitch 2.00 mm
JSPI1	SPI connector	4 x 2 header, pitch 2.00 mm
DP1	DP connector	
NGFF2	M.2 KEY-E 2230 connector	

NGFF3	M.2 KEY-M 2242/2280 connector	
NGFF4	M.2 KEY-B 2242/3042/3052 connector	
DCOUT1	DC Output connector	6 x 1 wafer, pitch 2.50 mm
JEC_ROM1	EC Debug connector	5 x 2 header, pitch 2.00 mm

2.4 EBM-TGLS Jumpers & Connectors settings

2.4.1 Multi-function select (SW1)



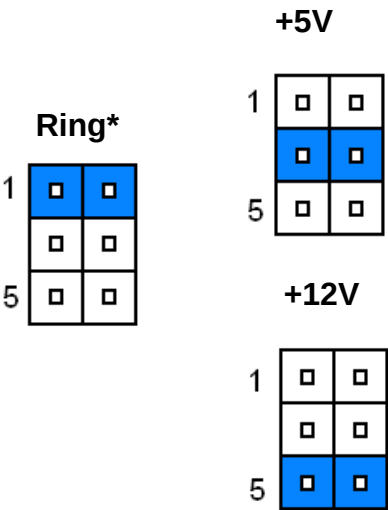
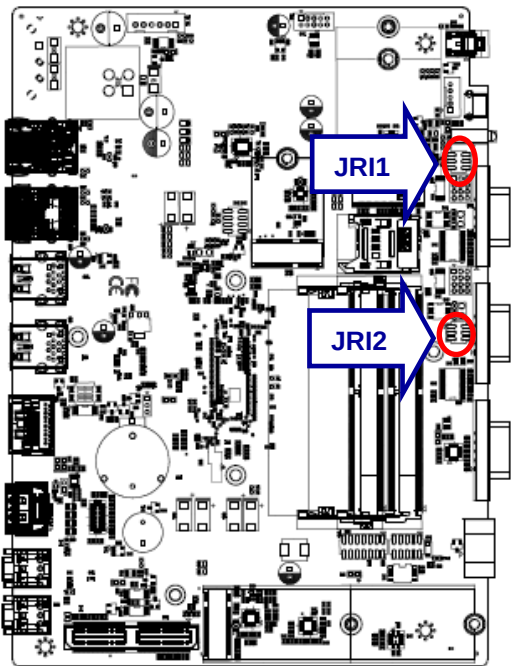
Power mode

	AT	ATX*
1	ON	OFF

DDI2 mode(DP+)

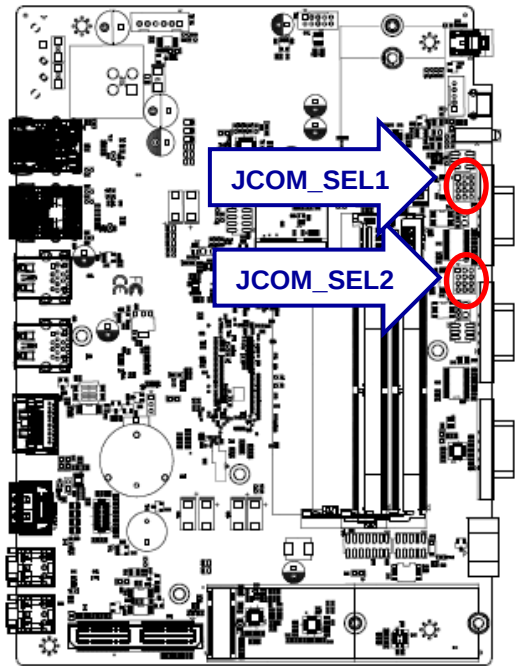
	DisplayPort*	HDMI	Cable select
2	OFF	OFF	ON
3	ON	OFF	OFF

2.4.2 Serial port 1/2 pin 9 signal select (JRI1/2)



* Default

2.4.3 Serial port 1/2 RS-232/422/485 mode select (JCOM_SEL1/2)



RS-232*

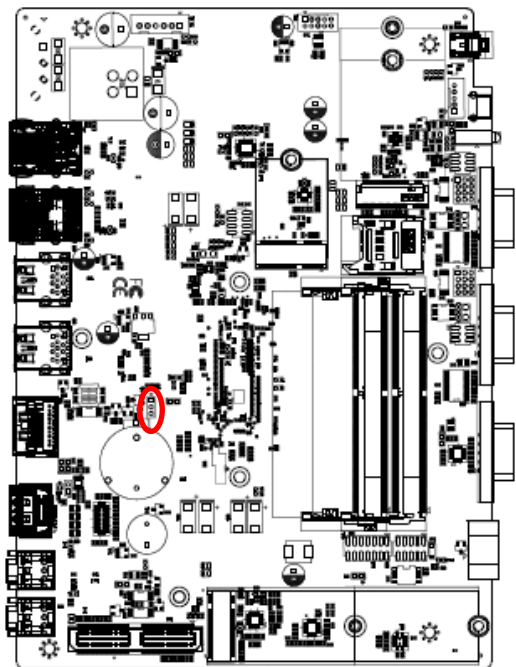
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	☒	☐	☐	
	☐	☒	☐	
10	☐	☒	☐	12

RS-422/ 485

1	☐	☒	☒	3
	☐	☒	☒	
	☐	☒	☒	
10	☐	☒	☒	12

*Default

2.4.4 Clear CMOS (JBAT1)



Protect *

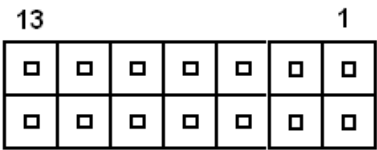
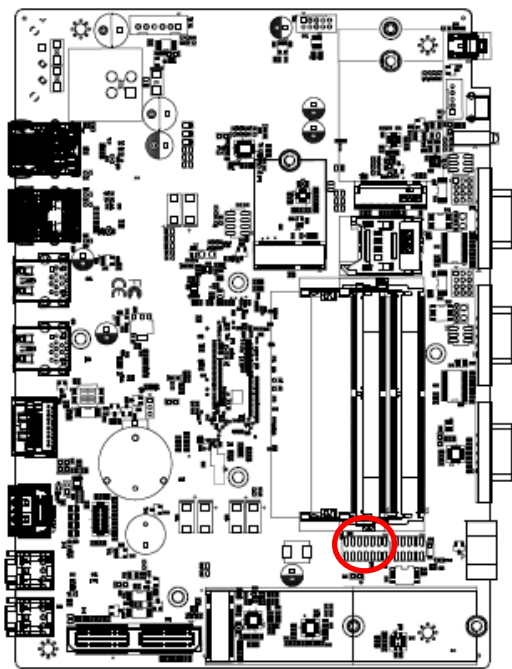
1	☒
	☒
	☐

Clear CMOS

1	☐
	☒
	☒

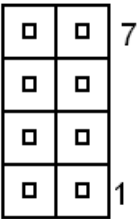
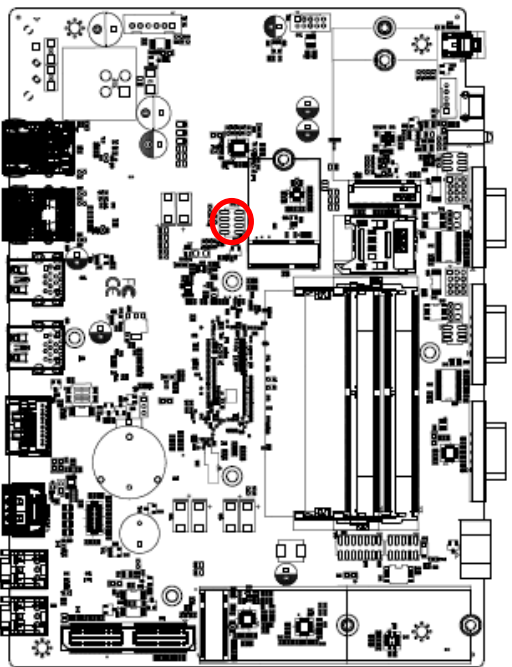
*Default

2.4.5 LPC port connector (JLPC1)



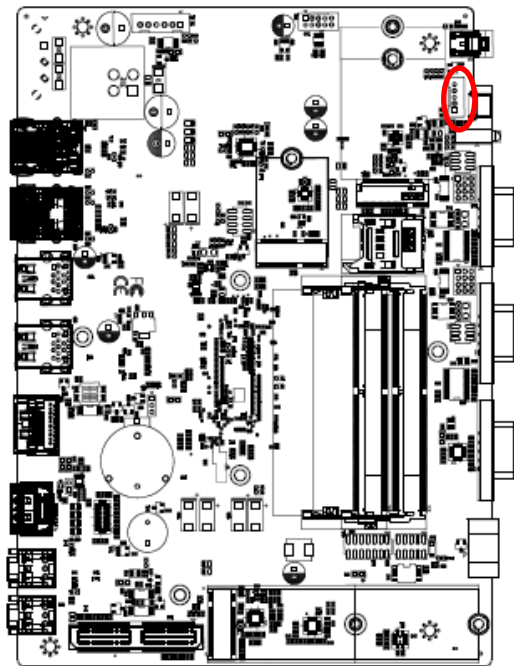
Signal	PIN	PIN	Signal
LPC_AD0	1	2	+3.3V
LPC_AD1	3	4	IET_RST#
LPC_AD2	5	6	LPC_LFRAME#
LPC_AD3	7	8	CLK_24M_80
LPC_SERIRQ	9	10	GND
+5V	11	12	GND
+5VSB	13	14	NC

2.4.6 SPI connector (JSPI1)



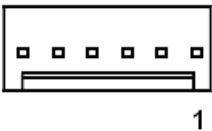
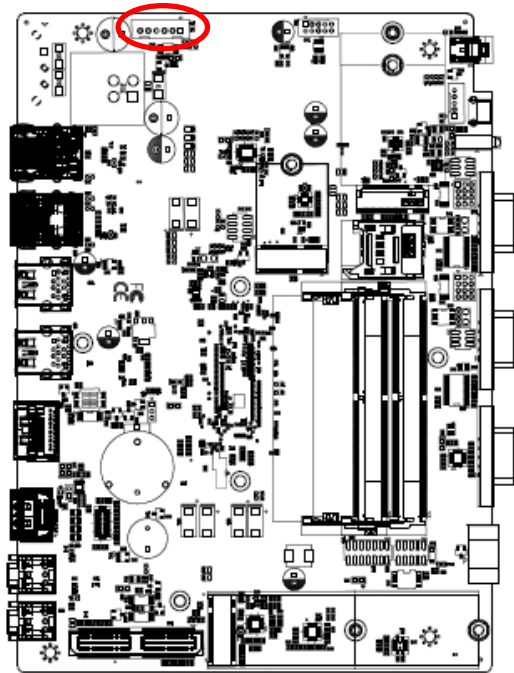
Signal	PIN	PIN	Signal
BIOS_WP#	8	7	BIOS_HOLD#
SPI0_BIOS_MOSI	6	5	SPI0_BIOS_MISO
SPI0_BIOS_CLK	4	3	SPI0_CS0#
GND	2	1	+3.3VSB

2.4.7 Front Panel Connector (JFP1)



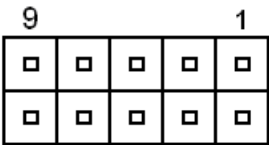
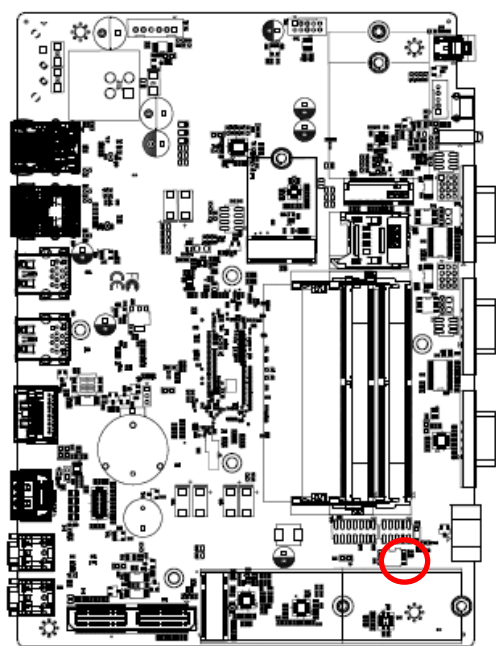
Signal	PIN
PWR_LED-	5
+5VSB	4
GND	3
PM_SYSRST#	2
PWRBTN_IN#	1

2.4.8 DC Output connector (DCOUT1)



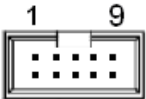
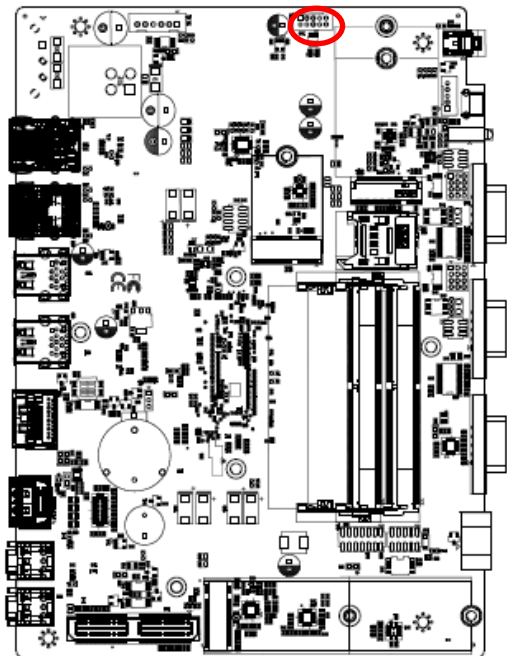
Signal	PIN
+VIN	1
+VIN	2
+VIN	3
GND	4
GND	5
GND	6

2.4.9 EC Debug connector (JEC_ROM1)



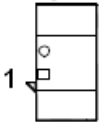
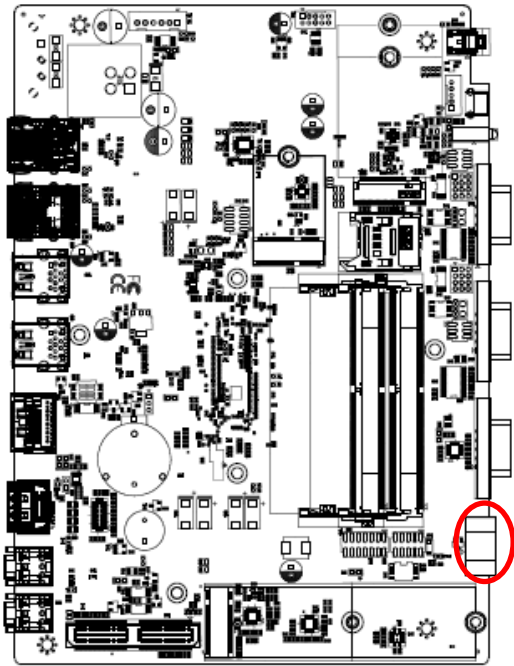
Signal	PIN	PIN	Signal
+VSPI_EC	1	2	GND
EC_FSCE#	3	4	EC_FSCK
EC_FMISO	5	6	EC_FMOSI
EC_HOLD#	7	8	NC
EC_SMCLK_DEBUG	9	10	EC_SMDAT_DEBUG

2.4.10 On-board header for USB2.0 (JUSB1)



Signal	PIN	PIN	Signal
+5VSB	1	2	+5VSB
USB2_R_DN5	3	4	USB2_R_DN4
USB2_R_DP5	5	6	USB2_R_DP4
GND	7	8	GND
GND	9	10	GND

2.4.11 Power on/off connector (PWRBTN1)



Signal	PIN
GND	2
PWRBTN#_R	1

2.5 AUX-M07 and EPM-1722 Connector list

2.5.1 AUX-M07

Connectors

Label	Function	Note
USB1~2	USB connector 1~2	
COM3~6	Serial port connector 3~6	
SJP2	SMBUS of TCA9555 address setting	3 x 2 header, pitch 2.00mm

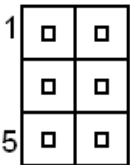
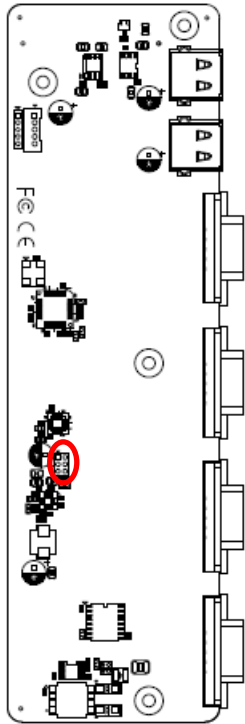
2.5.2 EPM-1722

Jumpers

Label	Function	Note
DC_OUT	DC Output connector	6 x 1 wafer, pitch 2.50mm
DC_IN	DC Input connector	6 x 1 wafer, pitch 2.50mm

2.6 AUX-M07 Connector settings

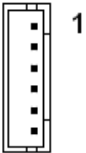
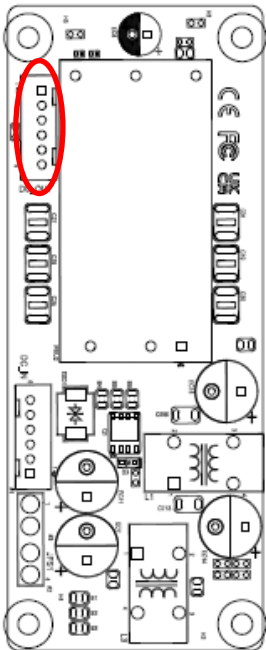
2.6.1 SMBUS of TCA9555 address setting (SJP2)



Signal	PIN	PIN	Signal
GND	1	2	SMC_9555A0
GND	3	4	SMC_9555A1
GND	5	6	SMC_9555A2

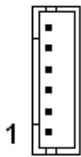
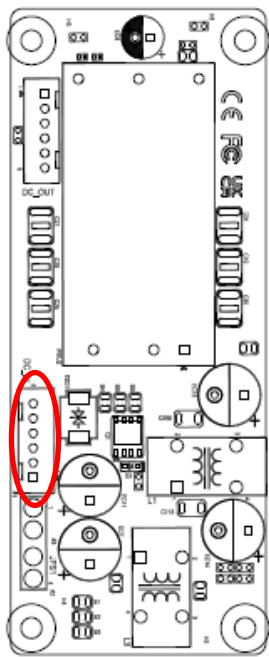
2.7 EPM-1722 Connectors settings

2.7.1 DC Output connector (DC_OUT)



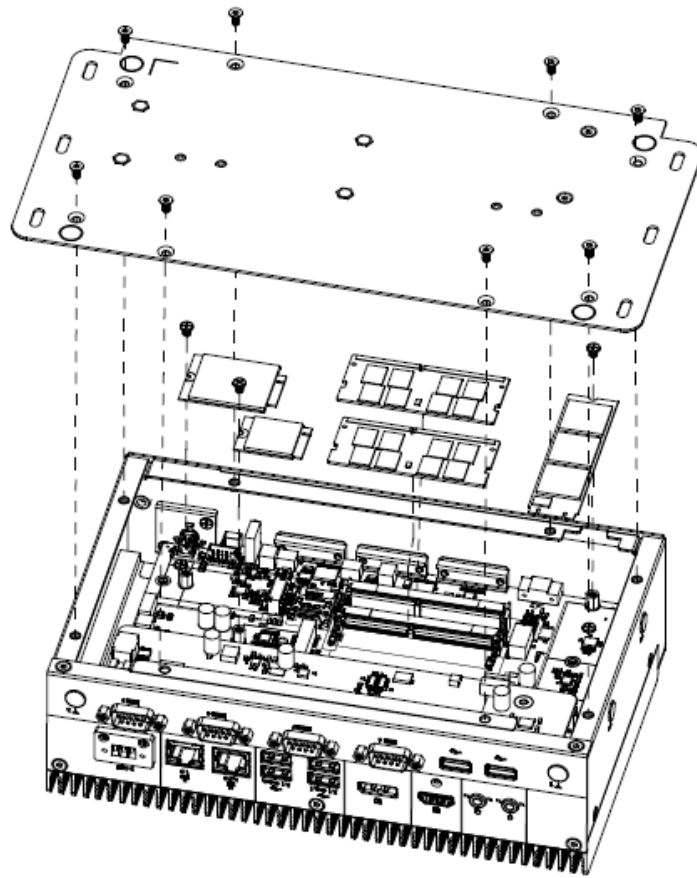
Signal	PIN
+12V_OUT	1
+12V_OUT	2
+12V_OUT	3
GND	4
GND	5
GND	6

2.7.2 DC Input connector (DC_IN)



Signal	PIN
GND	6
GND	5
GND	4
+PWRIN	3
+PWRIN	2
+PWRIN	1

2.8 Installing M.2 cards & Memory (EMS-TGL-Marine)



Step 1. Remove 8 screws from the bottom of your system and take it off.

Step 2. Slide the DDR4 SODIMM into the memory socket and press it down until properly seated.

Step 3. Insert M.2 Key-B/Key-E/Key-M cards into designated locations, between M.2 and thermal solution please paste with thermal pad, and fasten with screws to complete installation.

3.BIOS Setup

3.1 Introduction

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

3.2 Starting Setup

The AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the NVRAM and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

By pressing or <F2> immediately after switching the system on, or

By pressing the or <F2> key when the following message appears briefly at the left-top of the screen during the POST (Power On Self Test).

Press or <F2> to enter SETUP

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to.

Press F1 to Continue, DEL to enter SETUP

3.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Button	Description
↑	Move to previous item
↓	Move to next item
←	Move to the item in the left hand
→	Move to the item in the right hand
Esc key	Main Menu -- Quit and not save changes into NVRAM Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
+ key	Increase the numeric value or make changes
- key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2 key	Previous Values.
F3 key	Optimized defaults
F4 key	Save & Exit Setup

- **Navigating Through The Menu Bar**

Use the left and right arrow keys to choose the menu you want to be in.



Note: Some of the navigation keys differ from one screen to another.

- **To Display a Sub Menu**

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A “➤” pointer marks all sub menus.

3.4 Getting Help

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the F1 key again.

3.5 In Case of Problems

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the AMI BIOS supports an override to the NVRAM settings which resets your system to its defaults.

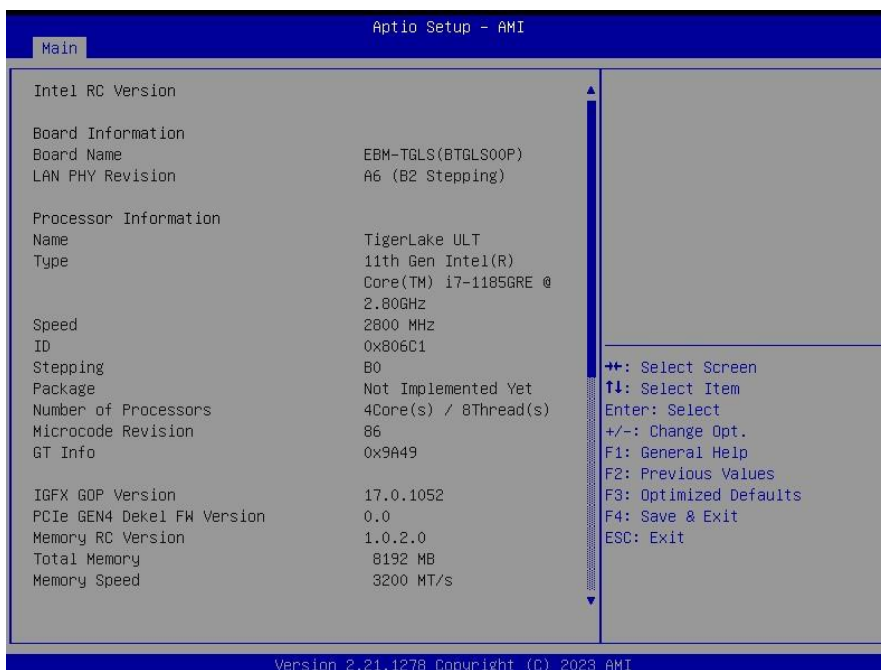
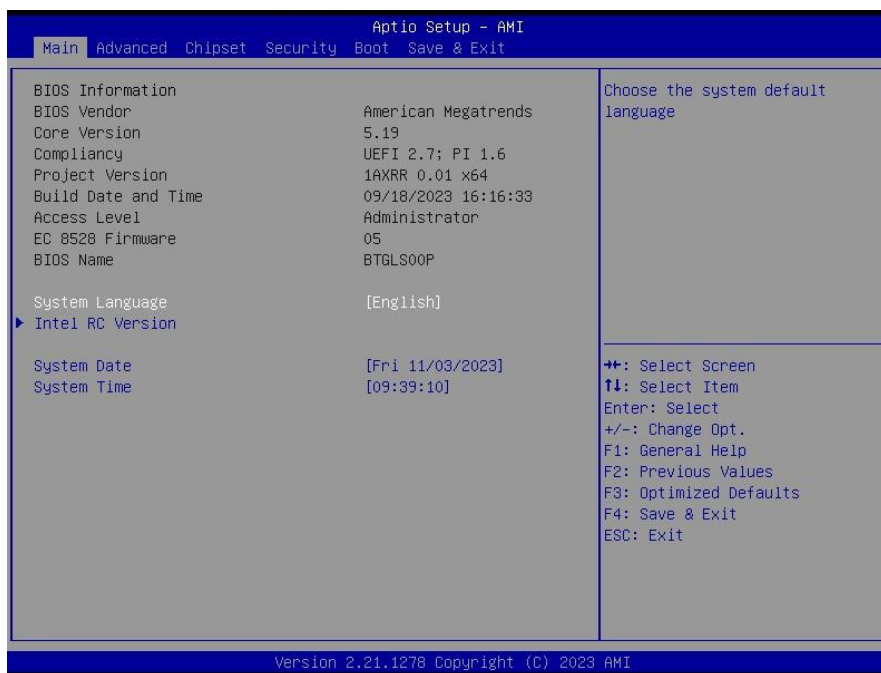
The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both BIOS Vendor and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

3.6 BIOS setup

Once you enter the Aptio Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

3.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.



3.6.1.1 System Language

This option allows choosing the system default language.

3.6.1.2 System Date

Use the system date option to set the system date. Manually enter the month, day and year.

3.6.1.3 System Time

Use the system time option to set the system time. Manually enter the hours, minutes and seconds.

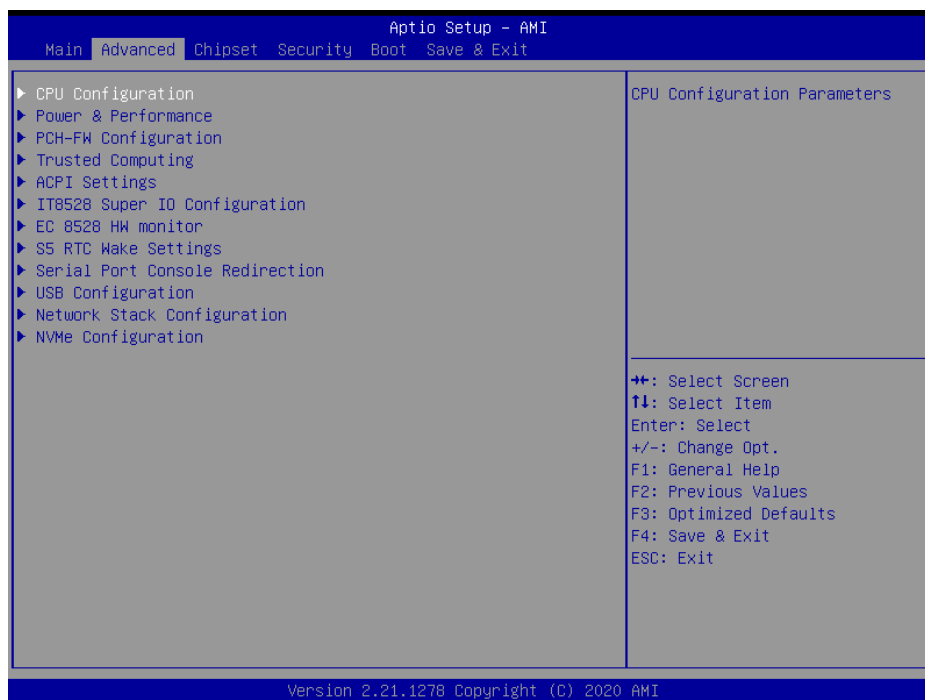


Note: The BIOS setup screens shown in this chapter are for reference purposes only, and may not exactly match what you see on your screen.

Visit the Avalue website (www.avalue.com.tw) to download the latest product and BIOS information.

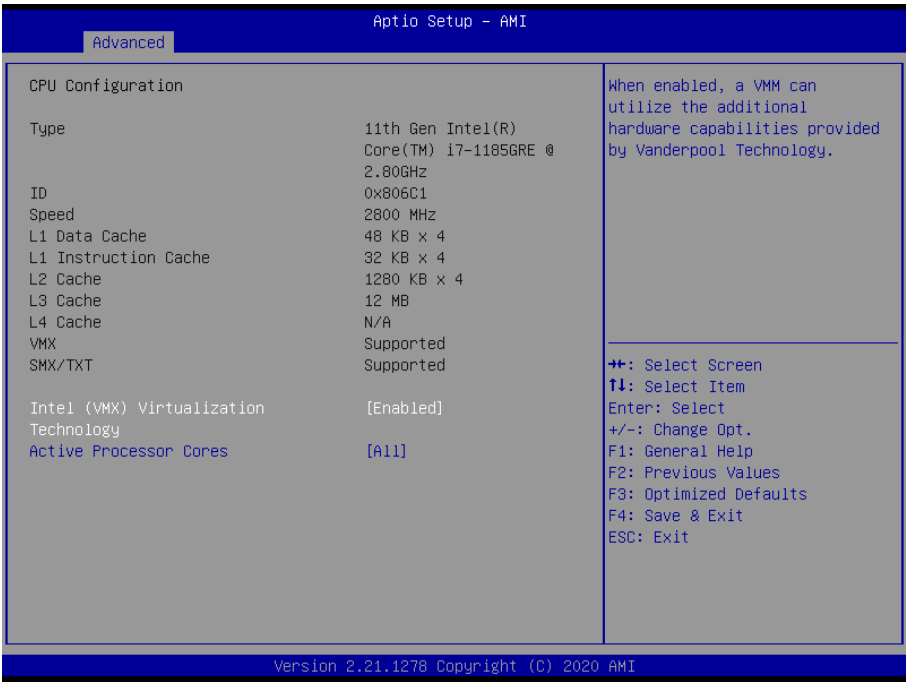
3.6.2 Advanced Menu

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.



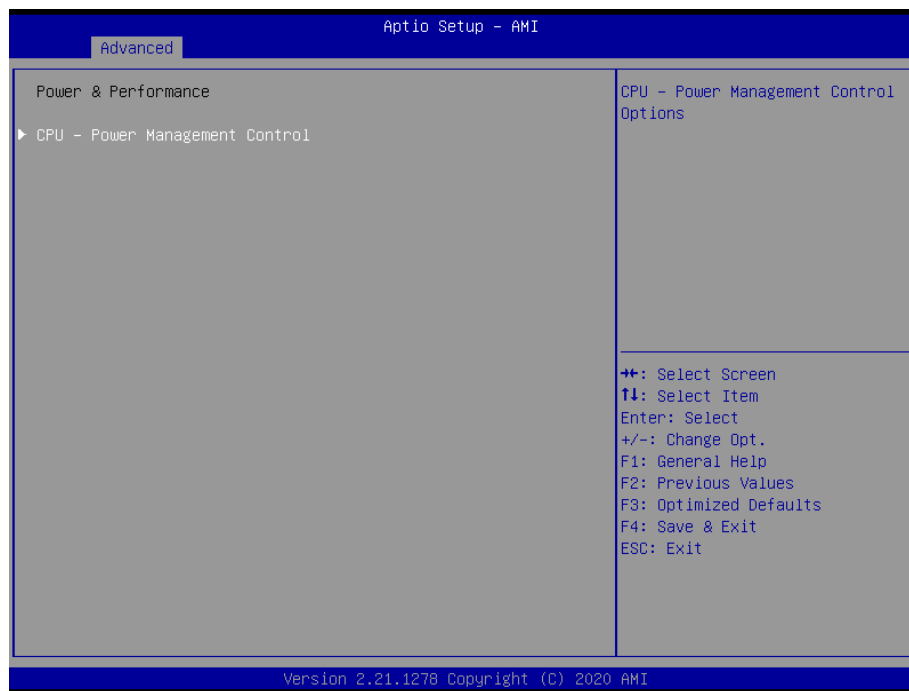
3.6.2.1 CPU Configuration

Use the CPU configuration menu to view detailed CPU specification and configure the CPU.

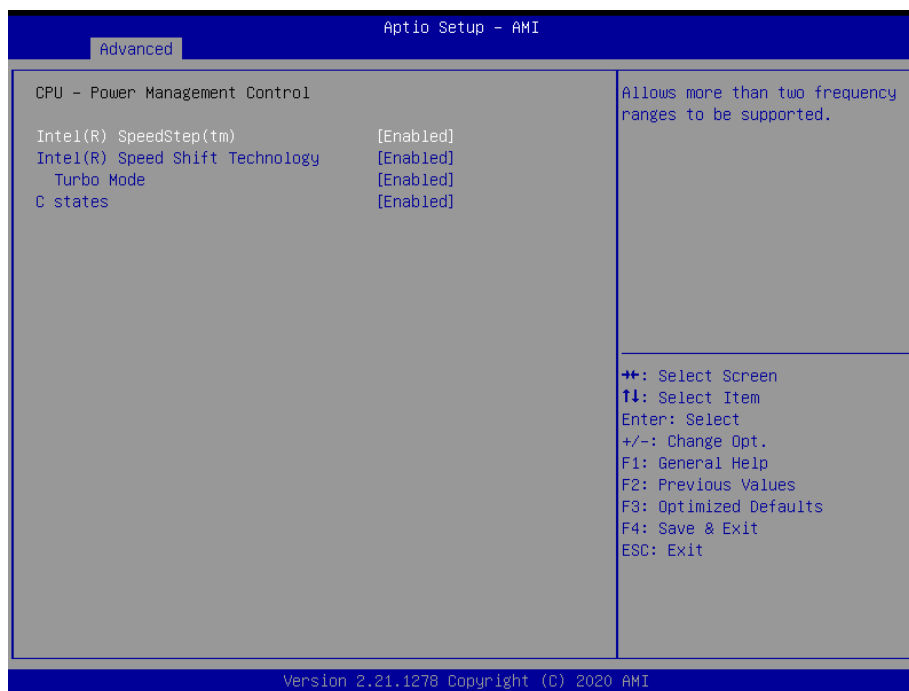


Item	Options	Description
Intel(VMX) Virtualization Technology	Disabled Enabled[Default]	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.
Active Processor Cores	All[Default] 1 2 3	Number of cores to enable in each processor package.

3.6.2.2 Power & Performance



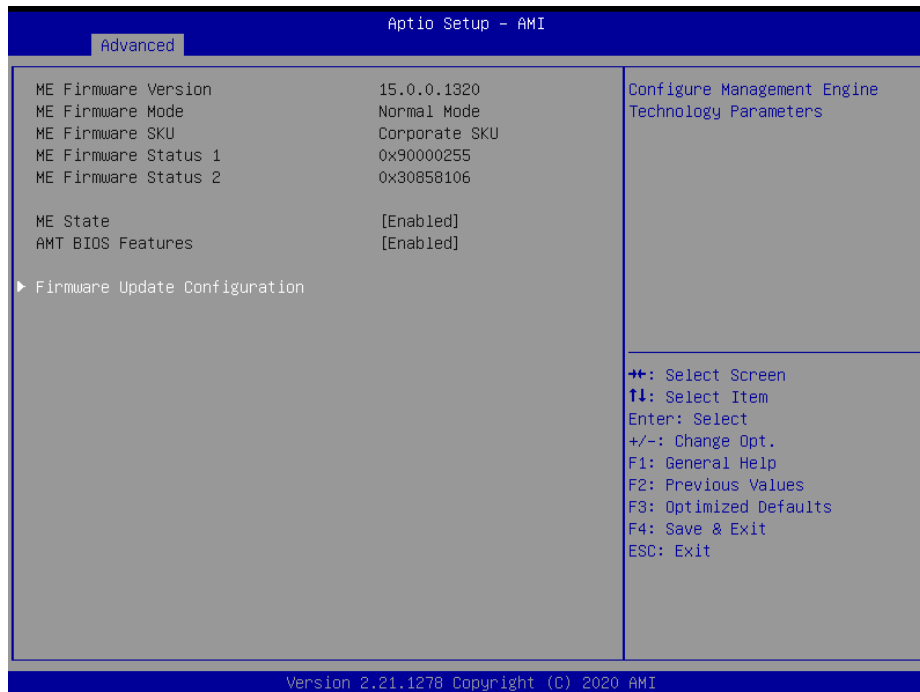
3.6.2.2.1 CPU-Power Management Control



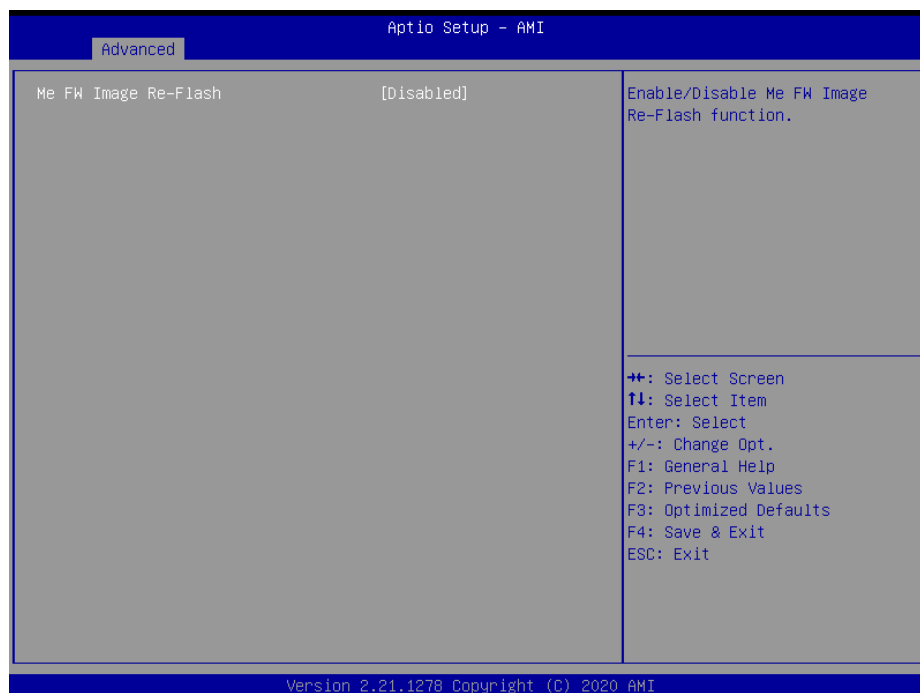
Item	Option	Description
Intel® SpeedSteer™	Enabled[Default], Disabled	Allows more than two frequency ranges to be supported.
Intel® Speed Shift Technology	Enabled[Default], Disabled	Enable/Disable Intel® Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states.

Turbo Mode	Enabled[Default], Disabled	Enable/Disable processor Turbo Mode (requires EMTTM enabled too). AUTO means enabled.
C states	Enabled[Default], Disabled	Enable/Disable CPU Power Management. Allows CPU to go to C states when it's not 100% utilized.

3.6.2.3 PCH-FW Configuration



3.6.2.3.1 Firmware Update Configuration



Item	Option	Description
Me FW Image Re-Flash	Enabled Disabled[Default],	Enable/Disable Me FW Image Re-Flash function.

3.6.2.4 Trusted Computing



Item	Options	Description
Security Device Support	Disable Enable[Default]	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

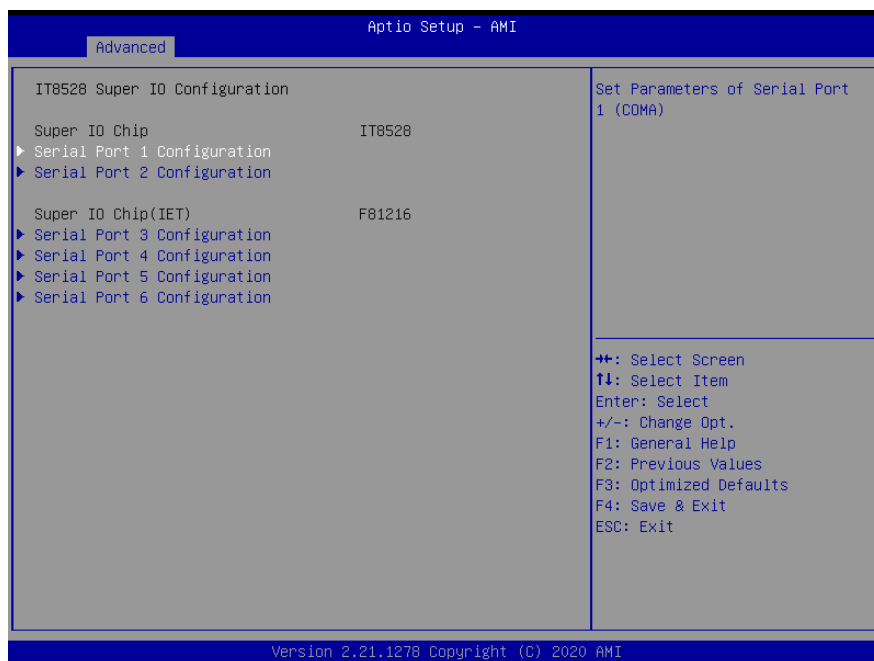
3.6.2.5 APCI Settings



Item	Options	Description
Enable Hibernation	Disabled Enabled[Default],	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	Suspend Disabled, S3 (Suspend to RAM) [Default]	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.

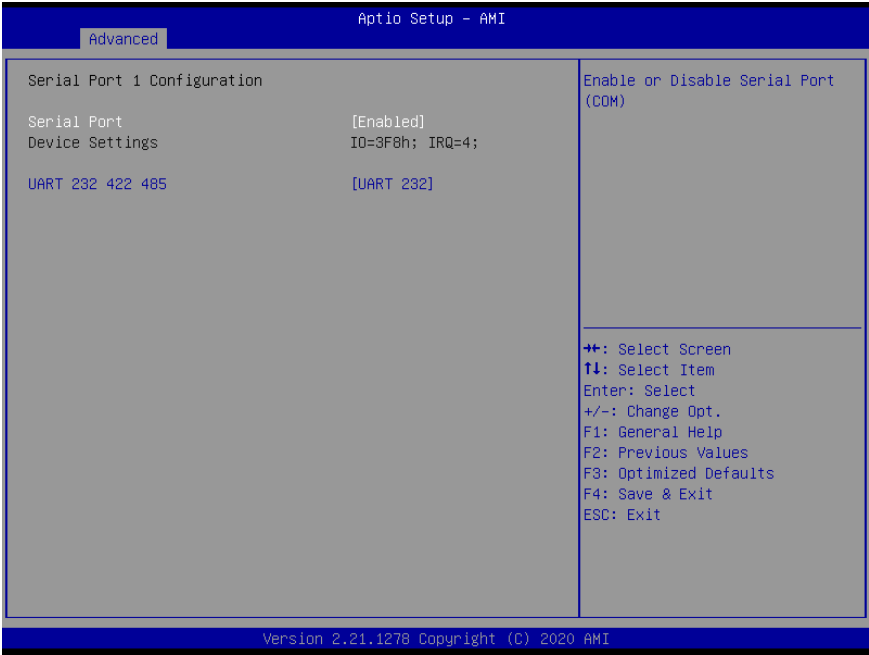
3.6.2.6 IT8528 Super IO Configuration

You can use this item to set up or change the IT8528 Super IO configuration for serial ports. Please refer to 3.6.2.6.1~ 3.6.2.6.6 for more information.



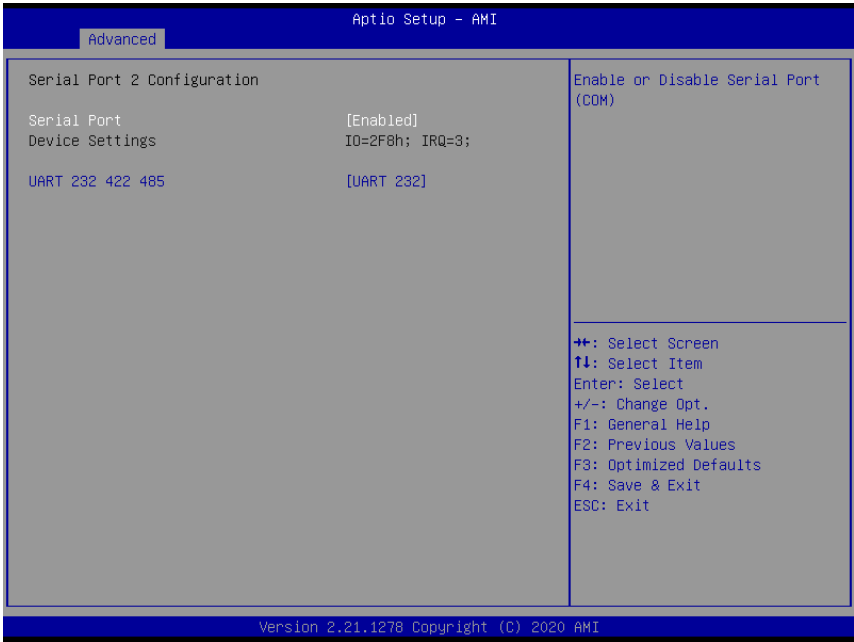
Item	Description
Serial Port 1 Configuration	Set Parameters of Serial Port 1 (COMA).
Serial Port 2 Configuration	Set Parameters of Serial Port 2 (COMB).
Serial Port 3 Configuration	Set Parameters of Serial Port 3 (COMC).
Serial Port 4 Configuration	Set Parameters of Serial Port 4 (COMD).
Serial Port 5 Configuration	Set Parameters of Serial Port 5 (COME).
Serial Port 6 Configuration	Set Parameters of Serial Port 5 (COMF).

3.6.2.6.1 Serial Port 1 Configuration



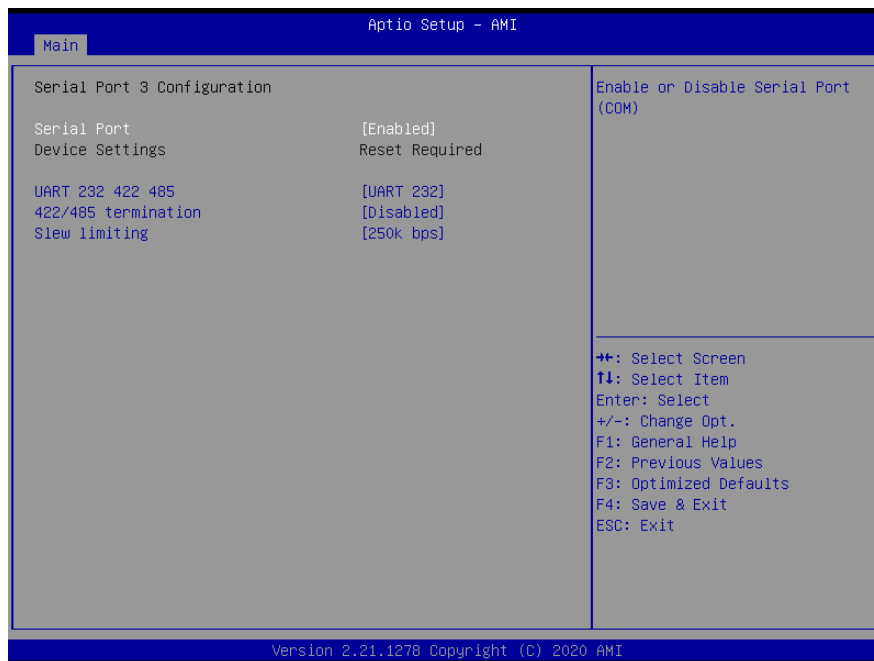
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
UART 232 422 485	UART 232[Default] UART 422 UART 485	Change the Serial Port as RS232/422/485.

3.6.2.6.2 Serial Port 2 Configuration



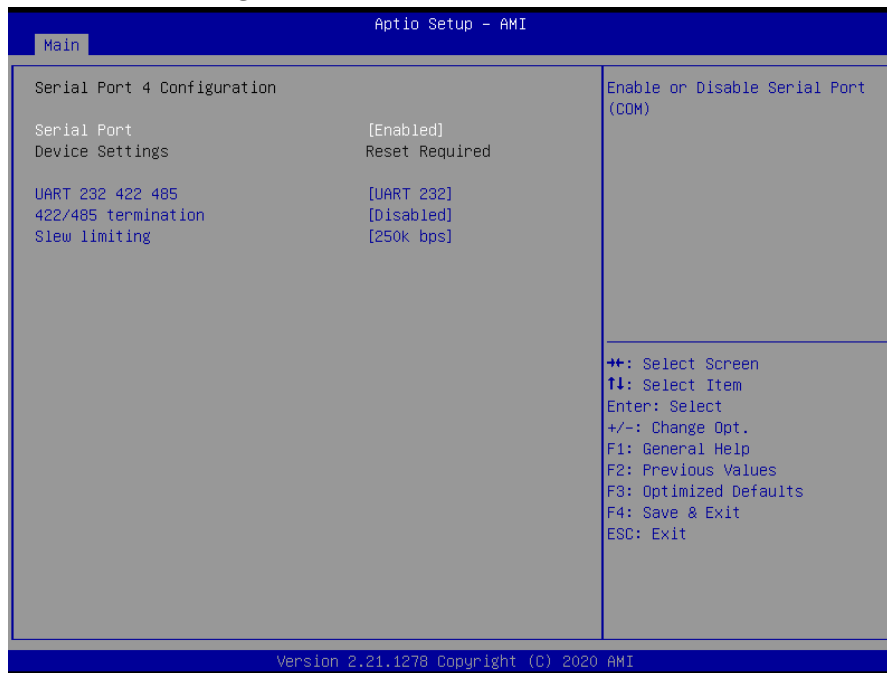
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
UART 232 422 485	UART 232[Default] UART 422 UART 485	Change the Serial Port as RS232/422/485.

3.6.2.6.3 Serial Port 3 Configuration



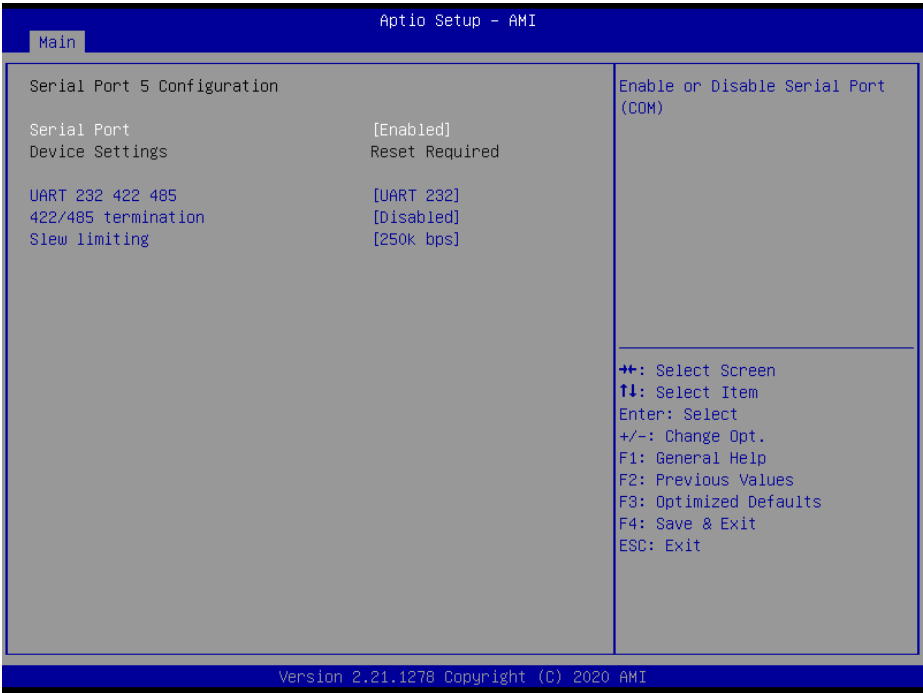
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
UART 232 422 485	UART 232(LOOPBACK) UART 232[Default] UART 422 UART 485	Change the Serial Port as RS232/422/485.
422/485 termination	Enabled, Disabled[Default]	TERM from GPIO.
Slew limiting	10M bps 250k bps[Default]	SLEW from GPIO.

3.6.2.6.4 Serial Port 4 Configuration



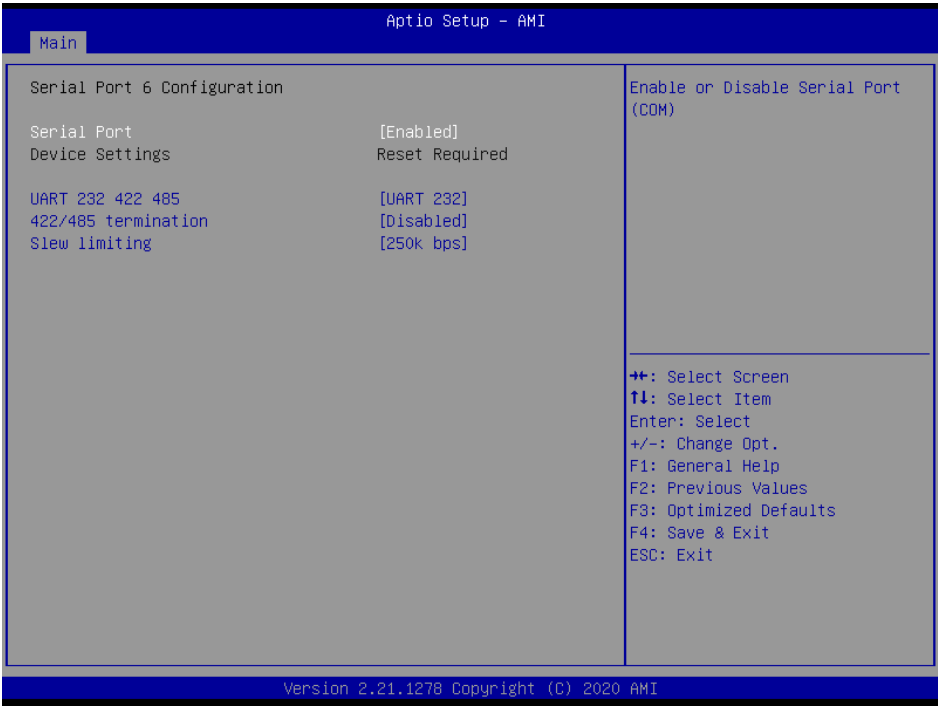
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
UART 232 422 485	UART 232(LOOPBACK) UART 232[Default] UART 422 UART 485	Change the Serial Port as RS232/422/485.
422/485 termination	Enabled, Disabled[Default]	TERM from GPIO.
Slew limiting	10M bps 250k bps[Default]	SLEW from GPIO.

3.6.2.6.5 Serial Port 5 Configuration



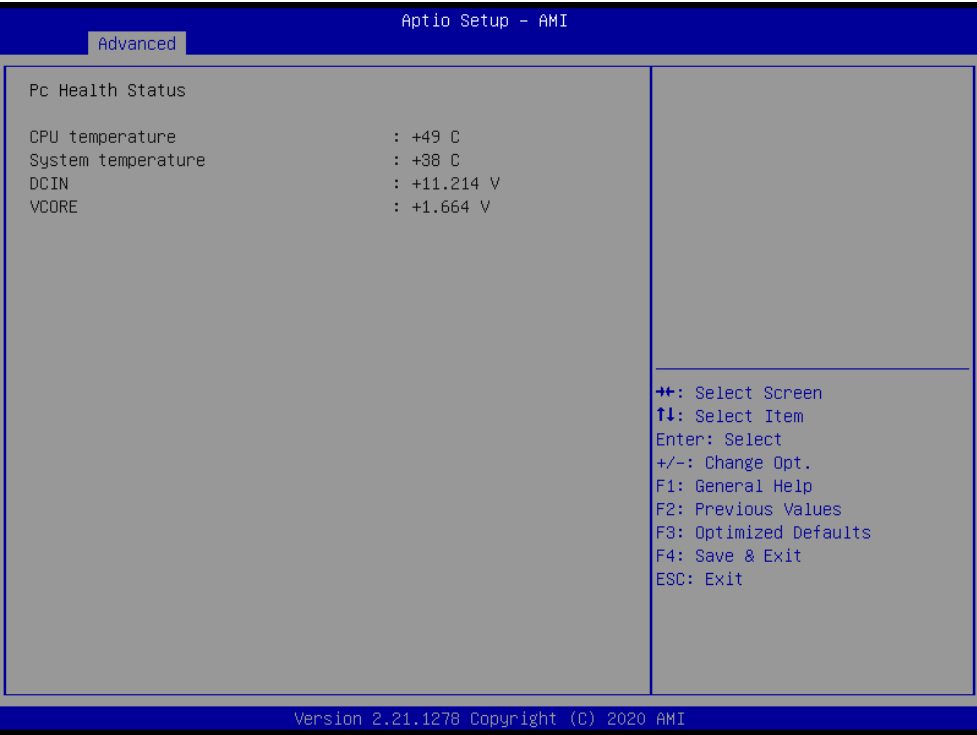
Item	Option	Description
Serial Port	Enabled [Default] , Disabled	Enable or Disable Serial Port (COM).
UART 232 422 485	UART 232(LOOPBACK) UART 232 [Default] UART 422 UART 485	Change the Serial Port as RS232/422/485.
422/485 termination	Enabled, Disabled [Default]	TERM from GPIO.
Slew limiting	10M bps 250k bps [Default]	SLEW from GPIO.

3.6.2.6.6 Serial Port 6 Configuration



Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
UART 232 422 485	UART 232(LOOPBACK) UART 232[Default] UART 422 UART 485	Change the Serial Port as RS232/422/485.
422/485 termination	Enabled, Disabled[Default]	TERM from GPIO.
Slew limiting	10M bps 250k bps[Default]	SLEW from GPIO.

3.6.2.7 H/W Monitor

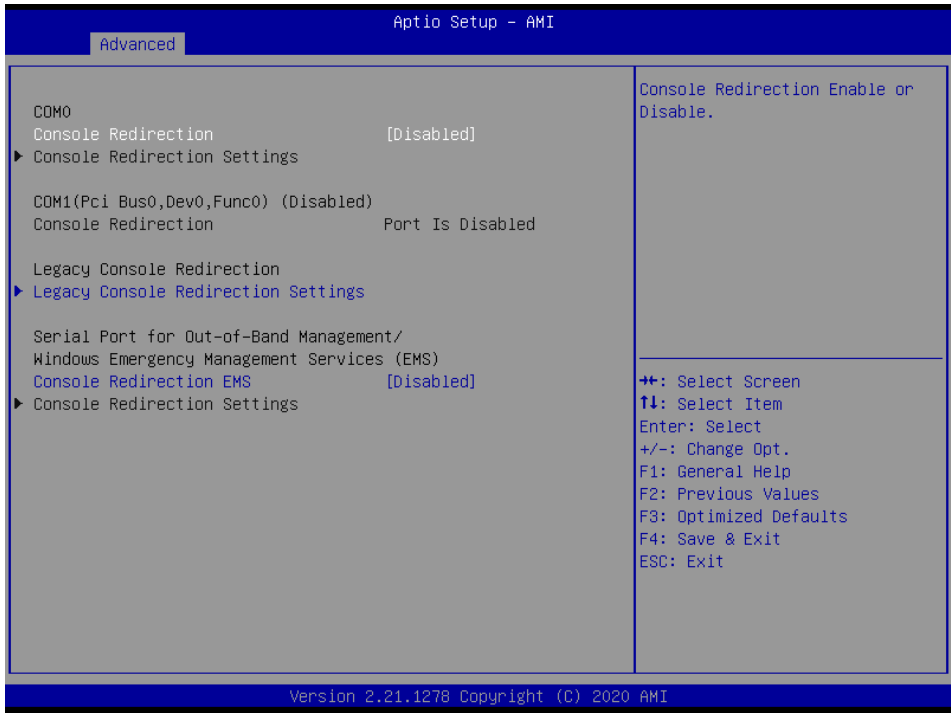


3.6.2.8 S5 RTC Wake Settings



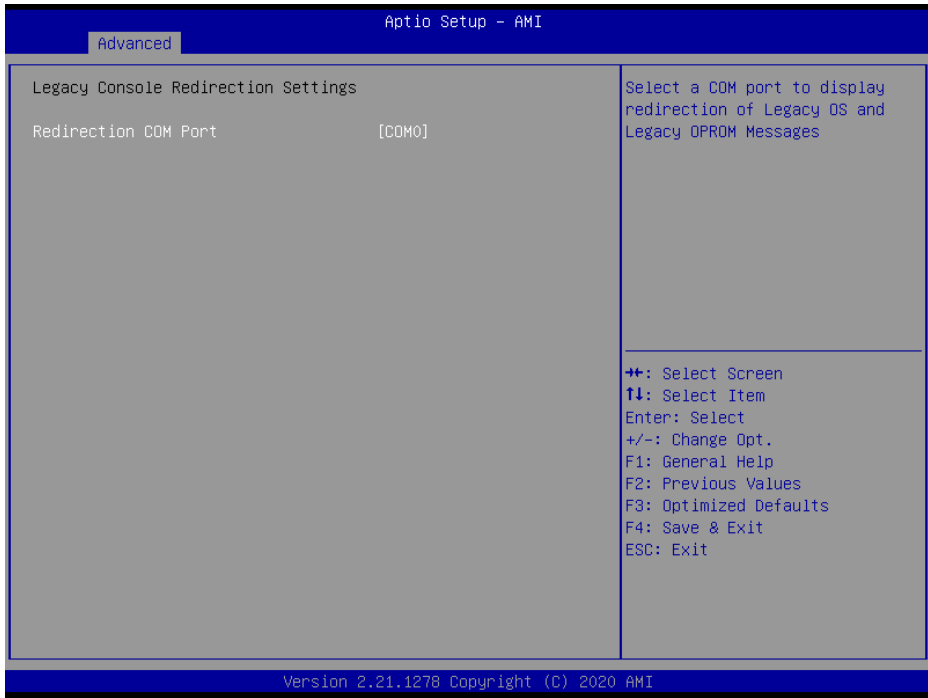
Item	Options	Description
Wake system from S5	Disabled[Default], Fixed Time Dynamic Time	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Time, System will wake on the current time + Increase minute(s).

3.6.2.9 Serial Port Console Redirection



Item	Options	Description
Console Redirection	Disabled[Default], Enabled	Console Redirection Enable or Disable.
Console Redirection EMS	Disabled[Default], Enabled	Console Redirection Enable or Disable.

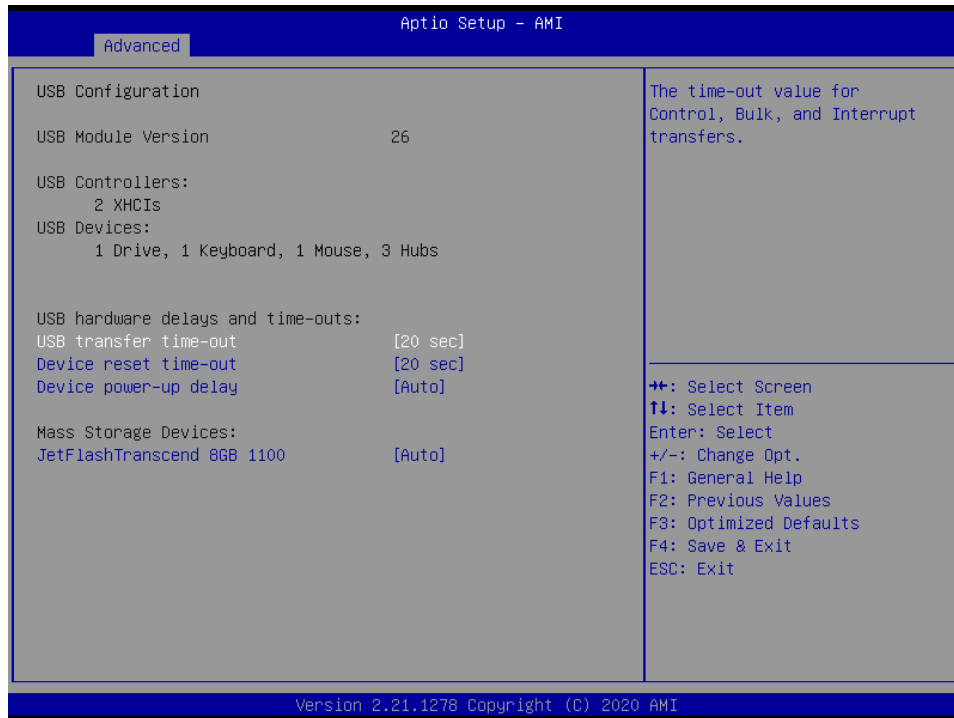
3.6.2.9.1 Legacy Console Redirection Settings



Item	Option	Description
Redirection COM Port	COM0[Default],	Select a COM port to display redirection of Legacy OS and Legacy OPRM Messages.

3.6.2.10 USB Configuration

The USB Configuration menu helps read USB information and configures USB settings.



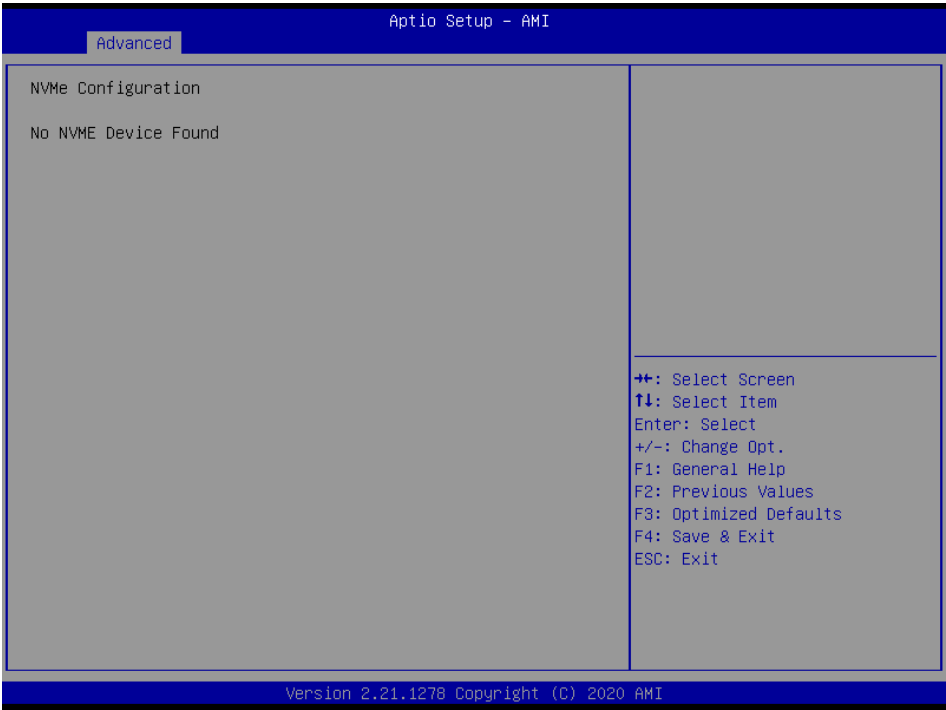
Item	Options	Description
USB transfer time-out	1 sec 5 sec 10 sec 20 sec[Default]	The time-out value for Control, Bulk, and Interrupt transfers.
Device reset time-out	10 sec 20 sec[Default] 30 sec 40 sec	USB mass storage device Start Unit command time-out.
Device power-up delay	Auto[Default] Manual	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100ms, for a Hub port the delay is taken from Hub descriptor.
JetFlashTranscend 8GB 1100	Auto[Default] Floppy Forced FDD Hard Disk CD-ROM	Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type.

3.6.2.11 Network Stack Configuration



Item	Options	Description
Network Stack	Enabled Disabled [Default]	Enable/Disable UEFI Network Stack.

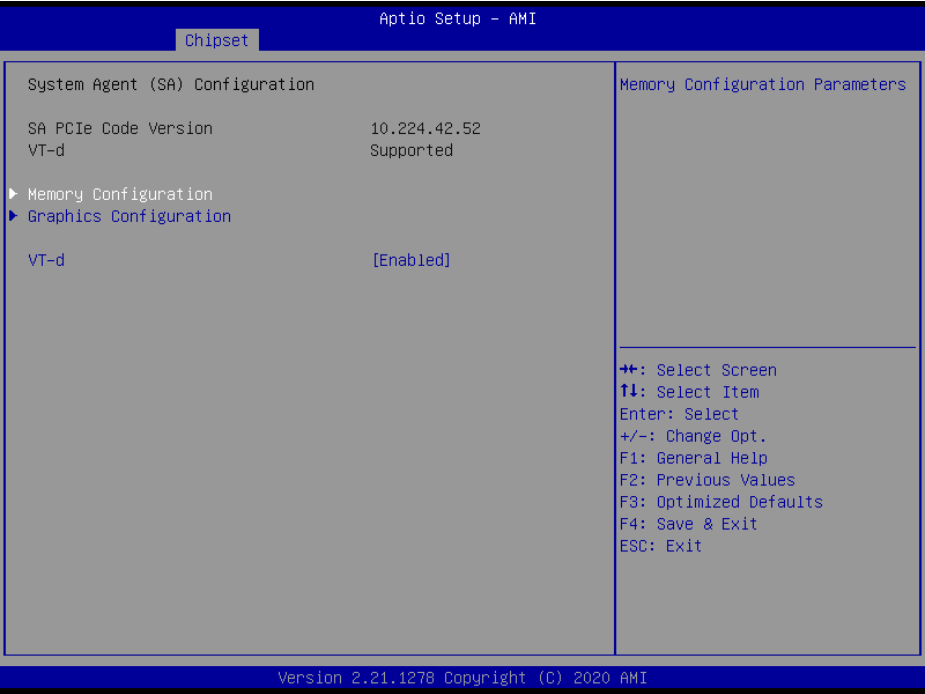
3.6.2.12 NVMe Configuration



3.6.3 Chipset

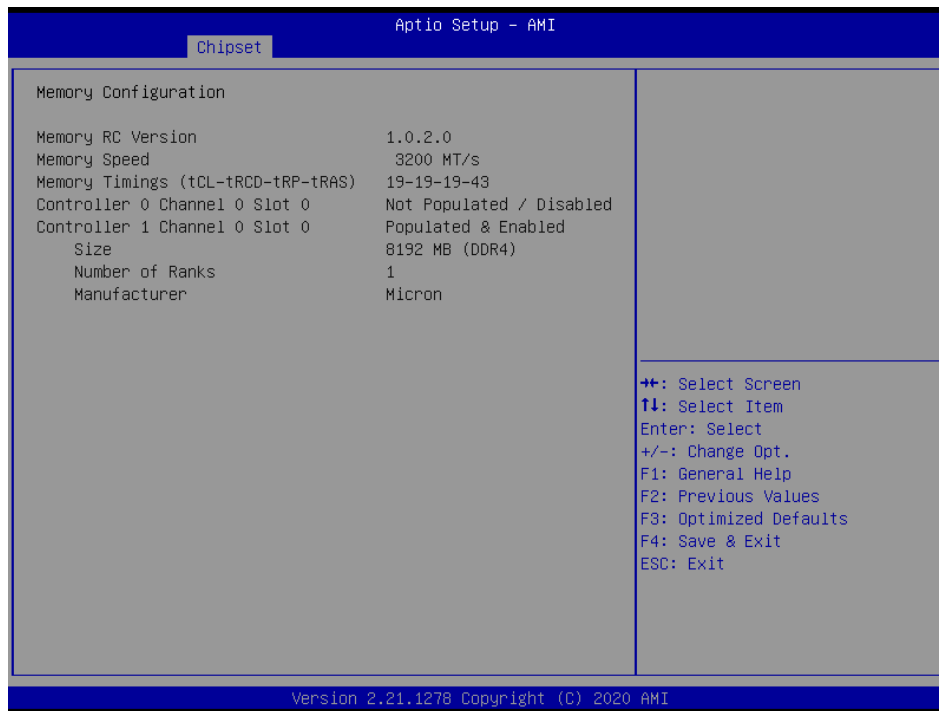


3.6.3.1 System Agent (SA) Configuration



Item	Option	Description
VT-d	Enabled[Default] Disabled	VT-d capability.

3.6.3.1.1 Memory Configuration



3.6.3.1.2 Graphics Configuration



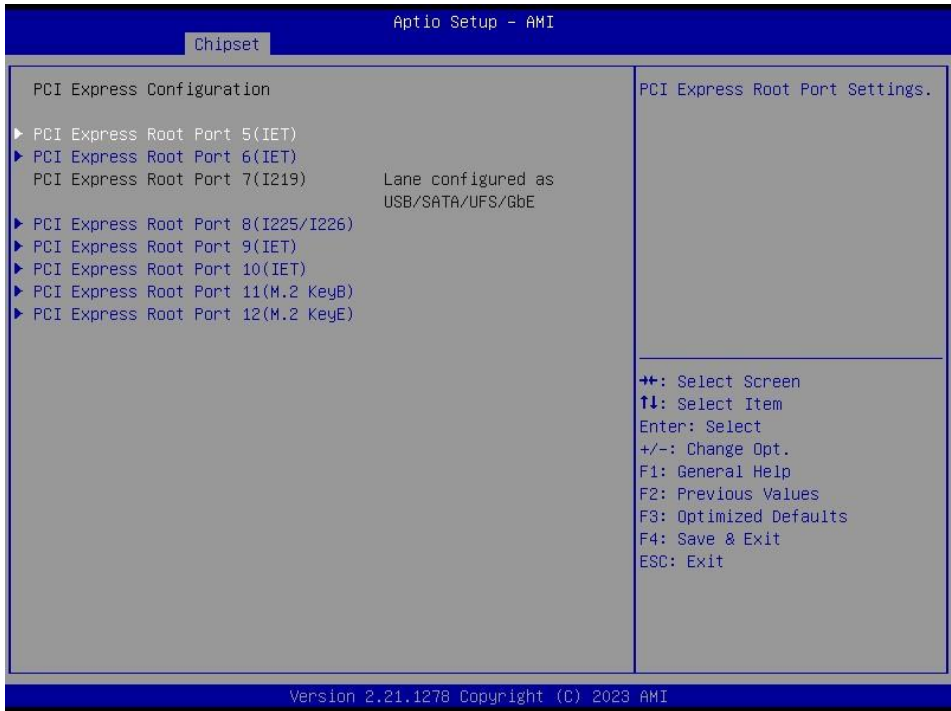
Item	Option	Description
Primary Display	Auto[Default] IGFX	Select which of IGFX/PEG/PCI Graphics device should be Primary Display Or select HG for Hybrid Gfx.

3.6.3.2 PCH-IO Configuration

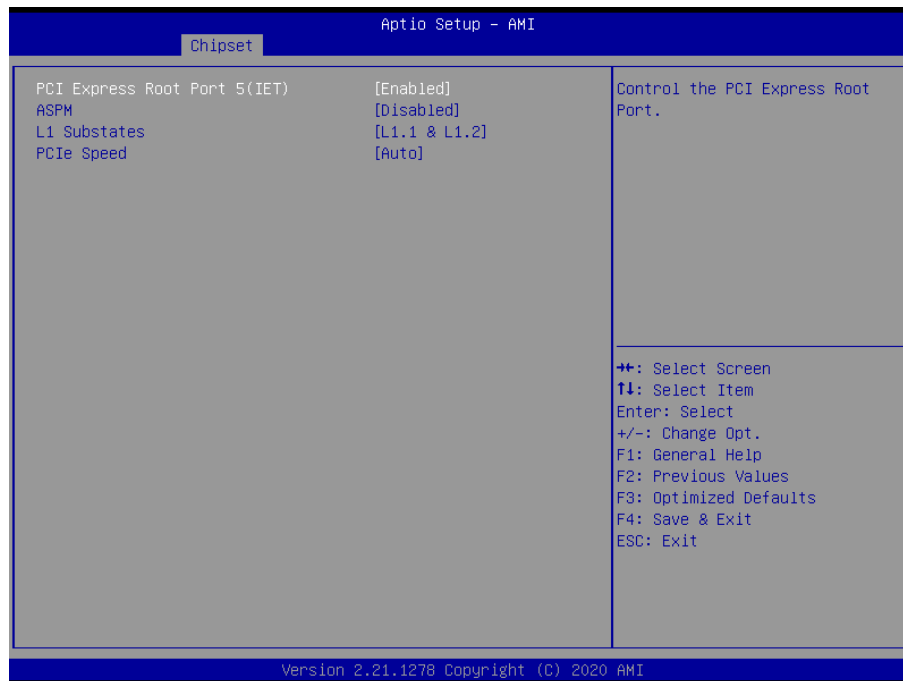


Item	Option	Description
PCH LAN Controller	Disabled Enabled [Default]	Enable or disable onboard NIC.

3.6.3.2.1 PCI Express Configuration



3.6.3.2.1.1 PCI Express Root Port 5 (IET)



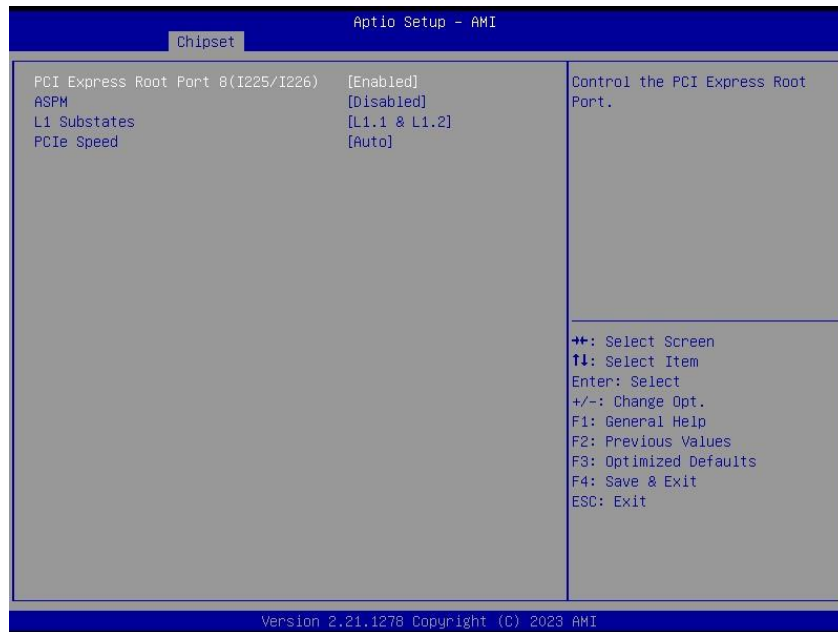
Item	Option	Description
PCI Express Root Port 5(IET)	Enabled[Default], Disabled	Control the PCI Express Root Port.
ASPM	Disabled[Default] L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled L1.1 L1.1 & L1.2[Default],	PCI Express L1 Substates settings.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.2 PCI Express Root Port 6 (IET)



Item	Option	Description
PCI Express Root Port 6(IET)	Enabled[Default], Disabled	Control the PCI Express Root Port.
ASPM	Disabled[Default] L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled L1.1 L1.1 & L1.2[Default],	PCI Express L1 Substates settings.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.3 PCI Express Root Port 8 (I225/I226)



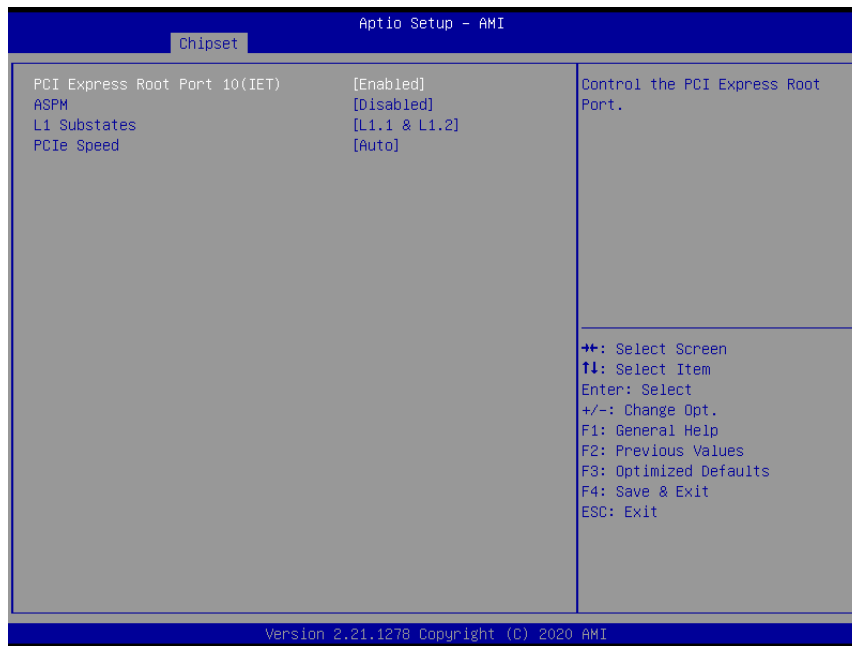
Item	Option	Description
PCI Express Root Port 8(I225)	Enabled[Default], Disabled	Control the PCI Express Root Port.
ASPM	Disabled[Default] L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled L1.1 L1.1 & L1.2[Default],	PCI Express L1 Substates settings.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.4 PCI Express Root Port 9 (IET)



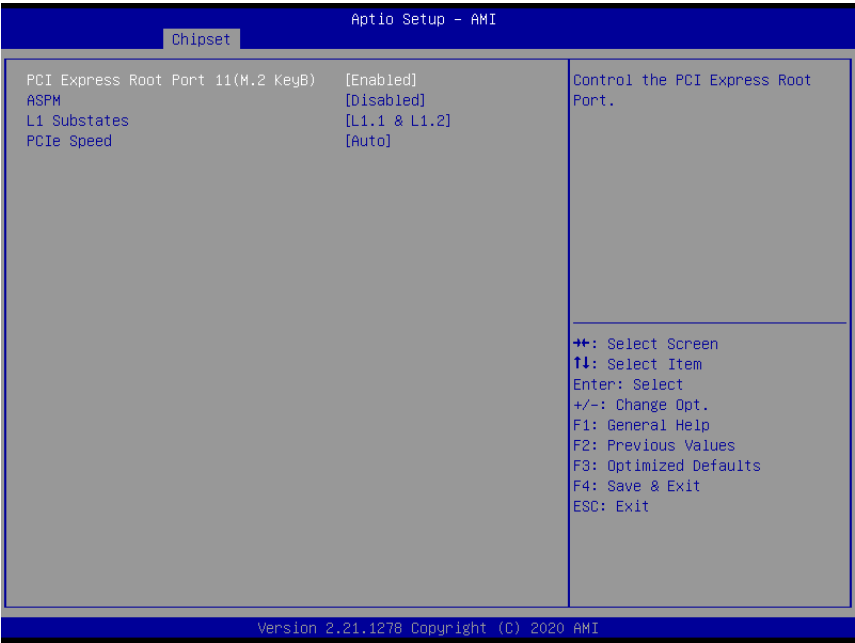
Item	Option	Description
PCI Express Root Port 9(IET)	Enabled[Default], Disabled	Control the PCI Express Root Port.
ASPM	Disabled[Default] L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled L1.1 L1.1 & L1.2[Default],	PCI Express L1 Substates settings.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.5 PCI Express Root Port 10 (IET)



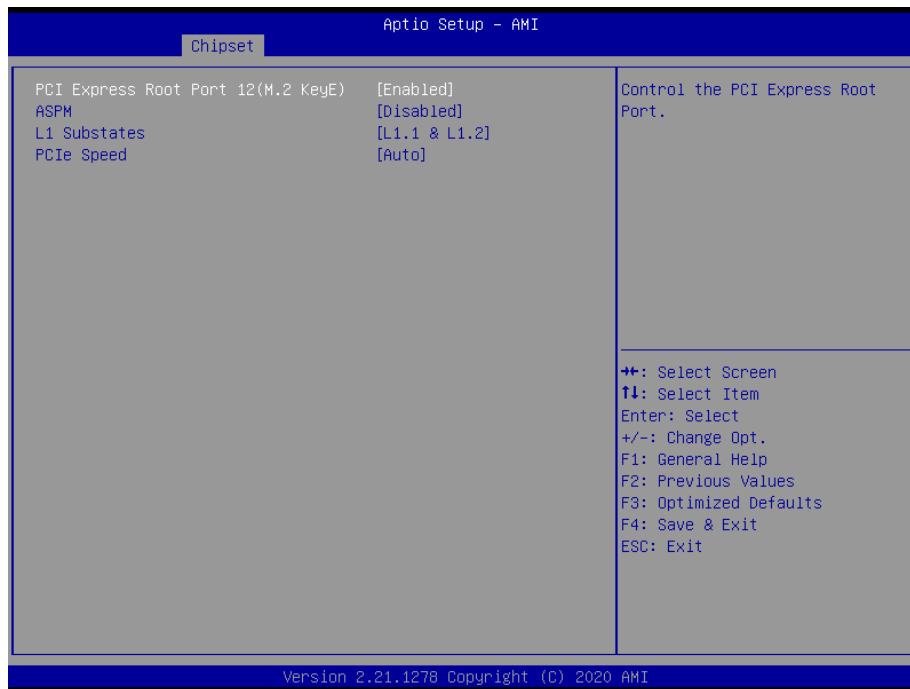
Item	Option	Description
PCI Express Root Port 10(IET)	Enabled[Default], Disabled	Control the PCI Express Root Port.
ASPM	Disabled[Default] L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled L1.1 L1.1 & L1.2[Default],	PCI Express L1 Substates settings.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.6 PCI Express Root Port 11 (M.2 KeyB)



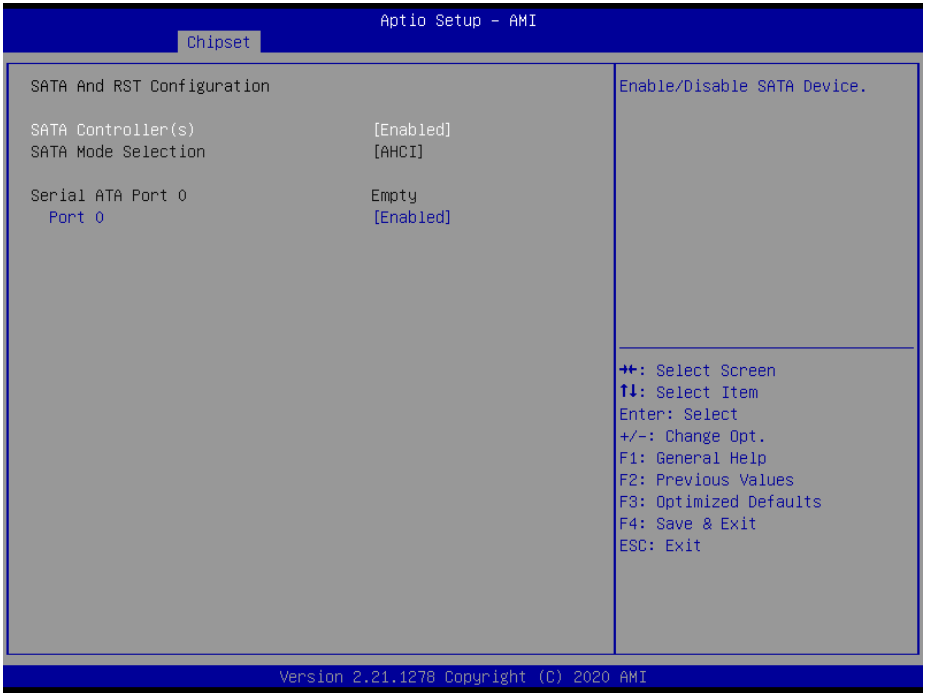
Item	Option	Description
PCI Express Root Port 11 (M.2 KeyB)	Enabled[Default], Disabled	Control the PCI Express Root Port.
ASPM	Disabled[Default] L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled L1.1 L1.1 & L1.2[Default],	PCI Express L1 Substates settings.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.1.7 PCI Express Root Port 12 (M.2 KeyE)



Item	Option	Description
PCI Express Root Port 12 (M.2 KeyE)	Enabled[Default], Disabled	Control the PCI Express Root Port.
ASPM	Disabled[Default] L0s L1 L0sL1 Auto	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
L1 Substates	Disabled L1.1 L1.1 & L1.2[Default],	PCI Express L1 Substates settings.
PCIe Speed	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe speed.

3.6.3.2.2 SATA And RST Configuration



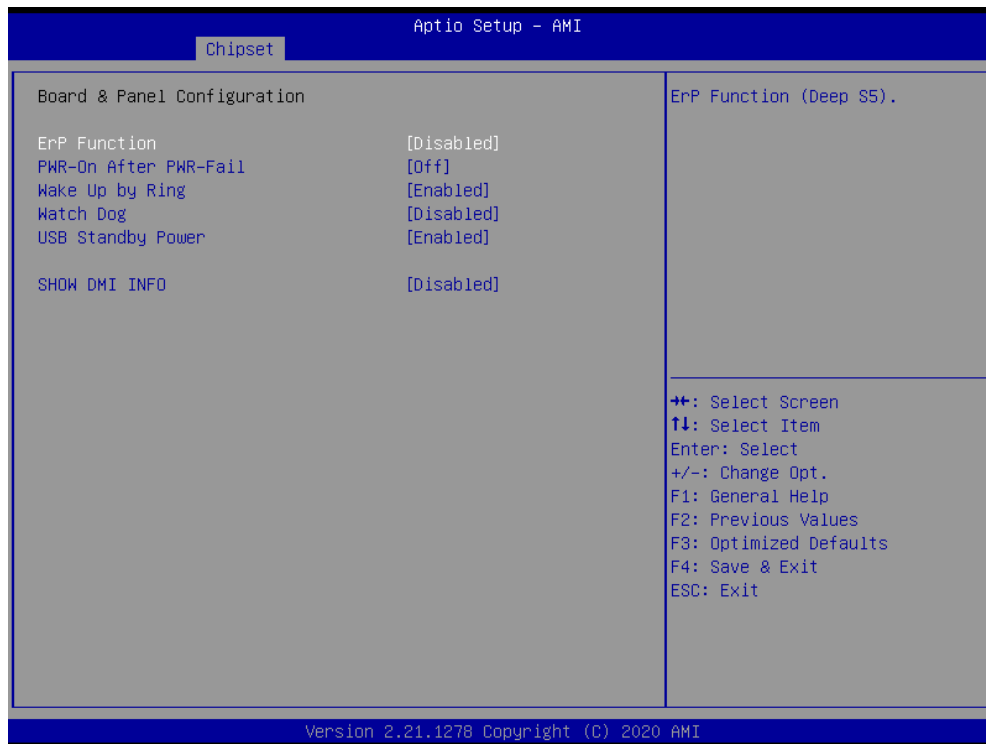
Item	Option	Description
SATA Controller(s)	Enabled[Default] Disabled,	Enable/Disable SATA Device.
Port 0	Enabled[Default] Disabled,	Enable or Disable SATA Port.

3.6.3.2.3 HD Audio Configuration



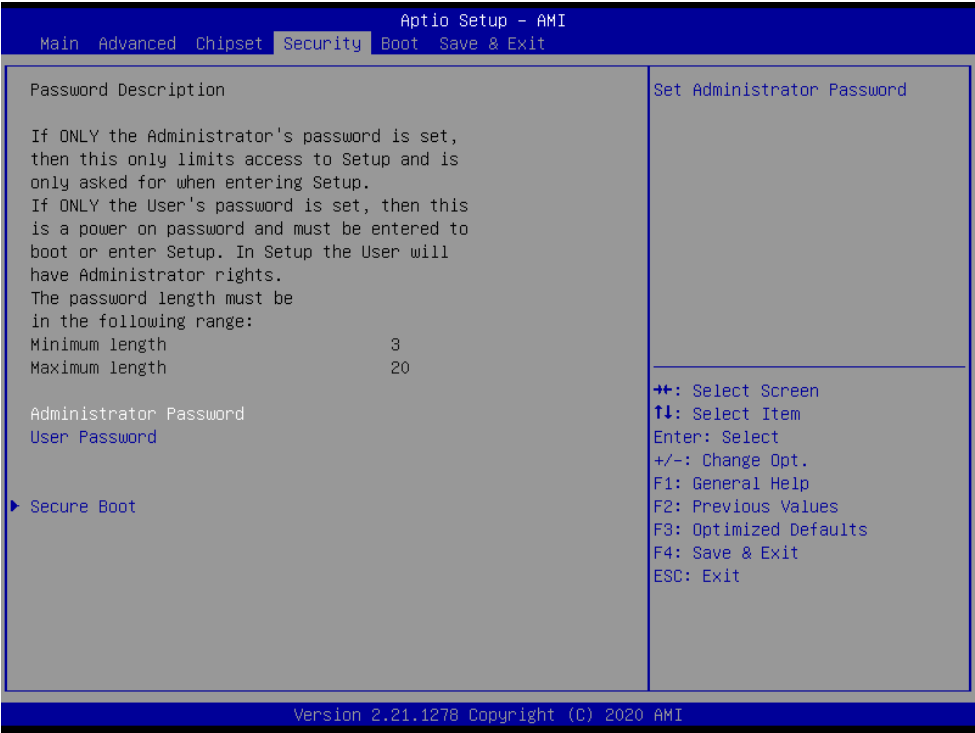
Item	Option	Description
HD Audio	Disabled Enabled[Default],	Control Detection of the HD-Audio device. Disable = HDA will be unconditionally disabled Enabled = HDA will be unconditionally enabled.

3.6.3.2 Board & Panel Configuration



Item	Option	Description
ErP Function	Disabled[Default] Enabled	ErP Function (Deep S5).
PWR-On After PWR-Fail	Off[Default] On Last state	AC loss resume.
Wake Up by Ring	Disabled Enabled[Default]	Wake Up by Ring from S3/S4/S5.
Watch Dog	Disabled[Default] 30 sec 40 sec 50 sec 1 min 2 min 10 min 30 min	Select WatchDog.
USB Standby Power	Disabled Enabled[Default]	Enable/Disabled USB Standby Power during S3/S4/S5.
SHOW DMI INFO	Disabled[Default] Enabled	SHOW DMI INFO.

3.6.4 Security



● Administrator Password

Set setup Administrator Password

● User Password

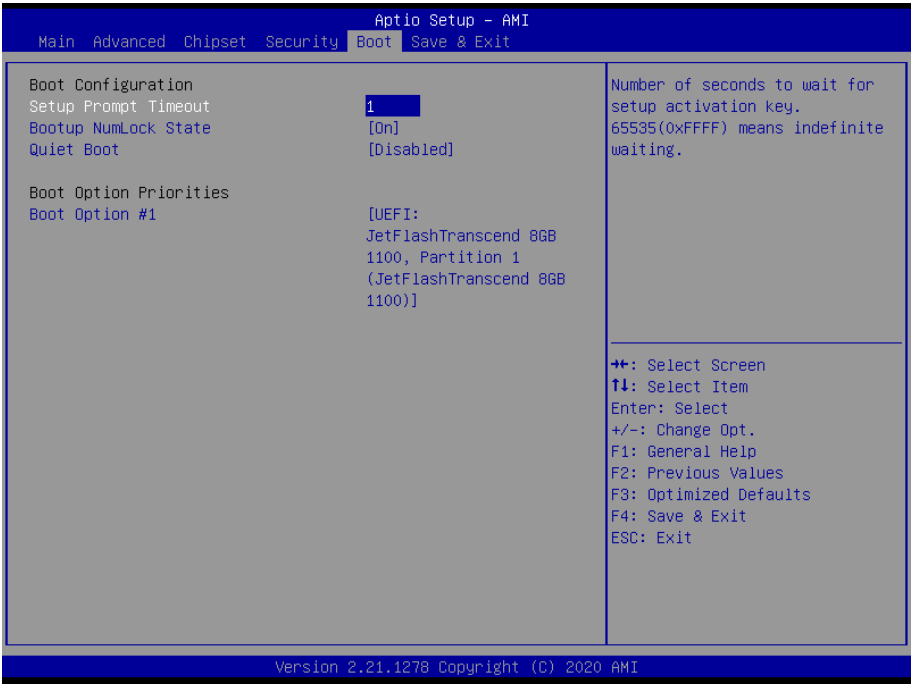
Set User Password

3.6.4.1 Secure Boot menu



Item	Option	Description
Secure Boot	Disabled Enabled[Default]	Secure Boot can be enabled if 1.System running in User mode with enrolled Platform Key(PK) 2.CSM function is disabled.
Secure Boot Mode	Standard[Default] Custom	Customizable Secure Boot mode: In Custom mode Secure Boot Policy variables can be configured by a physically present user without full authentication.

3.6.5 Boot



Item	Option	Description
Setup Prompt Timeout	1~ 65535	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Bootup NumLock State	On[Default] Off	Select the Keyboard NumLock state
Quiet Boot	Disabled[Default] Enabled	Enables or disables Quiet Boot option
Boot Option #1	Set the system boot order.	

3.6.6 Save and exit



3.6.6.1 Save Changes and Reset

Reset the system after saving the changes.

3.6.6.2 Discard Changes and Reset

Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and reboots the controller.

3.6.6.3 *Restore Defaults*

This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting.

3.6.6.4 *Launch EFI Shell from filesystem device*

Attempts to Launch EFI Shell application (Shellx64.efi) from one of the available filesystem devices.

4. Drivers Installation



Note: Installation procedures and screen shots in this section are for your reference and may not be exactly the same as shown on your screen.

4.1 Install Chipset Driver

All drivers can be found on the Avalue Official Website:

<http://www.avalue.com.tw>.



Note: The installation procedures and screen shots in this section are based on Windows 10 operation system. If the warning message appears while the installation process, click Continue to go on.



Step 3. Click Install.



Step1. Click Next.



Step 4. Click Finish to complete setup.



Step 2. Click Accept.

4.2 Install ME Driver

All drivers can be found on the Avalue Official Website:

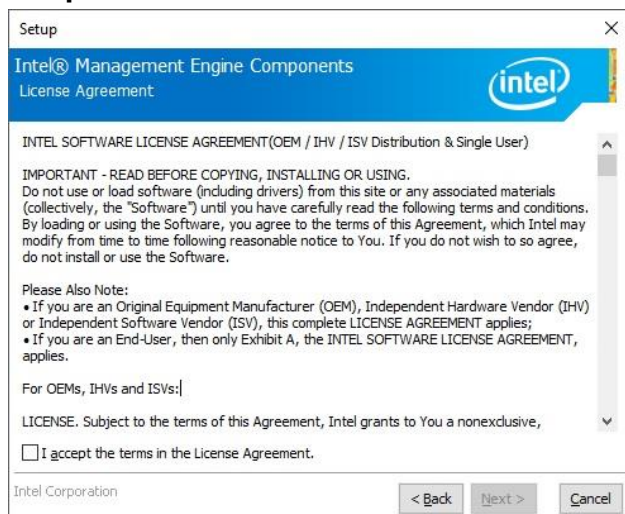
<http://www.avalue.com.tw>.



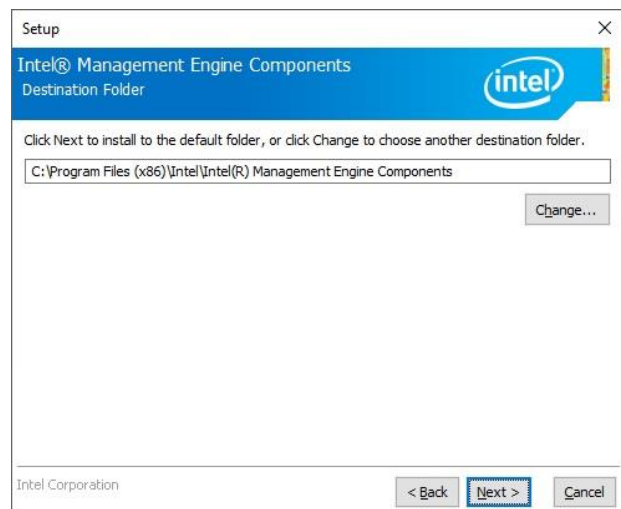
Note: The installation procedures and screen shots in this section are based on Windows 10 operation system. If the warning message appears while the installation process, click Continue to go on.



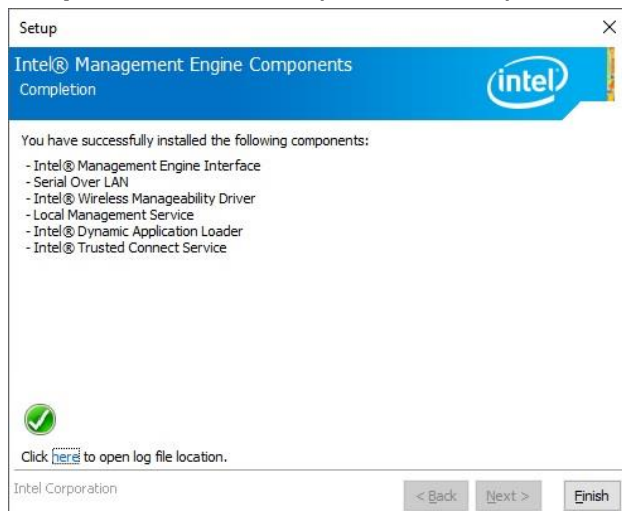
Step1. Click **Next** to start installation.



Step 2. Click **Next**.



Step 3. Click **Next** to proceed setup.



Step 4. Click **Finish** to complete setup.

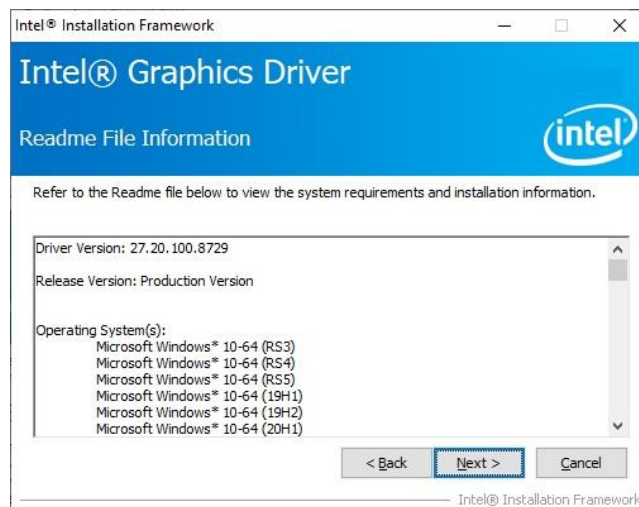
4.3 Install VGA Driver

All drivers can be found on the Avalue Official Website:

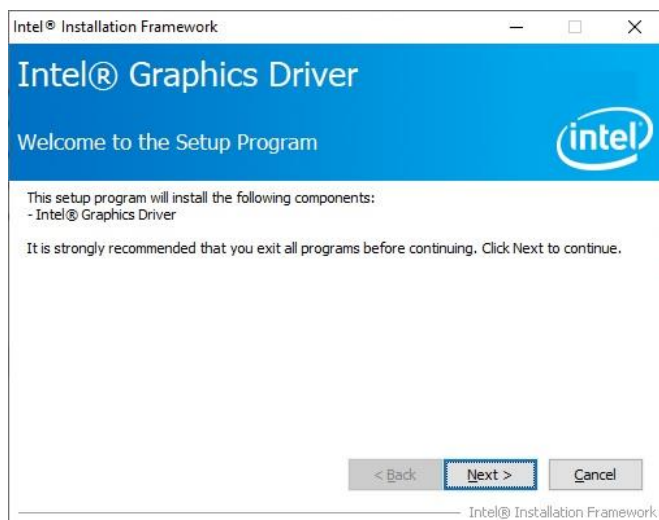
<http://www.avalue.com.tw>.



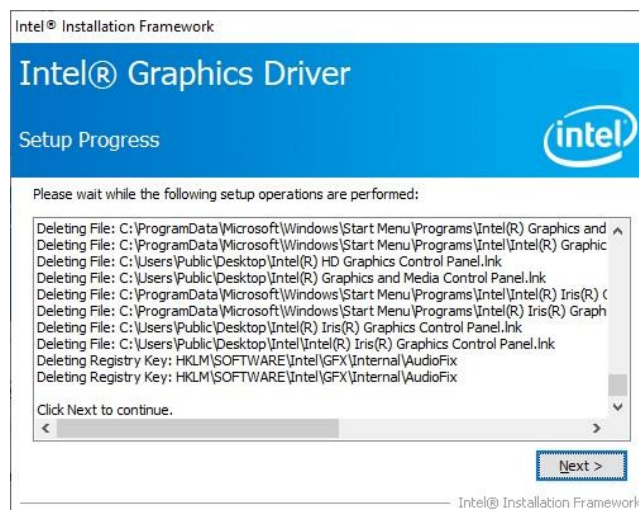
Note: The installation procedures and screen shots in this section are based on Windows 10 operation system.



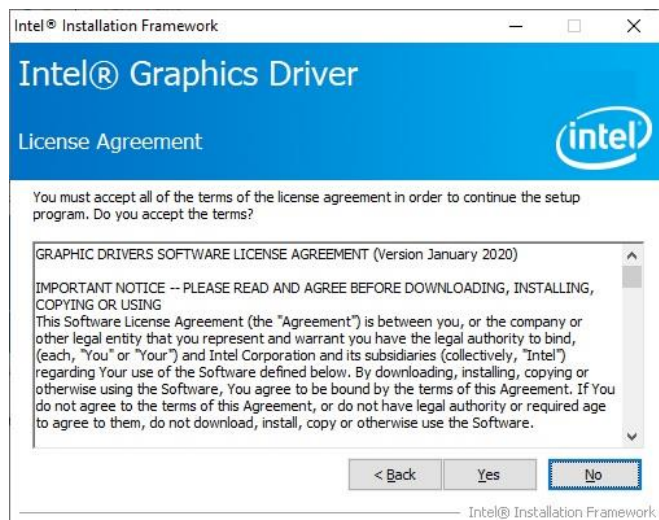
Step 3. Click Next.



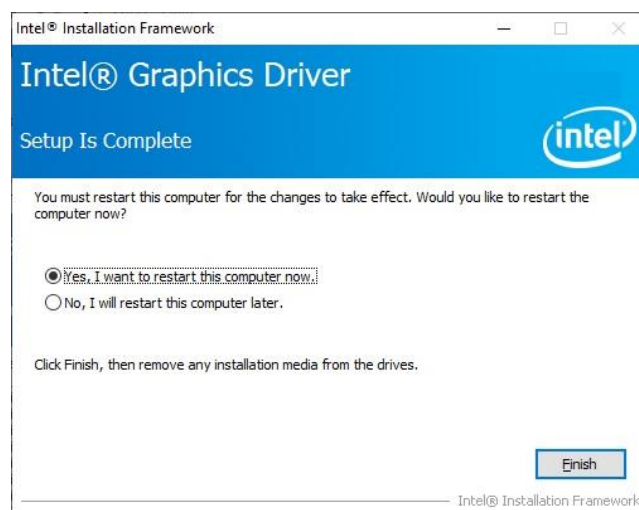
Step 1. Click Next to continue installation.



Step 4. Click Next.



Step 2. Click Yes.



Step 5. Click Finish to complete setup.

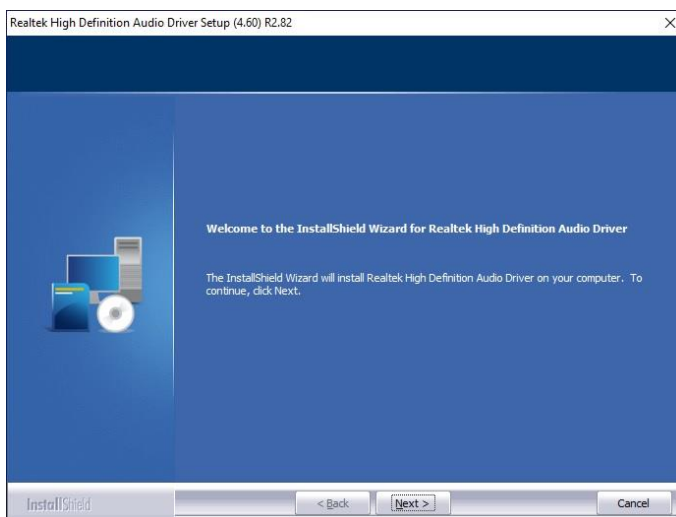
4.4 Install Audio Driver (For Realtek ALC888S)

All drivers can be found on the Avalue Official Website:

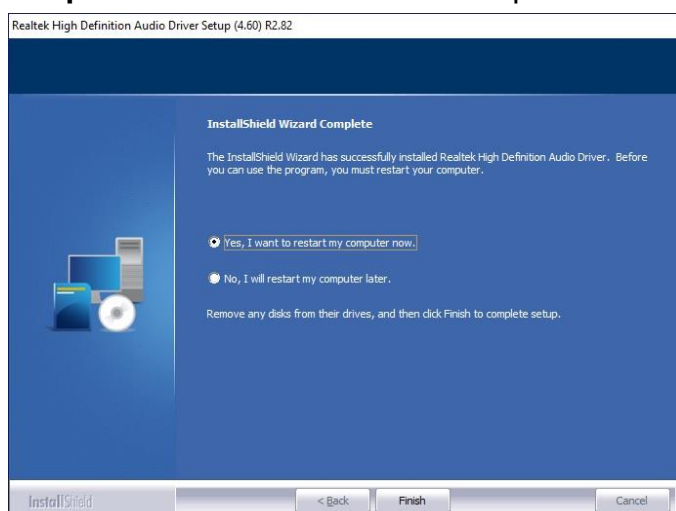
<http://www.avalue.com.tw>.



Note: The installation procedures and screen shots in this section are based on Windows 10 operation system.



Step 1. Click **Next** to continue setup.



Step 2. Click **Finish** to complete the setup.

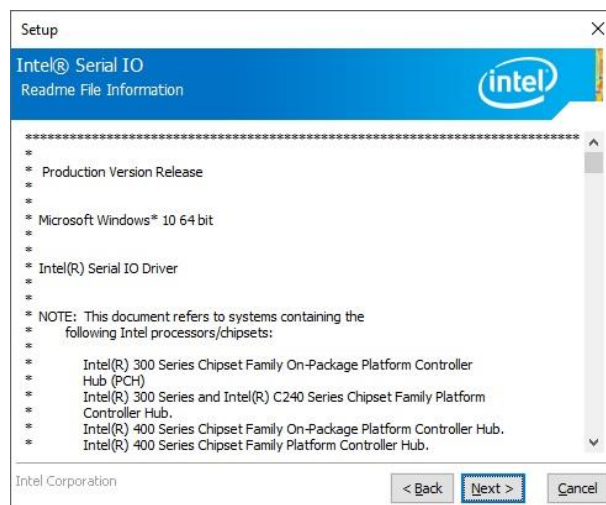
4.5 Install SerialIO Driver

All drivers can be found on the Avalue Official Website:

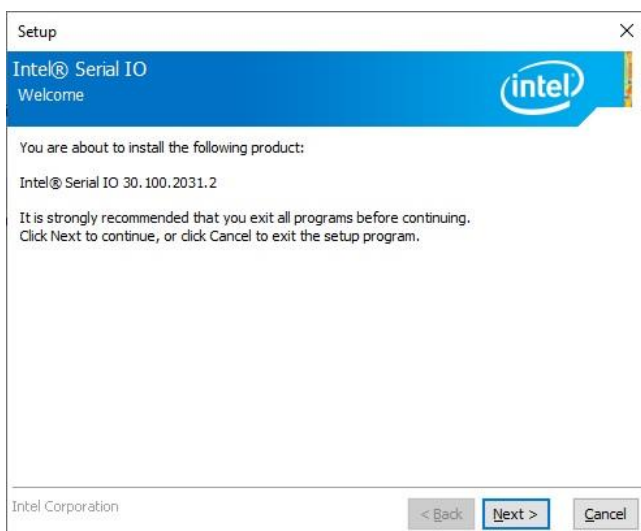
<http://www.avalue.com.tw>.



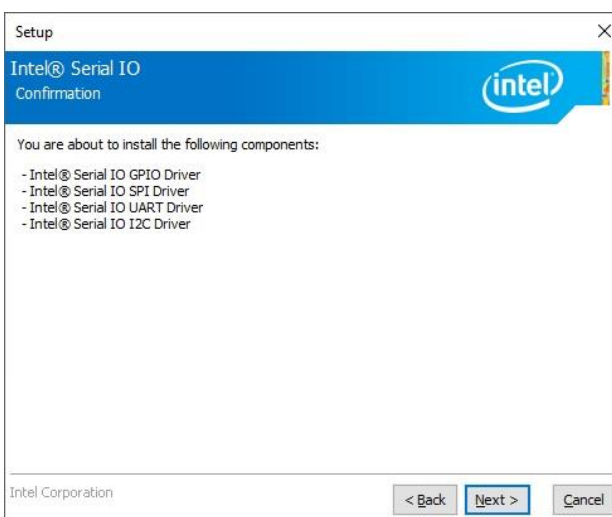
Note: The installation procedures and screen shots in this section are based on Windows 10 operation system.



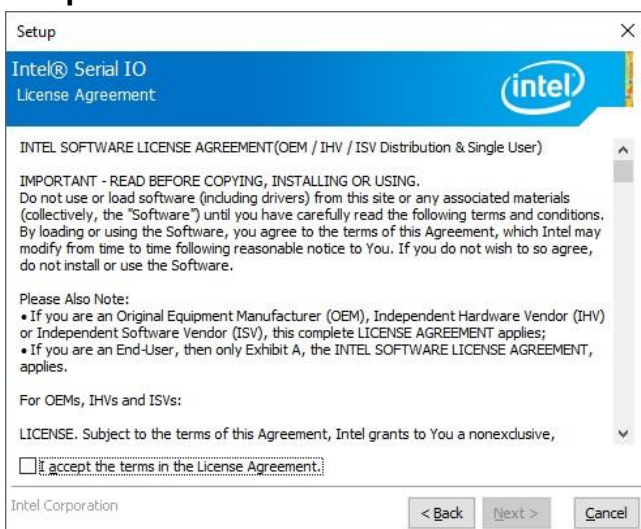
Step 3. Click Next.



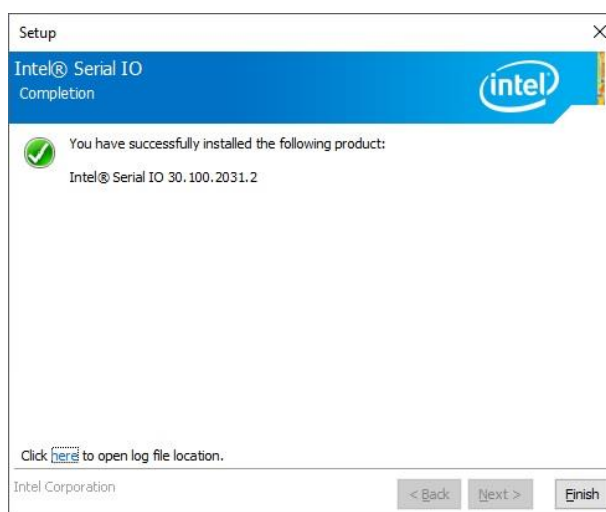
Step 1. Click Next to continue installation.



Step 4. Click Next.



Step 2. Click Next.



Step 5. Click Finish to complete setup.

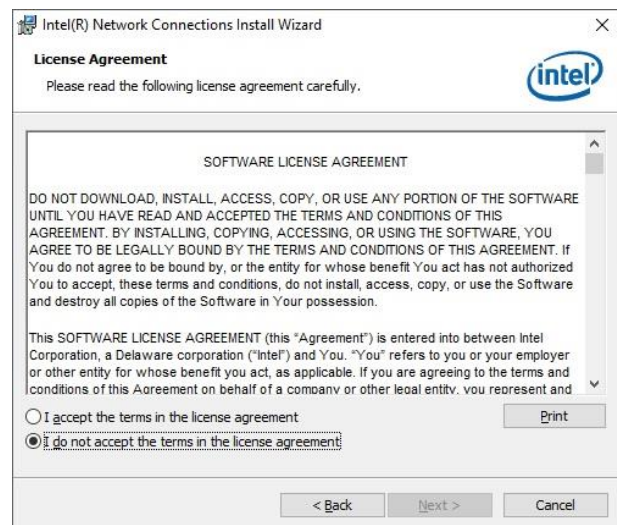
4.6 Install Ethernet Driver

All drivers can be found on the Avalue Official Website:

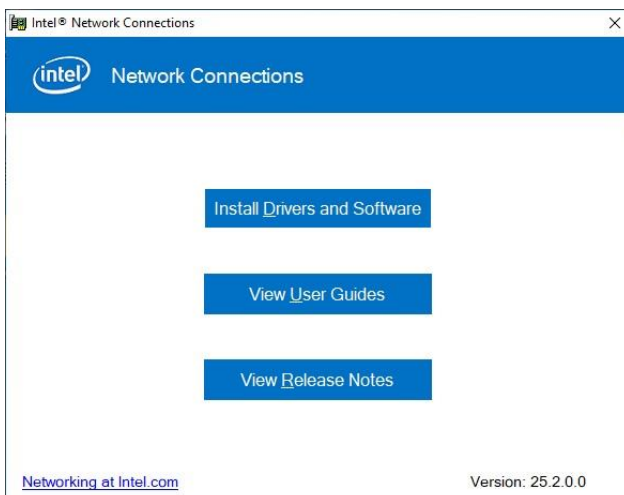
<http://www.avalue.com.tw>.



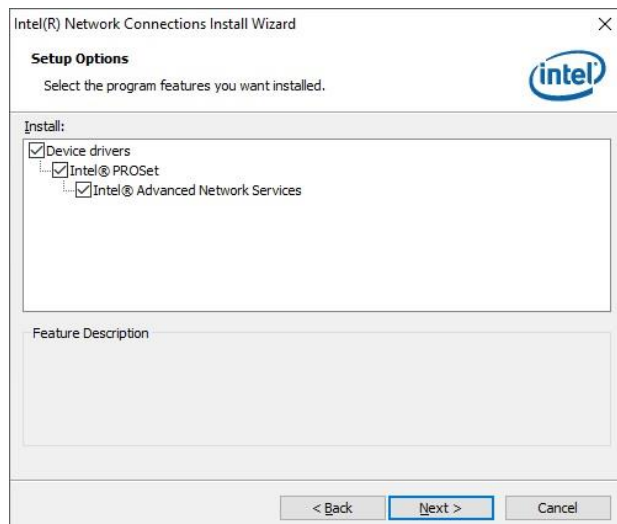
Note: The installation procedures and screen shots in this section are based on Windows 10 operation system.



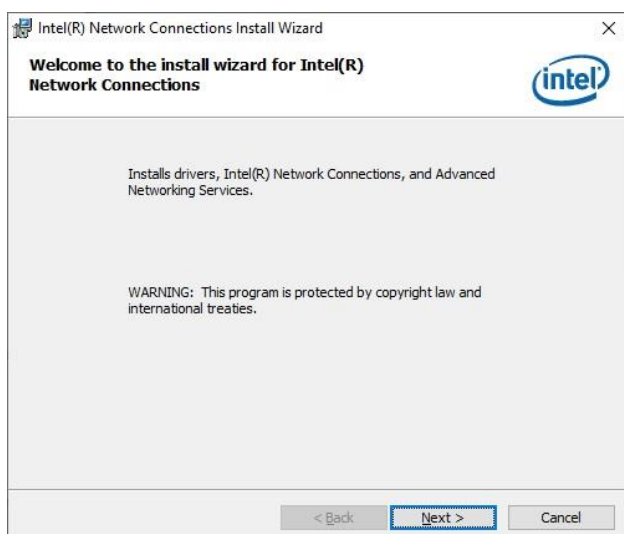
Step 3. Click Next.



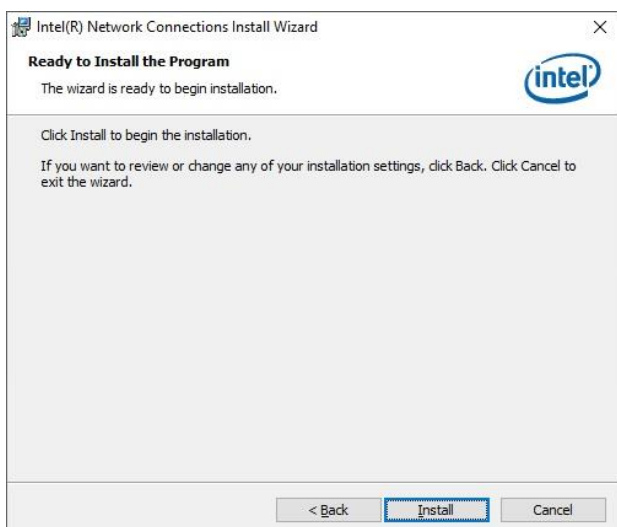
Step 1. Click Install Drivers and Software.



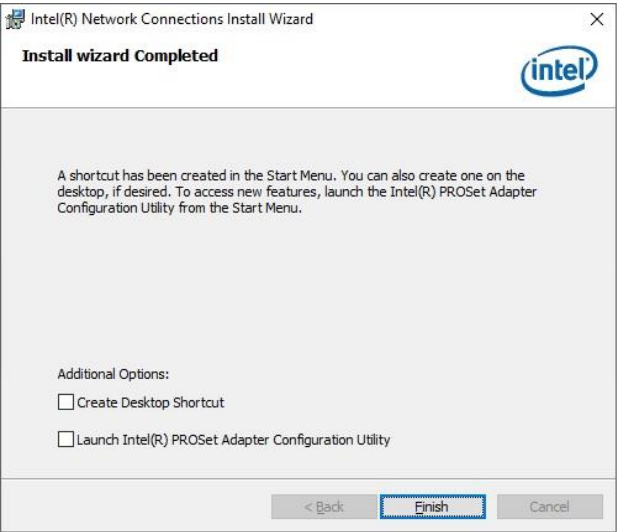
Step 4. Click Next.



Step 2. Click Next.



Step 5. Click Install.



Step 6. Click **Finish** to complete the setup.

