

## PCIE-1158 8-port PCI Express USB Vision Frame Grabber Startup Manual

### Packing List

Before you begin installing your card, please make sure that the following items have been shipped:

- 1 x PCIE-1158 8-port PCI Express USB Vision Frame Grabber

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

### Specifications

#### General

- **Chipset:**
  - 4 Renesas  $\mu$ PD720202 USB3.0 host controller
  - Compliant with USB 3.0 Specification Revision 1.0
  - Compliant with Intel<sup>®</sup> xHCI Specification Revision 1.0
- **Interface:** PCIe 2.0 4x
- **Data Transfer Rate:**
  - SuperSpeed
  - High Speed
  - Full Speed
  - Low Speed
- **Current Protection:** 900 mA per-port current limit

Note: For better compatibility, please change the PCIe settings to Gen 2.0 on device card slot.

#### Mechanical and Environmental

- **Dimensions (L x W):** 153 x 106 mm
- **Power Requirements:** Slot: PCIe 2.0 standard
- **Net Weight:** 300 g

For more information on this and other Advantech products, please visit our website at:

<http://www.advantech.com>

For technical support and service, please visit our support website at:

<http://support.advantech.com.tw/support/default.aspx>

This manual is for the PCIE-1158 Series Rev. A1

Part No. 2041115800  
Printed in China

1st Edition  
October 2021

### Connectors

#### Main Board Connectors

Label	Function
USB1~USB8	USB3.0 Ports 1~8
Connector1	AT/ATX 12V <sub>DC</sub> input 4-pin power connector

#### AT/ATX 12V DC Input 4-pin Power Connector

Pin	Function
Pin 1	GND
Pin 2	GND
Pin 3	+12V
Pin 4	+12V

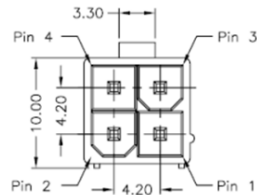


Figure 1: AT/ATX 12V DC Input 4-pin Power Connector

### Switch (Reserved)

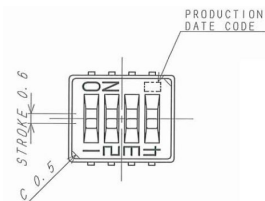


Figure 2: Slide Switches

## Declaration of Conformity

This device complies with the requirements in Part 15 of the FCC rules. Operation is subject to the following two conditions:

1. The device may not cause harmful interference;
2. The device must accept any interference received, including interference that may cause undesired operation.

## Block Diagram

