

# **GIGABYTE™**

## **ME03-PE0**

AMD EPYC™ 8004 UP Server Board

### User Manual

Rev. 1.0

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## **Documentation Classifications**

In order to assist in the use of this product, GIGABYTE provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents

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For related product specifications, the latest firmware and software, and other information, please visit our website at: <http://www.gigabyte.com>.

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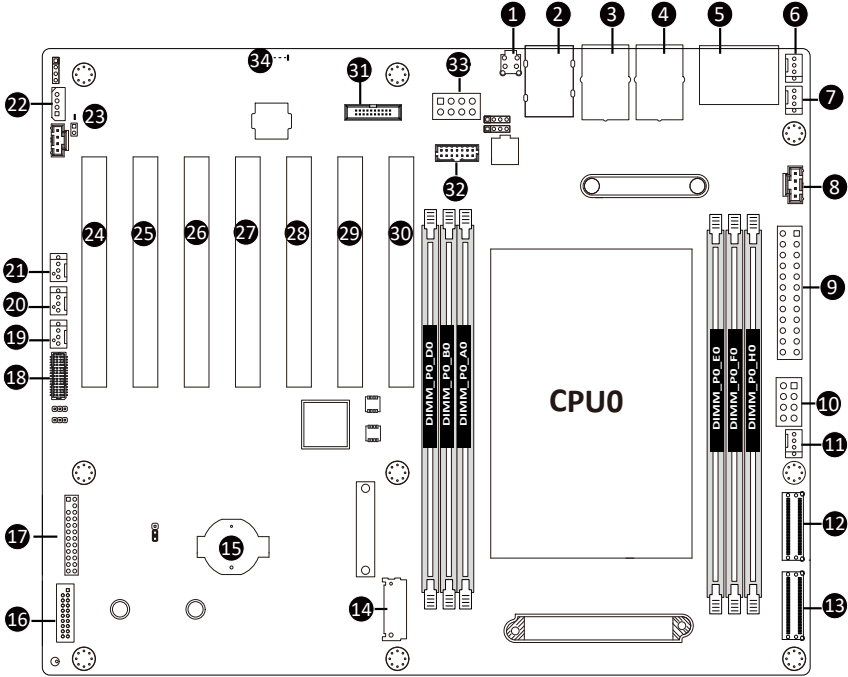
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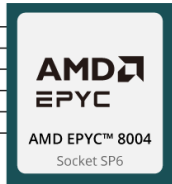
# ME03-PE0 Motherboard Layout



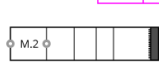
Item	Code	Description
1	SW_ID	ID Button with LED
2	USB3_MLAN	Sever Management LAN Port (Top)/USB 3.2 Gen1 Ports (Bottom)
3	LAN1	1GbE LAN Port #1
4	LAN2	1GbE LAN Port #2
5	COM1_VGA	Serial Port(Top)/VGA Port(Bottom)
6	SYS_FAN1	System Fan Connector #1
7	SYS_FAN3	System Fan Connector #3
8	PMBUS	PMBus Connector
9	ATX1	2x12 Pin Main Power Connector
10	P12V_AUX1	2x4 Pin 12V Power Connector
11	CPU_FAN1	CPU Fan Connector #1
12	U2_P0_G3A	MCIO Connector (PCIe Gen4)
13	U2_P0_G3B	MCIO Connector (PCIe Gen4)
14	M2_0	M.2 Slot(PCIe Gen4 x4, Support NGFF-2280/22110)
15	BAT	Battery Socket
16	F_USB3_1	Front Panel USB 3.2 Gen1 Connector #1
17	FP_1	Front Panel Header
18	BP_1	HDD Backplane Board Connector
19	SYS_FAN4	System Fan Connector #4
20	SYS_FAN5	System Fan Connector #5
21	SYS_FAN2	System Fan Connector #2
22	IPMB1	IPMB Connector
23	CASE_OPEN1	Case Open Intrusion Alert Header
24	PCIe_1	PCIe x16 Slot #1(Gen5 x16)
25	PCIe_2	PCIe x16 Slot #2(Gen5 x16)
26	PCIe_3	PCIe x16 Slot #3(Gen5 x16)
27	PCIe_4	PCIe x16 Slot #4(Gen5 x16)
28	PCIe_5	PCIe x16 Slot #5(Gen5 x16)
29	PCIe_6	PCIe x16 Slot #6(Gen5 x16)
30	PCIe_7	PCIe x16 Slot #7(Gen5 x16)
31	CN_NCSI1	NCSI Connector
32	SPI_TPM	TPM Connector
33	P12V_AUX2	2x4 Pin 12V Power Connector
34	LED_BMC	BMC Firmware Readiness LED

# Block Diagram

6-Channel DDR5, 6 x DIMMs  
Speed up to 4800 MHz



2 x M.2



FUSB3.2 Gen1



2 x RUBS3.2 Gen1



PCIe5.0 x16 / SATAIII x16

PCIe3.0 x4

USB3.2 Gen1 x2

RTL5411 USB Hub

USB3.2 Gen1

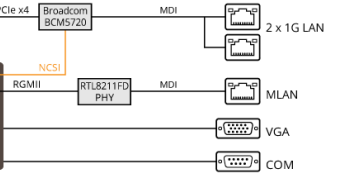
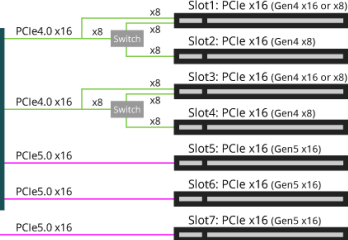
TPM

PCIe x1  
eSATA  
USB2.0 x1



SPI Flash 32MB

SPI Flash 64MB



# Chapter 1 Hardware Installation

## 1-1 Installation Precautions

The motherboard contains numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user's manual and follow these procedures:










- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

# 1-2 Product Specifications



**NOTE:**

We reserve the right to make any changes to the product specifications and product-related information without prior notice.

 Board Size	<ul style="list-style-type: none"> <li>♦ ATX</li> <li>♦ 304.8 x 243.8mm (W x D)</li> </ul>
 Processor Supported	<ul style="list-style-type: none"> <li>♦ AMD EPYC™ 8004 Series processors</li> <li>♦ Single processor, cTDP up to 225W</li> </ul>
 Socket	<ul style="list-style-type: none"> <li>♦ 1 x LGA 4844</li> <li>♦ Socket SP6</li> </ul>
 Chipset	<ul style="list-style-type: none"> <li>♦ System on Chip</li> </ul>
 Memory	<ul style="list-style-type: none"> <li>♦ 6 x DIMM slots</li> <li>♦ DDR5 memory supported only</li> <li>♦ 6-Channel memory per processor</li> <li>♦ RDIMM: Up to 4800 MT/s</li> </ul>
 LAN	<ul style="list-style-type: none"> <li>♦ 2 x 1Gb/s BASE-T LAN ports (1 x Broadcom® BCM5720)</li> <li>♦ NCSI function supported</li> <li>♦ 1 x 10/100/1000 management LAN</li> </ul>
 Video	<ul style="list-style-type: none"> <li>♦ Integrated in ASPEED® AST2600 - 1 x VGA port</li> </ul>
 Storage Interface	<p><b>MCIO</b></p> <ul style="list-style-type: none"> <li>♦ 2 x MCIO 8i for 4 x Gen5 NVMe or 16 x SATA</li> </ul> <p><b>M.2</b></p> <ul style="list-style-type: none"> <li>♦ 1 x M.2 (2280/22110), PCIe Gen3 x4</li> </ul>
 PCIe Expansion Slots	<ul style="list-style-type: none"> <li>♦ Slot_7: PCIe x16 (Gen5 x16)</li> <li>♦ Slot_6: PCIe x16 (Gen5 x16)</li> <li>♦ Slot_5: PCIe x16 (Gen5 x16)</li> <li>♦ Slot_4: PCIe x16 (Gen4 x8)</li> <li>♦ Slot_3: PCIe x16 (Gen4 x16 or x8), <b>shared with Slot_4</b></li> <li>♦ Slot_2: PCIe x16 (Gen4 x8)</li> <li>♦ Slot_1: PCIe x16 (Gen4 x16 or x8), <b>shared with Slot_2</b></li> </ul> <p>♦ 2 x MCIO 8i (PCIe Gen5 x8 or SATA) [1]</p> <p><small><sup>[1]</sup> The connector supports SATA by default. To enable the other option, please refer to the manual and adjust the settings in the BIOS.</small></p>



#### Internal I/O

- ◆ 1 x 24-pin ATX main power connector
- ◆ 2 x 8-pin ATX 12V power connectors
- ◆ 1 x CPU fan header
- ◆ 5 x System fan headers
- ◆ 1 x USB 3.2 Gen1 x2 header
- ◆ 1 x M.2 slot
- ◆ 2 x MCIO 8i connectors
- ◆ 1 x Front panel header
- ◆ 1 x Backplane board header
- ◆ 1 x PMBus connector
- ◆ 1 x IPMB connector
- ◆ 1 x TPM header






#### Rear I/O Connectors

- ◆ 2 x USB 3.2 Gen1 ports (Type-A)
- ◆ 1 x VGA port
- ◆ 1 x COM port
- ◆ 2 x RJ45 ports
- ◆ 1 x MLAN port
- ◆ 1 x ID button with LED



#### Security Modules

- ◆ 1 x TPM header with SPI interface
- **Optional** TPM2.0 kit: [CTM010](#)

	<b>Board Management</b>	<ul style="list-style-type: none"> <li>◆ ASPEED® AST2600 management controller</li> <li>◆ GIGABYTE Management Console (AMI MegaRAC SP-X) web interface</li>   <li>◆ Dashboard</li> <li>◆ HTML5 KVM</li> <li>◆ Sensor Monitor (Voltage, RPM, Temperature, CPU Status ...etc.)</li> <li>◆ Sensor Reading History Data</li> <li>◆ FRU Information</li> <li>◆ SEL Log in Linear Storage / Circular Storage Policy</li> <li>◆ Hardware Inventory</li> <li>◆ Fan Profile</li> <li>◆ System Firewall</li> <li>◆ Power Consumption</li> <li>◆ Power Control</li> <li>◆ Advanced power capping</li> <li>◆ LDAP / AD / RADIUS Support</li> <li>◆ Backup &amp; Restore Configuration</li> <li>◆ Remote BIOS/BMC/CPLD Update</li> <li>◆ Event Log Filter</li> <li>◆ User Management</li> <li>◆ Media Redirection Settings</li> <li>◆ PAM Order Settings</li> <li>◆ SSL Settings</li> <li>◆ SMTP Settings</li> </ul>
	<b>Operating Properties</b>	<ul style="list-style-type: none"> <li>◆ Operating temperature: 10°C to 40°C</li> <li>◆ Operating humidity: 8-80% (non-condensing)</li> <li>◆ Non-operating temperature: -40°C to 60°C</li> <li>◆ Non-operating humidity: 20%-95% (non-condensing)</li> </ul>
	<b>PSU Connectors</b>	<ul style="list-style-type: none"> <li>◆ 1 x 24-pin ATX main power connector</li> <li>◆ 2 x 8-pin ATX 12V power connectors</li> </ul>

## 1-3 Installing and Removing the CPU and Heat Sink



Read the following guidelines before you begin to install the CPU:

- Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- Open the system according to the instructions.



### WARNING!

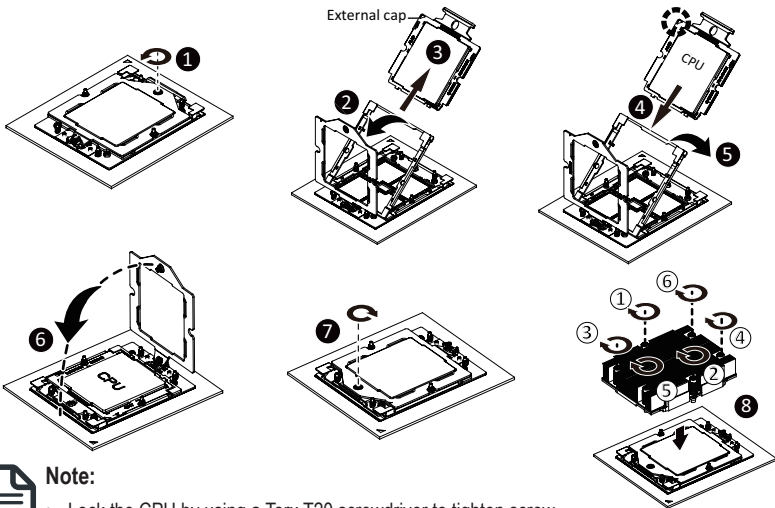
Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

### Follow these instructions to Install the CPU:

1. Loosen the captive screw securing the CPU cover.
2. Flip open the CPU cover.
3. Remove the CPU carrier from the CPU frame using the handle on the CPU carrier.
4. Using the handle on the CPU carrier insert the new CPU carrier with CPU installed into the CPU frame.

NOTE: Ensure the CPU is installed in the CPU carrier in the correct orientation, with the triangle on the CPU aligned to the top left corner of the CPU carrier.

5. Flip the CPU frame with CPU installed into place in the CPU socket.
6. Flip the CPU cover into place over the CPU socket.
7. Tighten the CPU cover screw to secure the CPU cover in place.



### Note:

- Lock the CPU by using a Torx T20 screwdriver to tighten screw.  
The screw tightening torque:  $13.5 \pm 0.5$  kgf-cm.



## 1-4 Installing and Removing Memory

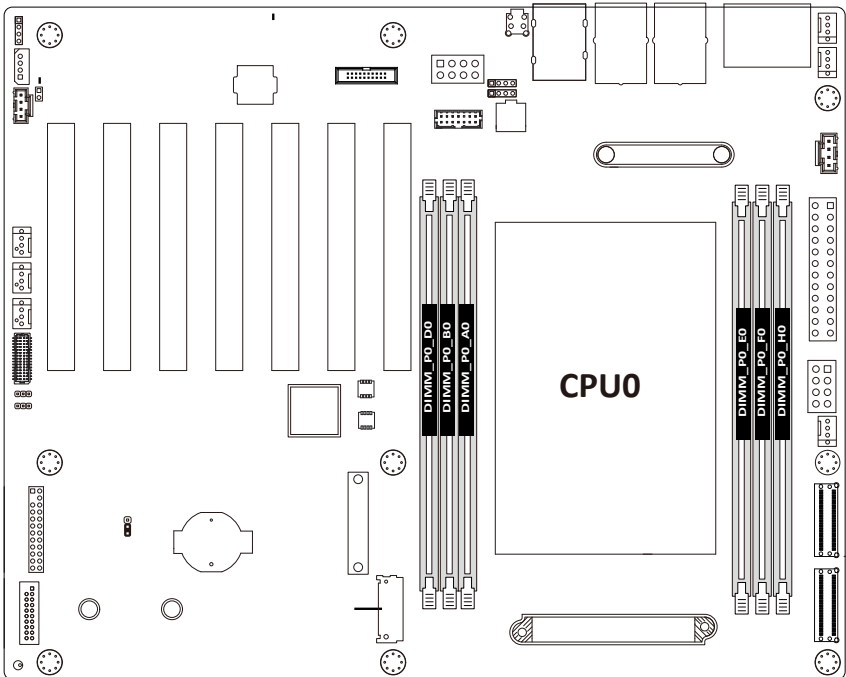


Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

### 1-4-1 6-Channel Memory Configuration

This motherboard provides 6 DDR5 memory slots and supports 6-Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.



## 1-4-2 Installing and Removing the Memory Module

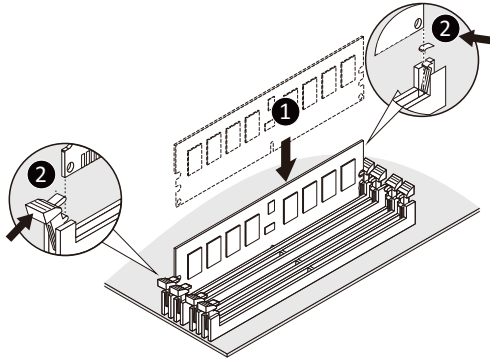


Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module.

Be sure to install DDR5 DIMMs on to this motherboard.

Follow these instructions to install a DIMM module:

1. Insert the DIMM memory module vertically into the DIMM slot and push it down.
2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
3. Reverse the installation steps when you want to remove the DIMM module.



## 1-4-3 Processor and Memory Module Matrix Table

Memory Q'ty	CPU0					
	D0	B0	A0	E0	F0	H0
1 DIMM			v			
2 DIMM			v	v		
4 DIMM	v		v	v		v
6 DIMM	v	v	v	v	v	v

### 1-4-4 Memory Population Table

DIMM Type	DIMM Population		DDR5 Frequency (MT/s)
	DIMM 0	DIMM 1	
RDIMM	--	1R	4800
	1R	1R	4000
	--	2R	4800
	2R	2R	3600



**Note:**

- When only one DIMM per channel is used, it must be populated in memory slot DIMM1.

## 1-5 Installing the M.2 SSD Module



### WARNING

Installation of the thermal pad over the M.2 device is required when installing an M.2 device. Lack of the thermal pad may result in the system overheating and throttle the system performance.

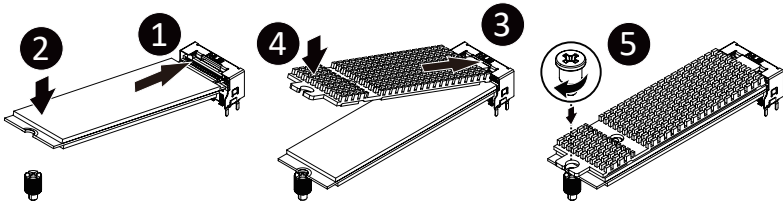


### CAUTION

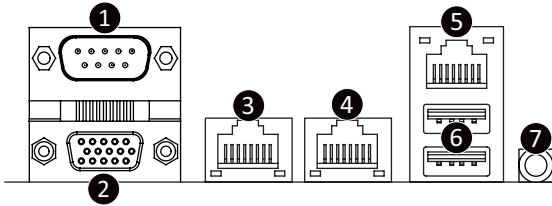
The position of the stand-off screw will depend on the size of the M.2 device. The stand-off screw is pre-installed for 22110 cards as standard. Refer to the size of the M.2 device and change the position of the stand-off screw accordingly.

### Follow these instructions to install the M.2 device and heat sink:

1. Insert the M.2 device into the M.2 connector.
2. Press down on the M.2 device.
3. Install the thermal pad of the M.2 device to the M.2 device.
4. Press down on the thermal pad.
5. Secure the M.2 device and its thermal pad to the motherboard with a single screw.
6. Reverse steps 1-5 to remove the M.2 device.



# 1-6 Back Panel Connectors



**1 Serial Port**

Connect to serial-based mouse or data processing devices.

**2 VGA Port**

Connect to a monitor device.

**3 1 GbE LAN Port #2**

The Gigabit Ethernet LAN port provides Internet connection at up to 1 Gbps data rate. See the section below for a description of the states of the LAN port LEDs.

**4 1 GbE LAN Port #1**

The Gigabit Ethernet LAN port provides Internet connection at up to 1 Gbps data rate. See the section below for a description of the states of the LAN port LEDs.

**5 Server Management LAN Port**

The LAN port provides Internet connection with data transfer speeds of 10/100/1000Mbps. This port is the dedicated LAN port for Server Management.

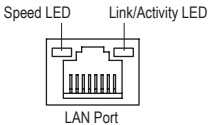
**6 USB 3.2 Gen1 Ports**

The USB port supports the USB 3.2 specification. Use this port for USB devices such as a USB keyboard/mouse, USB printer, USB flash drive etc.

**7 ID button with LED**

When the system identification is active, the ID LED on the front/ back panel glows blue.

**LAN and ID Button LEDs**



**ID button/LED:**

State	Description
Blue On	System identification is active
Off	System identification is disabled

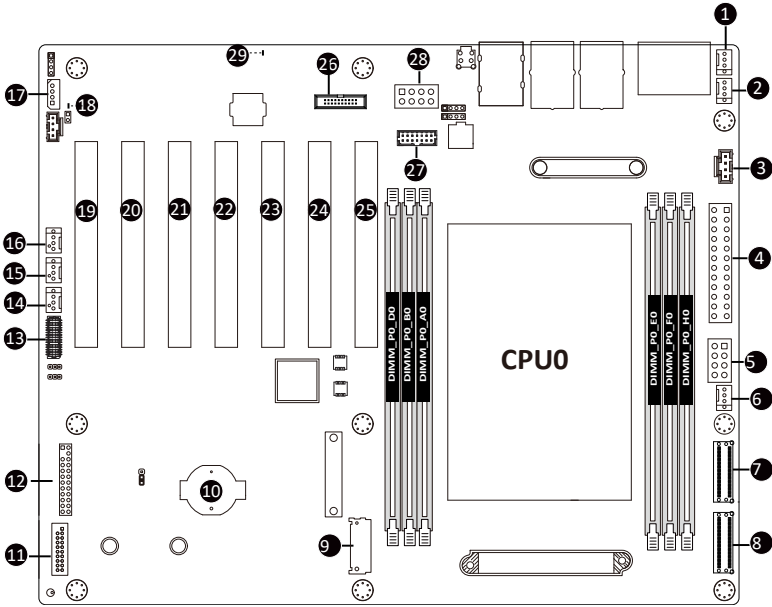
**10/100/1000 LAN LED:**

State	Description
Yellow On	1Gbps data rate
Green On	100Mbps data rate
Off	10Mbps data rate



- When removing the cable connected to a back panel connector, first remove the cable from your device and then remove it from the motherboard.
- When removing the cable, pull it straight out from the connector. Do not rock it side to side to prevent an electrical short inside the cable connector.

# 1-7 Internal Connectors



1) SYS_FAN1	16) SYS_FAN2
2) SYS_FAN3	17) IPMB1
3) PMBUS	18) CASE_OPEN1
4) ATX1	19) PCIe_1
5) P12V_AUX1	20) PCIe_2
6) CPU_FAN1	21) PCIe_3
7) U2_P0_G3A	22) PCIe_4
8) U2_P0_G3B	23) PCIe_5
9) M2_0	24) PCIe_6
10) BAT	25) PCIe_7
11) F_USB3_1	26) CN_NCSI1
12) FP_1	27) SPI_TPM
13) BP_1	28) P12V_AUX2
14) SYS_FAN4	29) LED_BMC
15) SYS_FAN5	

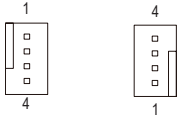
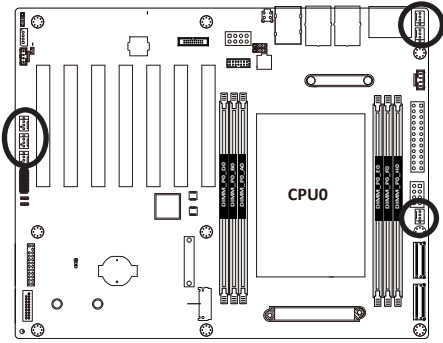


Read the following guidelines before connecting external devices:

- First make sure your devices are compliant with the connectors you wish to connect.
- Before installing the devices, be sure to turn off the devices and your computer. Unplug the power cord from the power outlet to prevent damage to the devices.
- After installing the device and before turning on the computer, make sure the device cable has been securely attached to the connector on the motherboard.

## 1/2/6/14/15/16) SYS\_FAN1/SYS\_FAN3/CPU\_FAN1/SYS\_FAN4/SYS\_FAN5/SYS\_FAN6 (FAN Headers)

The motherboard has one 4-pin CPU fan header (CPU\_FAN), and five 4-pin (SYS\_FAN) system fan headers. Most fan headers possess a foolproof insertion design. When connecting a fan cable, be sure to connect it in the correct orientation (the black connector wire is the ground wire). The motherboard supports CPU fan speed control, which requires the use of a CPU fan with fan speed control design. For optimum heat dissipation, it is recommended that a system fan be installed inside the chassis.



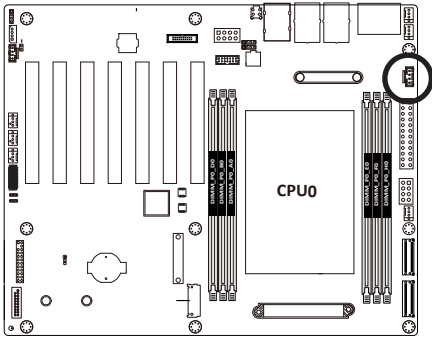
Pin No.	Definition
1	GND
2	+12V
3	Sense
4	Speed Control



- Be sure to connect fan cables to the fan headers to prevent your CPU and system from overheating. Overheating may result in damage to the CPU or the system may hang.
- These fan headers are not configuration jumper blocks. Do not place a jumper cap on the headers.

### 3) PMBus Connector

The Power Management Bus (PMBus) is a variant of the System Management Bus (SMBus) which is targeted at digital management of power supplies.



Pin No.	Definition
1	PMBus Clock
2	PMBus Data
3	PMBus Alert
4	GND
5	3.3V Sense

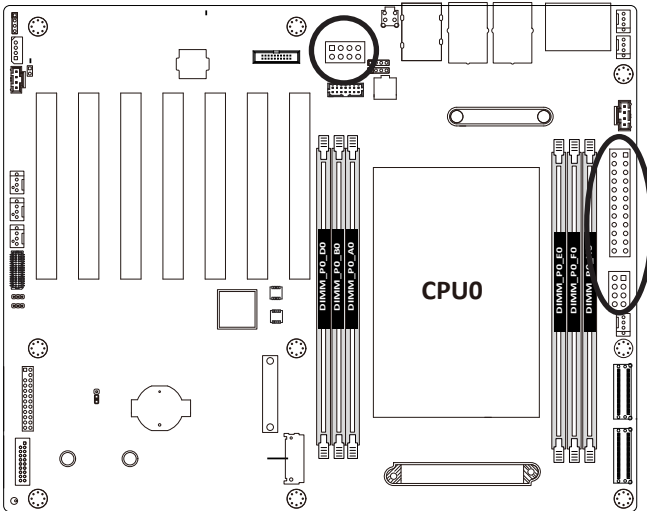
## 4/5/28) ATX1/P12V\_AUX1/P12V\_AUX2

### (2x12 Main Power Connector and 2x4 12V Power Connector)

With the use of the power connector, the power supply can supply enough stable power to all the components on the motherboard. Before connecting the power connector, first make sure the power supply is turned off and all devices are properly installed. The power connector possesses a foolproof design. Connect the power supply cable to the power connector in the correct orientation. The 12V power connector mainly supplies power to the CPU. If the 12V power connector is not connected, the computer will not start.



To meet expansion requirements, it is recommended that a power supply that can withstand high power consumption be used (500W or greater). If a power supply is used that does not provide the required power, the result can lead to an unstable or unbootable system.



13 1

#### ATX1

Pin No.	Definition	Pin No.	Definition
1	3.3V	13	3.3V
2	3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	Power Good	20	NC
9	5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	3.3V	24	GND

24 12

#### P12V\_AUX1/P12V\_AUX2

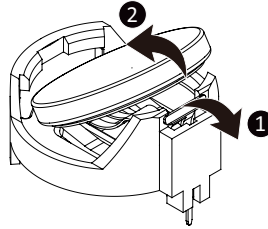
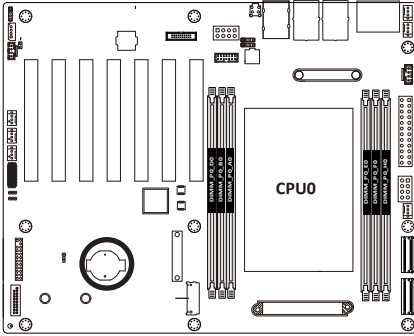
Pin No.	Definition
1	GND
2	GND
3	GND
4	GND
5	+12V
6	+12V
7	+12V
8	+12V





## 10) BAT (Battery Socket)

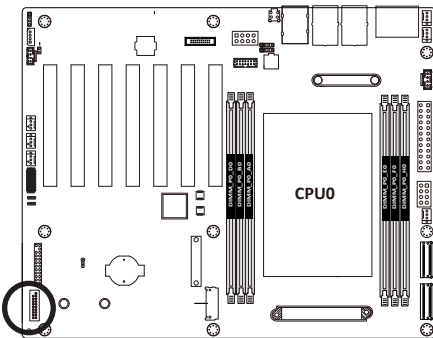
The battery provides power to keep the values (such as BIOS configurations, date, and time information) in the CMOS when the computer is turned off. Replace the battery when the battery voltage drops to a low level, or the CMOS values may not be accurate or may be lost.



- Always turn off your computer and unplug the power cord before replacing the battery.
- Replace the battery with an equivalent one. Danger of explosion if the battery is replaced with an incorrect model.
- Contact the place of purchase or local dealer if you are not able to replace the battery by yourself or uncertain about the battery model.
- Used batteries must be handled in accordance with local environmental regulations.

## 11) F\_USB3\_1 (USB 3.2 Gen1 Connector)

The connector/header conform to USB 2.0/ 3.0 specification. Each USB connector/header can provide two USB ports via an optional USB bracket. For purchasing the optional USB bracket, please contact the local dealer.



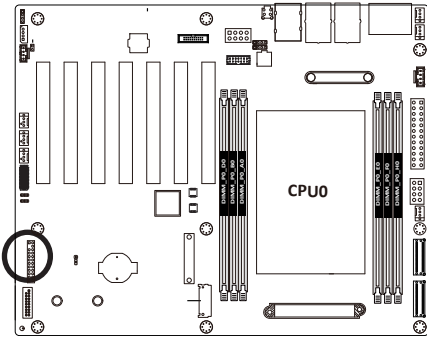
USB 3.0 Connector

Pin No.	Definition	Pin No.	Definition
1	Power	11	IntA_P2_D+
2	IntA_P1_SSRX-	12	IntA_P2_D-
3	IntA_P1_SSRX+	13	GND
4	GND	14	IntA_P2_SSTX+
5	IntA_P1_SSTX-	15	IntA_P2_SSTX-
6	IntA_P1_SSTX+	16	GND
7	GND	17	IntA_P2_SSRX+
8	IntA_P1_D-	18	IntA_P2_SSRX-
9	IntA_P1_D+	19	Power
10	NC	20	No Pin



## 12) FP\_1 (Front Panel Header)

Connect the power switch, reset switch, speaker, chassis intrusion switch/sensor and system status indicator on the chassis to this header according to the pin assignments below. Note the positive and negative pins before connecting the cables.



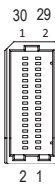
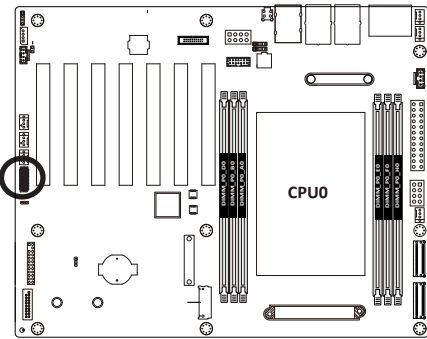
Pin No.	Definition	Pin No.	Definition
1	Power LED+	2	5V Standby
3	No Pin	4	ID LED+
5	Power LED-	6	ID LED-
7*	HDD LED+	8	System Status LED+
9*	HDD LED-	10	System Status LED-
11	Power Button	12	LAN1 Active LED+
13	GND	14	LAN1 Link LED-
15	Reset Button	16	SMBus Data
17	GND	18	SMBus Clock
19	ID Button	20	Case Open
21	GND	22	LAN2 Active LED+
23	NMI Switch	24	LAN2 Link LED-

\*Note: Pin 7 & Pin 9 are reserved for Gigabyte systems.



The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

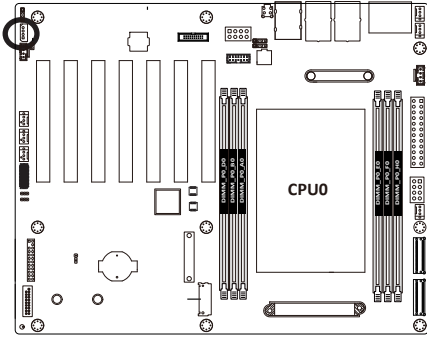
## 13) BP\_1 (HDD Backplane Board Header)



Pin No.	Definition	Pin No.	Definition
1	HP_ALERT_L	2	BPMI DIN/OUT
3	GND	4	BPMI DIN/IN
5	BPMI_LOAD	6	GND
7	BPMI_CLK	8	PLD_Program_EN
9	GLED_AMB_N	10	GLED_GRN_N
11	FAN_IRQ_N	12	Reserved
13	BP_SCL	14	GND
15	BP_SDA	16	BP_RST_N
17	SMB_U2_TMP_SCL	18	GND
19	SMB_U2_TMP_SDA	20	I2C_DEV_RST
21	PH_HP_SCL0	22	GND
23	PH_HP_SDA0	24	GND
25	PH_HP_SCL1	26	GND
27	PH_HP_SDA1	28	GND
15	P3V3_AUX	30	P3V3_AUX

### 17) IPMB (Intelligent Platform Management Bus) Connector

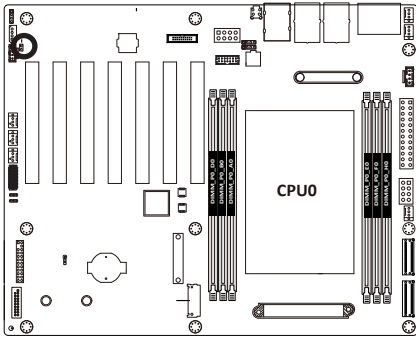
The Intelligent Platform Management Bus Communications Protocol defines a byte-level transport for transferring Intelligent Platform Management Interface Specification (IPMI) messages between intelligent I2C devices.



Pin No.	Definition
1	Clock
2	GND
3	Data
4	VCC

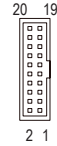
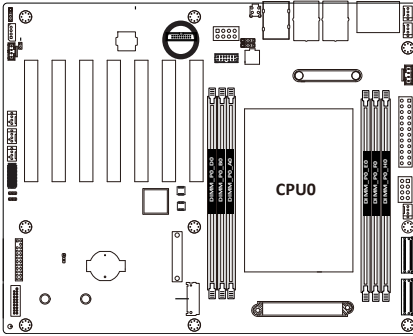
### 18) CASE\_OPEN (Case Open Intrusion Alert Header)

This motherboard provides a chassis detection feature that detects if the chassis cover has been removed. This function requires a chassis with chassis intrusion detection design.



- Open: Normal Operation (Default)
- Closed: Active Chassis Intrusion Alert

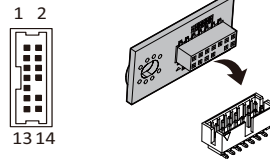
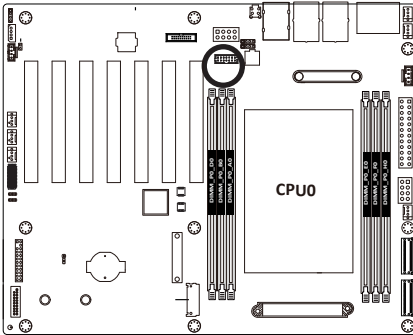
## 26) CN\_NCSI (NCSI Connector)



Pin No.	Definition	Pin No.	Definition
1	NCSI_CLK	2	GND
3	NCSI_RX_D0	4	GND
5	NCSI_RX_D1	6	GND
7	NCSI_CRD_DV	8	GND
9	NCSI_RX_ER	10	GND
11	P3V3_AUX	12	GND
13	NCSI_TX_D1	14	GND
15	NCSI_TX_D0	16	GND
17	NCSI_TX_EN	18	GND
19	NCSI_PRESENT	20	P3V3_AUX

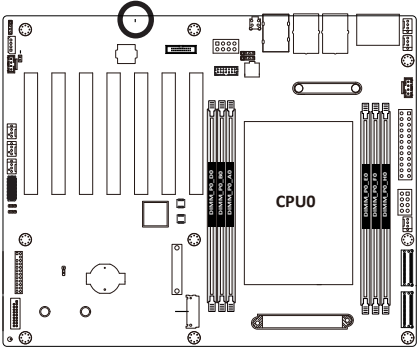
## 27) SPI\_TPM (Trusted Platform Module Connector)

Trusted Platform Module (TPM) is an international standard for a secure cryptoprocessor, a dedicated microcontroller designed to secure hardware through integrated cryptographic keys.




Pin No.	Definition	Pin No.	Definition
1	Clock	8	NC
2	P_3V3_AUX	9	NC
3	LPC_RST	10	No Pin
4	NC	11	NC
5	SPI_MISO	12	GND
6	IRQ_SPI	13	SPI_CS_N
7	SPI_MOSI	14	GND

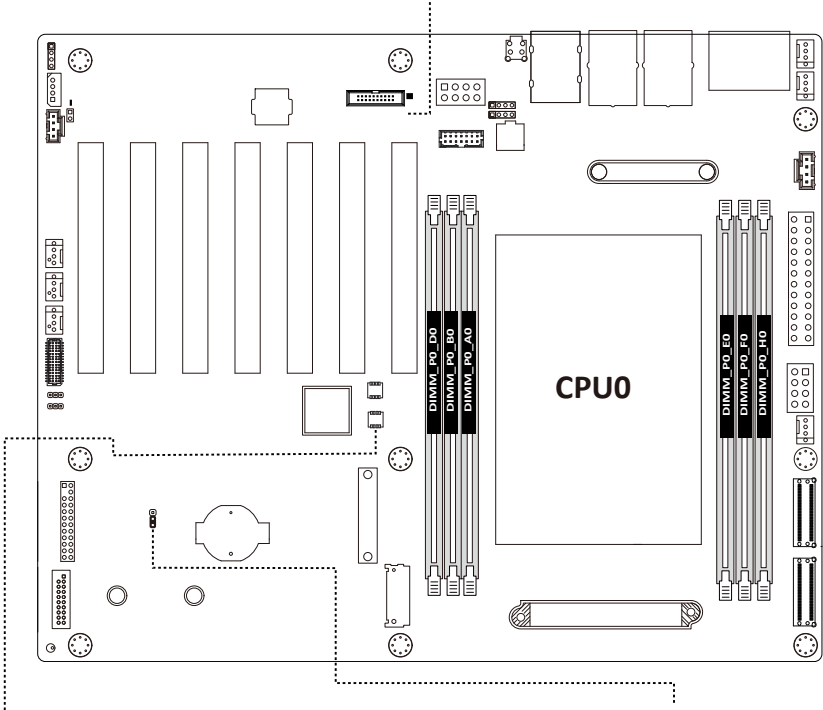
## 29) LED\_BMC (BMC Firmware Readiness LED)



State	Description
On	BMC firmware is initial
Blink	BMC firmware is ready
Off	AC loss


# 1-8 Jumper Settings

	SW	Device
	OFF	BCM 57416
	ON	CN_NCSI



J1		ON	OFF
1	HSMB_SEL	BIOS Defined	
2	PMBUS_SEL	BIOS Defined	
3	BIOS_PWD	Clear supervisor password	Normal [Default]
4	BIOS_RCVR	BIOS recovery mode	Normal [Default]

Clear CMOS  
CLR\_CMOS



1 — Default  
2 — Enable  
3 — Enable

## Chapter 2 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the <DEL> key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the **Exit** section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 1 for how to clear the CMOS values.)

### BIOS Setup Program Function Keys

<<-><->>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<Enter>	Execute command or enter the submenu
<Esc>	Main Menu: Exit the BIOS Setup program Submenus: Exit current submenu
<F1>	Show descriptions of general help
<F3>	Restore the previous BIOS settings for the current submenus
<F9>	Load the Optimized BIOS default settings for the current submenus
<F10>	Save all the changes and exit the BIOS Setup program

■ **Main**

This setup page includes all the items of the standard compatible BIOS.

■ **Advanced**

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

■ **AMD CBS**

This setup page includes the common items for configuration of AMD motherboard-related information.

■ **AMD PBS Option**

This setup page includes the common items for configuration of AMD CPM RAS related settings.

■ **Chipset**

This setup page includes all the submenu options for configuring the functions of the North Bridge.

■ **Server Management**

Server additional features enabled/disabled setup menus.

■ **Security**

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

■ **Boot**

This setup page provides items for configuration of the boot sequence.

■ **Save & Exit**

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)



# 2-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

## Main Menu Help

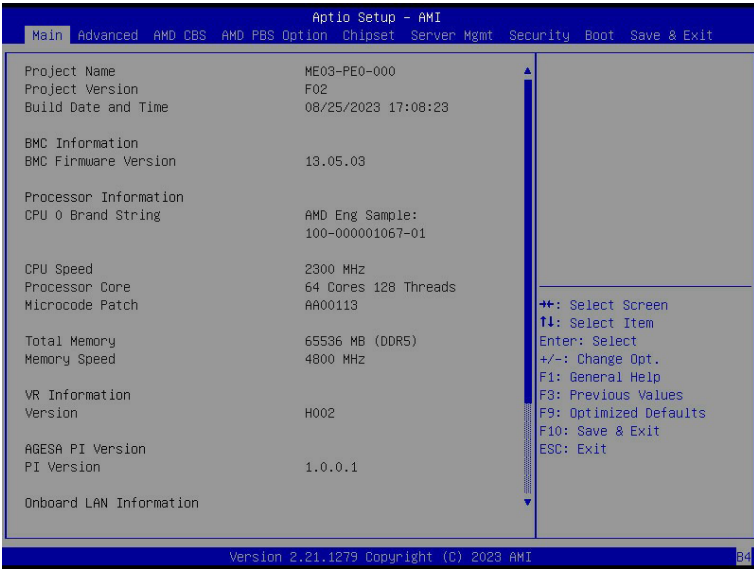
The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

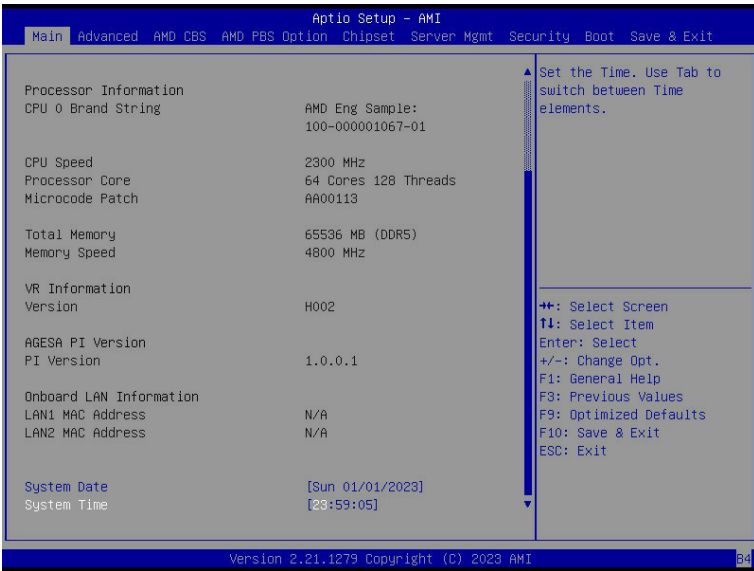
## Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



- When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.





Parameter	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information <sup>(Note1)</sup>	
BMC Firmware Version <sup>(Note1)</sup>	Displays BMC firmware version information.
Processor Information	
CPU Brand String/ CPU Speed / Processor Core / Microcode Patch	Displays the technical specifications for the installed processor(s).
Total Memory <sup>(Note2)</sup>	Displays the total memory size of the installed memory.
Memory Speed <sup>(Note2)</sup>	Displays the frequency information of the installed memory.
VR Information Version	Displays VR version information.
AGESA PI Version	
PI Version	Displays AGESA PI version information.

(Note1) Functions available on selected models.

(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Parameter	Description
Onboard LAN Information	
LAN1 MAC Address <sup>(Note)</sup>	Displays LAN MAC address information.
LAN2 MAC Address <sup>(Note)</sup>	Displays LAN MAC address information.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

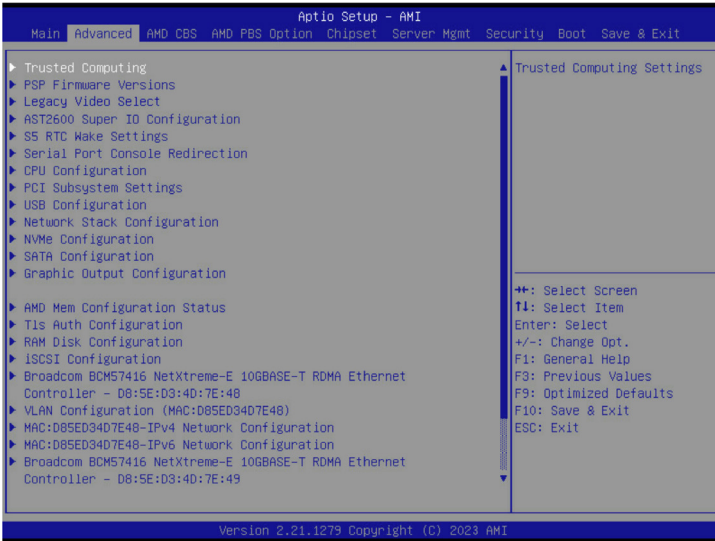
(Note) The number of LAN ports listed will depend on the motherboard / system model.

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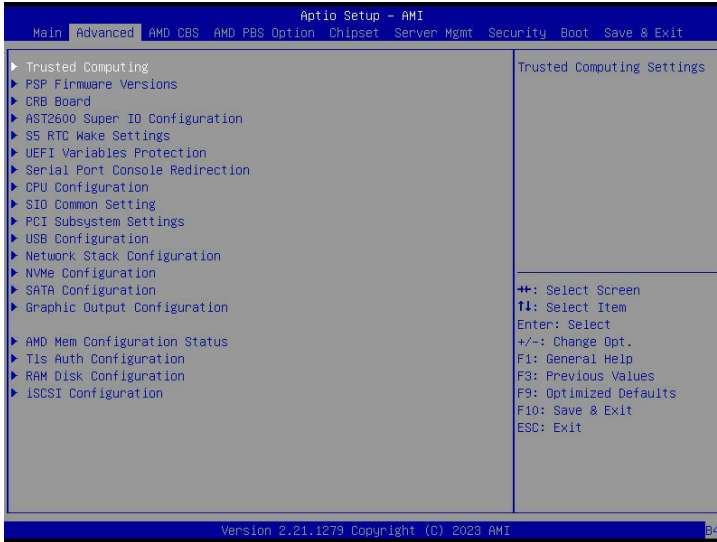
## 2-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.

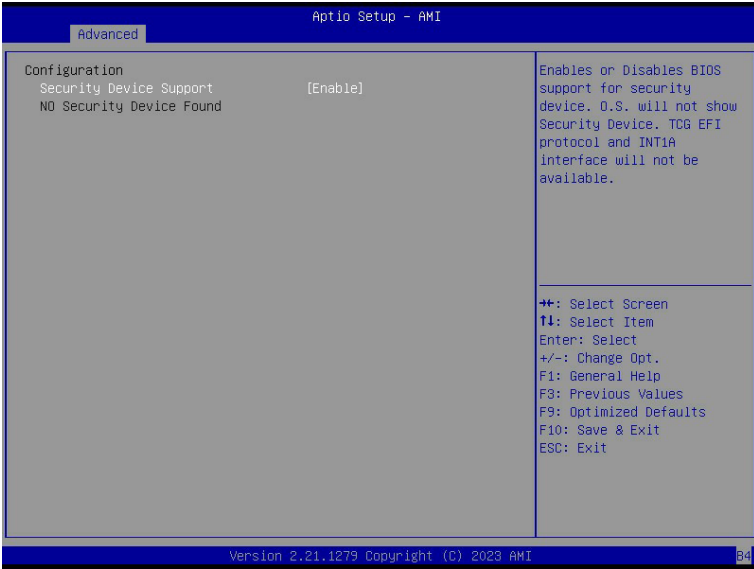
### When Boot Mode Select is set to UEFI (Default)



## When "Boot Mode Select" is set to Legacy in the Boot > Boot Mode Select section



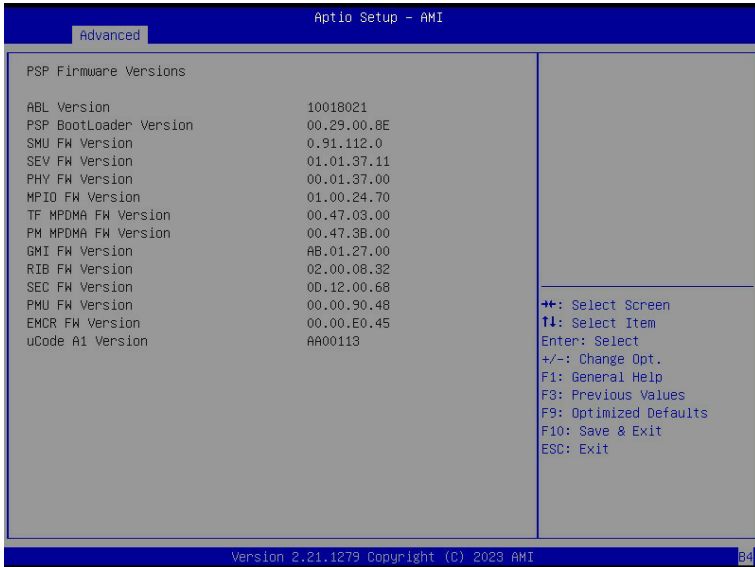
## 2-2-1 Trusted Computing



Parameter	Description
Configuration	
Security Device Support	<p>Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available.</p> <p>Options available: Disable, Enable. Default setting is <b>Enable</b>.</p>
SPI TPM Support	<p>Select Enable to activate TPM support feature.</p> <p>Options available: Disabled, Enabled. Default setting is <b>Disabled</b>.</p>

## 2-2-2 PSP Firmware Versions

The PSP Firmware Versions page displays the basic PSP firmware version information. Items on this window are non-configurable.



### 2-2-3 Legacy Video Select

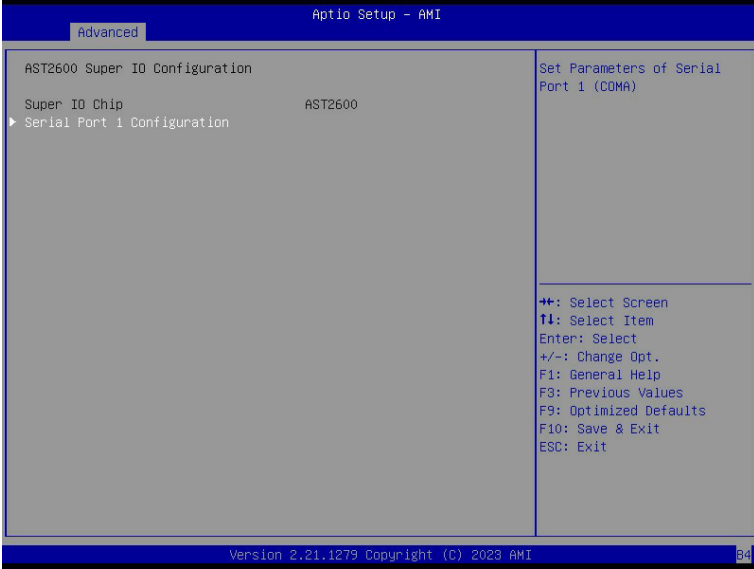


Parameter	Description
OnBrd/Ext VGA Select	Selects between onboard or external VGA support. Options available: Auto, Onboard, External. Default setting is <b>Auto</b> .

(Note) This configurable option will be displayed when "Boot Mode Select" is set to Legacy in the Boot > Boot Mode Select section.

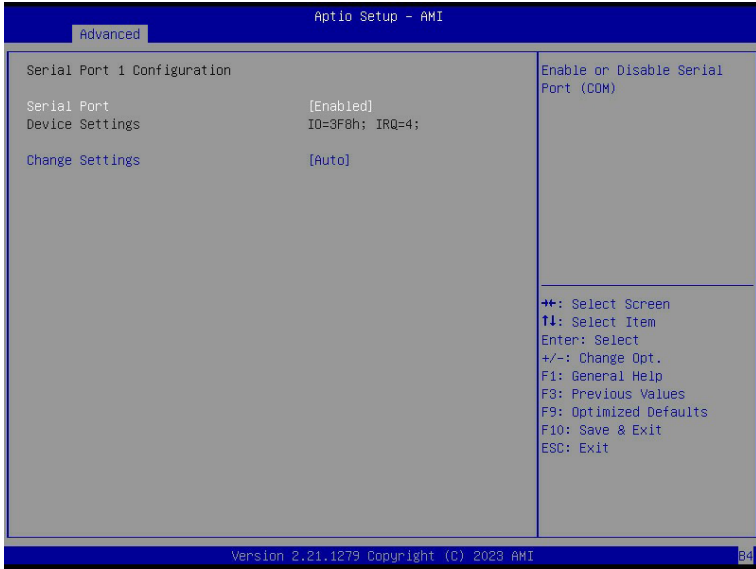


## 2-2-4 AST2600 Super IO Configuration



Parameter	Description
AST2600 Super IO Configuration	
Super IO Chip	Displays the super IO chip information
Serial Port 1 Configuration	Press [Enter] for configuration of advanced items.

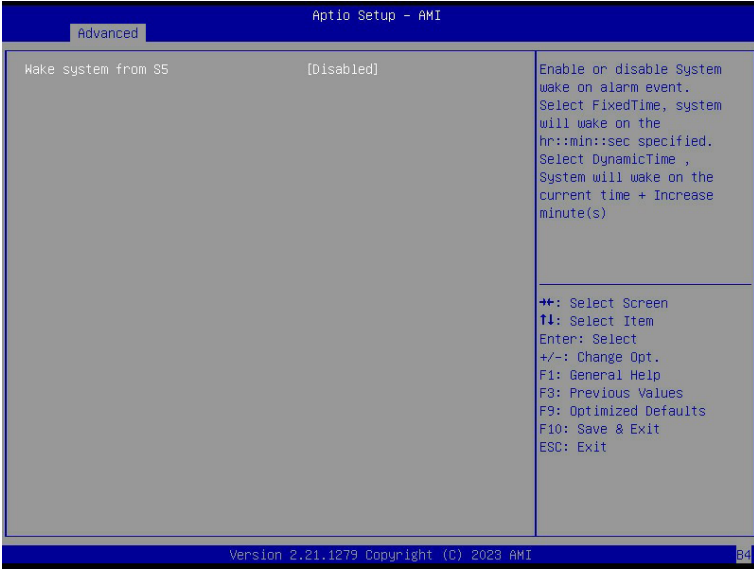
## 2-2-4-1 Serial Port 1 Configuration



Parameter	Description
Serial Port 1 Configuration	
Serial Port <sup>(Note)</sup>	Enable/Disable the Serial Port (COM). When set to Enabled allows you to configure the Serial port 1 settings. When set to Disabled, displays no configuration for the serial port. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
Devices Settings	Displays the Serial Port 1 device settings.
Change Settings	Select an optimal settings for Super IO Device. Options available for Serial Port 1: Auto IO=3F8h; IRQ=4; IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; Default setting is <b>Auto</b> .

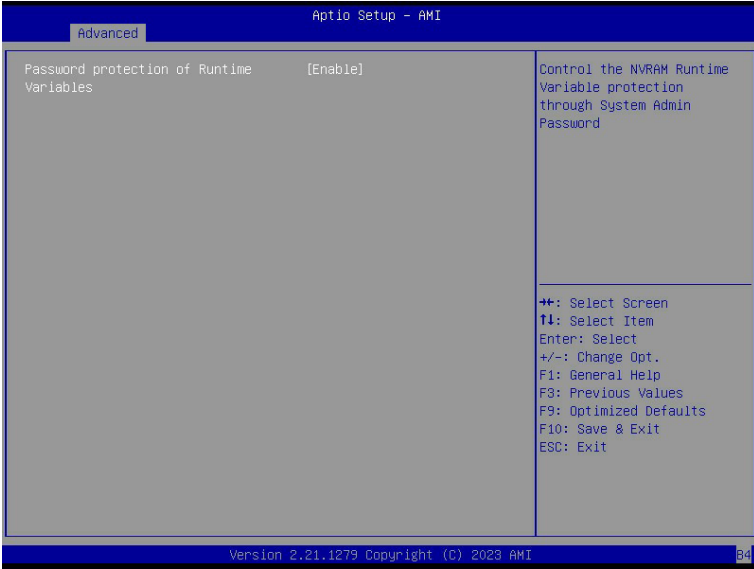
(Note) Advanced items prompt when this item is defined.

## 2-2-5 S5 RTC Wake Settings



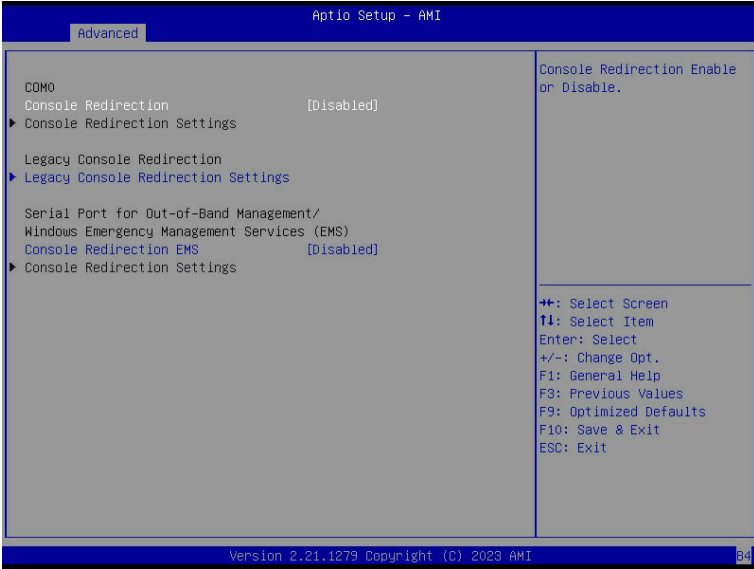
Parameter	Description
Wake System from S5	Enable/Disable system wake on alarm event. Options available: Disabled, Fixed Time, Dynamic Time. When Fixed Time is selected, system will wake on the hr::min::sec specified. Default setting is <b>Disabled</b> .

## 2-2-6 UEFI Settings



Parameter	Description
Password protection of Runtime Variables	Control the NVRAM Runtime Variable protection through System Admin Password. Options available: Enable, Disable. Default setting is <b>Enable</b> .

## 2-2-7 Serial Port Console Redirection



Parameter	Description
COM1/Serial Over LAN Console Redirection <sup>(Note)</sup>	Select whether to enable console redirection for specified device. Console redirection enables the users to manage the system from a remote location. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
COM1/Serial Over LAN Console Redirection Settings	Press [Enter] to configure advanced items. <b>Please note that this item is configurable when COM1/Serial Over LAN Console Redirection is set to Enabled.</b> <ul style="list-style-type: none"> <li>◆ Terminal Type           <ul style="list-style-type: none"> <li>– Selects a terminal type to be used for console redirection.</li> <li>– Options available: VT100, VT100+, ANSI, VT-UTF8. Default setting is <b>ANSI</b>.</li> </ul> </li> <li>◆ Bits per second           <ul style="list-style-type: none"> <li>– Selects the transfer rate for console redirection.</li> <li>– Options available: 9600, 19200, 38400, 57600, 115200. Default setting is <b>115200</b>.</li> </ul> </li> <li>◆ Data Bits           <ul style="list-style-type: none"> <li>– Selects the number of data bits used for console redirection.</li> <li>– Options available: 7, 8. Default setting is <b>8</b>.</li> </ul> </li> </ul>

(Note) Advanced items prompt when this item is defined.

Parameter	Description
COM1/Serial Over LAN Console Redirection Settings (continued)	<ul style="list-style-type: none"> <li>◆ Parity <ul style="list-style-type: none"> <li>– A parity bit can be sent with the data bits to detect some transmission errors.</li> <li>– Even: parity bit is 0 if the num of 1's in the data bits is even.</li> <li>– Odd: parity bit is 0 if num of 1's in the data bits is odd.</li> <li>– Mark: parity bit is always 1. Space: Parity bit is always 0.</li> <li>– Mark and Space Parity do not allow for error detection.</li> <li>– Options available: None, Even, Odd, Mark, Space. Default setting is <b>None</b>.</li> </ul> </li> <li>◆ Stop Bits <ul style="list-style-type: none"> <li>– Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.</li> <li>– Options available: 1, 2. Default setting is <b>1</b>.</li> </ul> </li> <li>◆ Flow Control <ul style="list-style-type: none"> <li>– Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</li> <li>– Options available: None, Hardware RTS/CTS. Default setting is <b>None</b>.</li> </ul> </li> <li>◆ VT-UTF8 Combo Key Support <ul style="list-style-type: none"> <li>– Enable/Disable the VT-UTF8 Combo Key Support.</li> <li>– Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>◆ Recorder Mode <ul style="list-style-type: none"> <li>– When this mode enabled, only texts will be send. This is to capture Terminal data.</li> <li>– Options available: Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>◆ Resolution 100x31 <ul style="list-style-type: none"> <li>– Enable/Disable extended terminal resolution.</li> <li>– Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>◆ Putty KeyPad <ul style="list-style-type: none"> <li>– Selects Function Key and KeyPad on Putty.</li> <li>– Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400. Default setting is <b>VT100</b>.</li> </ul> </li> </ul>

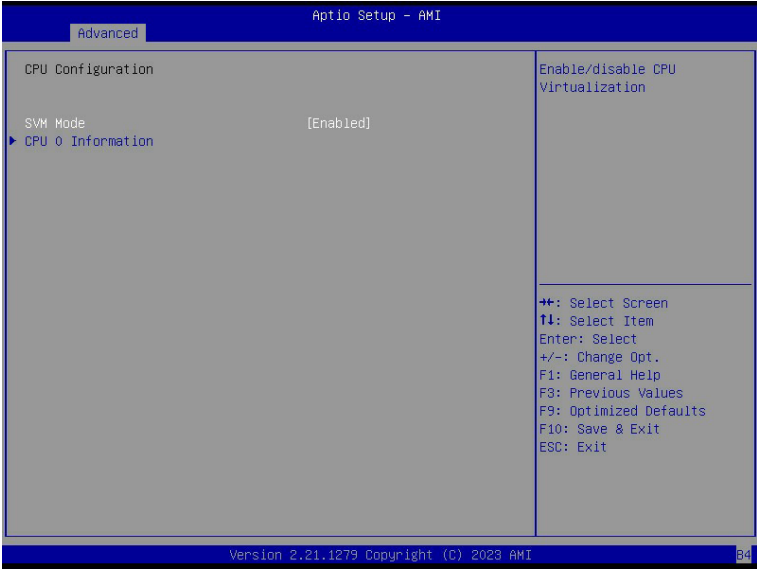
Parameter	Description
Legacy Console Redirection	
Legacy Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> <li>◆ Redirection COM Port <ul style="list-style-type: none"> <li>– Selects a COM port for Legacy serial redirection.</li> <li>– Default setting is <b>COM1/SOL</b>.</li> </ul> </li> <li>◆ Resolution <ul style="list-style-type: none"> <li>– Selects the number of rows and columns used in Console Redirection for legacy OS support.</li> <li>– Options available: 80x24, 80x25. Default setting is <b>80x24</b>.</li> </ul> </li> <li>◆ Redirect After POST <ul style="list-style-type: none"> <li>– When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS.</li> <li>– Options available: Always Enable, BootLoader. Default setting is <b>Always Enable</b>.</li> </ul> </li> </ul>
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection <sup>(Note)</sup>	<p>EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management.</p> <p>Options available: Disabled, Enabled. Default setting is <b>Disabled</b>.</p>
Serial Port for Out-of-Band EMS Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <p><b>Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled.</b></p> <ul style="list-style-type: none"> <li>◆ Out-of-Band Mgmt Port <ul style="list-style-type: none"> <li>– Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port.</li> <li>– Default setting is <b>COM1/SOL</b>.</li> </ul> </li> <li>◆ Terminal Type <ul style="list-style-type: none"> <li>– Selects a terminal type to be used for console redirection.</li> <li>– Options available: VT100, VT100+, ANSI, VT-UTF8. Default setting is <b>ANSI</b>.</li> </ul> </li> <li>◆ Bits per second <ul style="list-style-type: none"> <li>– Selects the transfer rate for console redirection.</li> <li>– Options available: 9600, 19200, 57600, 115200. Default setting is <b>115200</b>.</li> </ul> </li> </ul>

(Note) Advanced items prompt when this item is defined.

Parameter	Description
Serial Port for Out-of-Band EMS Console Redirection Settings(continued)	<ul style="list-style-type: none"><li>◆ Flow Control<ul style="list-style-type: none"><li>– Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</li><li>– Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is <b>None</b>.</li></ul></li></ul>



## 2-2-8 CPU Configuration



Parameter	Description
SVM Mode	Enable/Disable the CPU Virtualization. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
CPU 0 Information	Press [Enter] to view the memory information related to CPU 0.

2-2-9 SIO



Parameter	Description
Lock Legacy Resources	Options available: Disabled, Enabled. Default setting is <b>Disabled</b> .

## 2-2-10 PCI Subsystem Settings

Aptio Setup - AMI

Advanced

PCI Bus Driver Version	A5.01.30	▲ Change PCIe_7 PCIe lanes.
PCI_E_7	[Auto]	
PCI_E_7 I/O ROM	[Enabled]	
PCI_E_7 Link Speed	[Auto]	
SLot1 & SLot2	[Auto]	
SLot1 & SLot2 I/O ROM	[Enabled]	
SLot1 & SLot2 Link Speed	[Auto]	
PCI_E_6	[Auto]	
PCI_E_6 I/O ROM	[Enabled]	
PCI_E_6 Link Speed	[Auto]	
SLot3 & SLot4	[Auto]	
SLot3 & SLot4 I/O ROM	[Enabled]	
SLot3 & SLot4 Link Speed	[Auto]	
PCI_E_5	[Auto]	
PCI_E_5 I/O ROM	[Enabled]	
PCI_E_5 Link Speed	[Auto]	
U2_PO_G3	[Auto]	
U2_PO_G3 I/O ROM	[Enabled]	
U2_PO_G3 Link Speed	[Auto]	

▲ Select Screen  
 T1: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F3: Previous Values  
 F9: Optimized Defaults  
 F10: Save & Exit  
 ESC: Exit

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Aptio Setup - AMI

Advanced

PCI_E_6	[Auto]	▲ Enables or Disables PCI Express Device Relaxed Ordering.
PCI_E_6 I/O ROM	[Enabled]	
PCI_E_6 Link Speed	[Auto]	
SLot3 & SLot4	[Auto]	
SLot3 & SLot4 I/O ROM	[Enabled]	
SLot3 & SLot4 Link Speed	[Auto]	
PCI_E_5	[Auto]	
PCI_E_5 I/O ROM	[Enabled]	
PCI_E_5 Link Speed	[Auto]	
U2_PO_G3	[Auto]	
U2_PO_G3 I/O ROM	[Enabled]	
U2_PO_G3 Link Speed	[Auto]	
Onboard LAN Controller	[Enabled]	
Onboard LAN1 I/O ROM	[Enabled]	
Onboard LAN2 I/O ROM	[Enabled]	
PCI Devices Common Settings:		
Above 4G Decoding	[Enabled]	
SR-IOV Support	[Enabled]	
Relaxed Ordering	[Enabled]	

▲ Select Screen  
 T1: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F3: Previous Values  
 F9: Optimized Defaults  
 F10: Save & Exit  
 ESC: Exit

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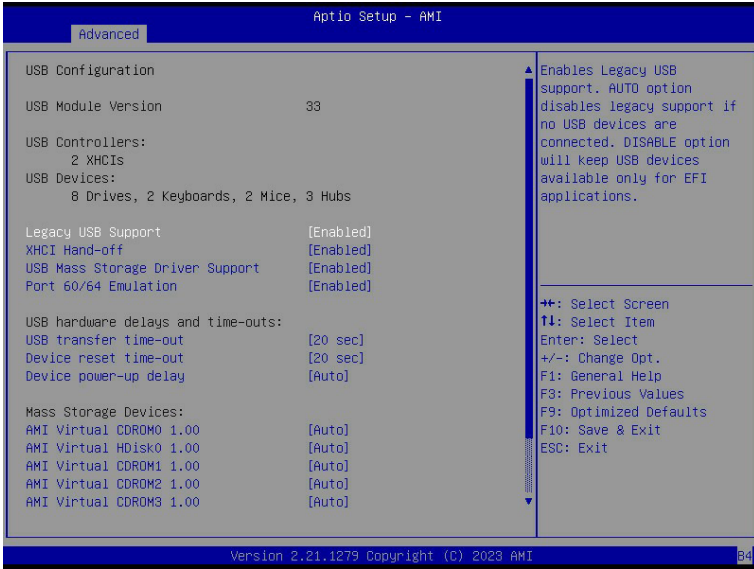
Parameter	Description
PCI Bus Driver Version	Displays the PCI Bus Driver version information.
PCIE_# <sup>(Note1)</sup>	Change the PCIe lanes. Options available: Disabled, Auto, x16, x8x8, x8x4x4, x4x4x8, x4x4x4x4. Default setting is <b>Auto</b> .
SLOT #_I/O ROM <sup>(Note1)</sup>	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
SLOT #_Link Speed <sup>(Note1)</sup>	Configure PCIe max link speed. Options available: Auto, Gen4, Gen3, Gen2, Gen1. Default setting is <b>Auto</b> .
U2_P0_G0/1/2 Lanes <sup>(Note2)</sup>	Change MCIO PCIe lanes. Options available: Disabled, Auto, x8, x16, x4x4, x8x8, x8x4x4, x4x4x8, x4x4x4x4. Default setting is <b>Auto</b> .
U2_P0_G3 Lanes	Change MCIO U2_P0_G3 PCIe lanes. Options available: Disabled, SATA, x16, x8x8, x8x4x4, x4x4x8, x4x4x4x4. Default setting is <b>SATA</b> .
U2_P0_G0/1/2/3 I/O ROM <sup>(Note2)</sup>	When enabled, this setting will initialize the device expansion ROM for the related devices. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
U2_P0_G0/1/2/3 Link Speed <sup>(Note2)</sup>	Configure MCIO PCIe max link speed. Options available: Auto, Gen4, Gen3, Gen2, Gen1. Default setting is <b>Auto</b> .
Onboard LAN Controller <sup>(Note3)</sup>	Enable/Disable the onboard LAN devices. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
Onboard LAN# I/O ROM <sup>(Note3)</sup>	Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
<b>PCI Devices Common Settings</b>	
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
Relaxed Ordering	Enable/Disable PCI express device relaxed ordering. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .

(Note1) This section is dependent on the available PCIe Slot.

(Note2) This section is dependent on the available MCIO connector.

(Note3) This section is dependent on the available LAN controller.

## 2-2-11 USB Configuration

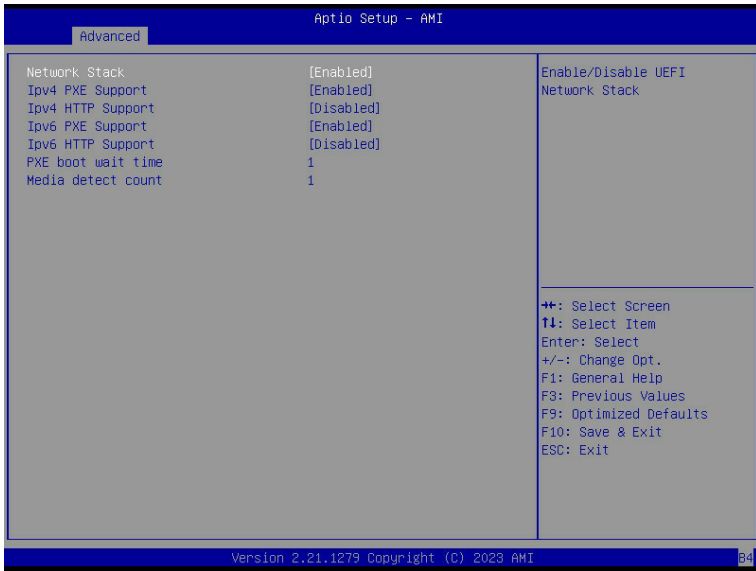


Parameter	Description
USB Configuration	
USB Module Version	Displays the USB module version information.
USB Controllers	Displays the supported USB controllers.
USB Devices:	Displays the USB devices connected to the system.
Legacy USB Support	Enable/Disable the Legacy USB support function. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications. Options available: Enabled, Disabled, Auto. Default setting is <b>Enabled</b> .
XHCI Hand-off	Enable/Disable the XHCI Hand-off support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
USB Mass Storage Driver Support <sup>(Note)</sup>	Enable/Disable the USB Mass Storage Driver Support. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
USB hardware delays and time-outs	
USB transfer time-out	Selects the time-out value for USB Control/Bulk/Interrupt transfers. Options available: 1 sec, 5 sec, 10 sec, 20 sec. Default setting is <b>20 sec</b> .

(Note) This item is present only if you attach USB devices.

Parameter	Description
Device reset time-out	Selects the time-out value during a USB mass storage device reset. Options available: 10 sec, 20 sec, 30 sec, 40 sec. Default setting is <b>20 sec</b> .
Device power-up delay	Maximum time the device will take before it properly reports itself to the Host Controller. "Auto" uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub descriptor. Options available: Auto, Manual. Default setting is <b>Auto</b> .

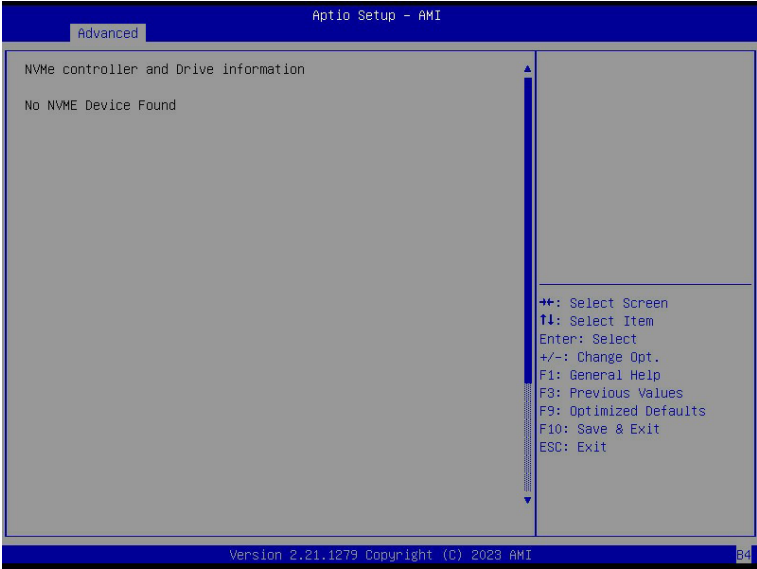
## 2-2-12 Network Stack Configuration



Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Ipv4 PXE Support <sup>(Note)</sup>	Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Ipv4 HTTP Support <sup>(Note)</sup>	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Ipv6 PXE Support <sup>(Note)</sup>	Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Ipv6 HTTP Support <sup>(Note)</sup>	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
PXE boot wait time <sup>(Note)</sup>	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count <sup>(Note)</sup>	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

(Note) This item appears when **Network Stack** is set to **Enabled**.

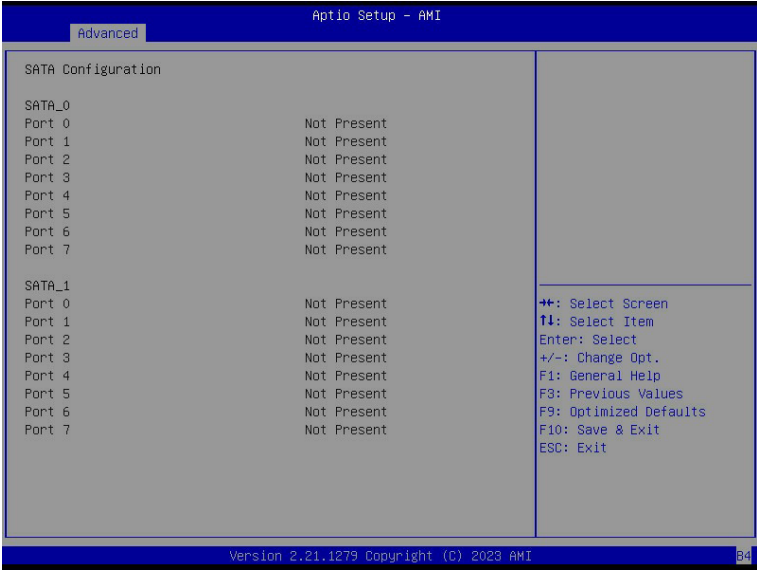
## 2-2-13 NVMe Configuration



Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system.
NVMe LED Control	Enable/Disable NVMe LED Control. Options available: System Default, Disabled, Enabled. Default setting is <b>Enabled</b> .

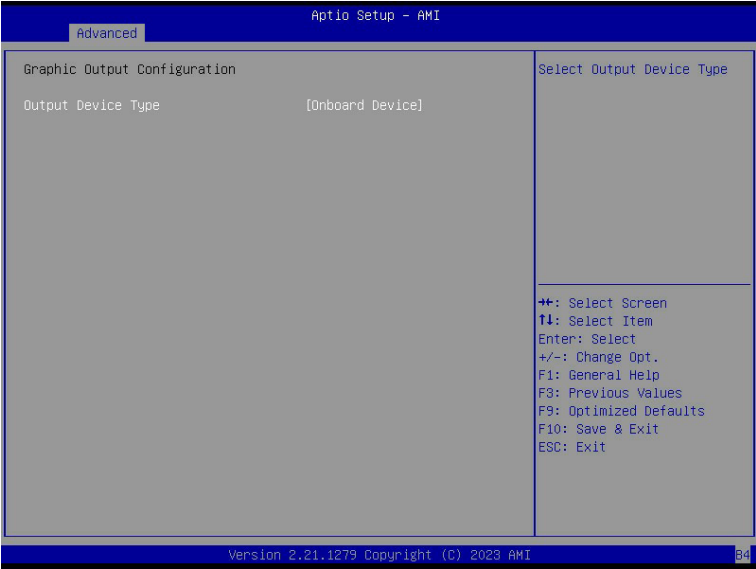


## 2-2-14 SATA Configuration



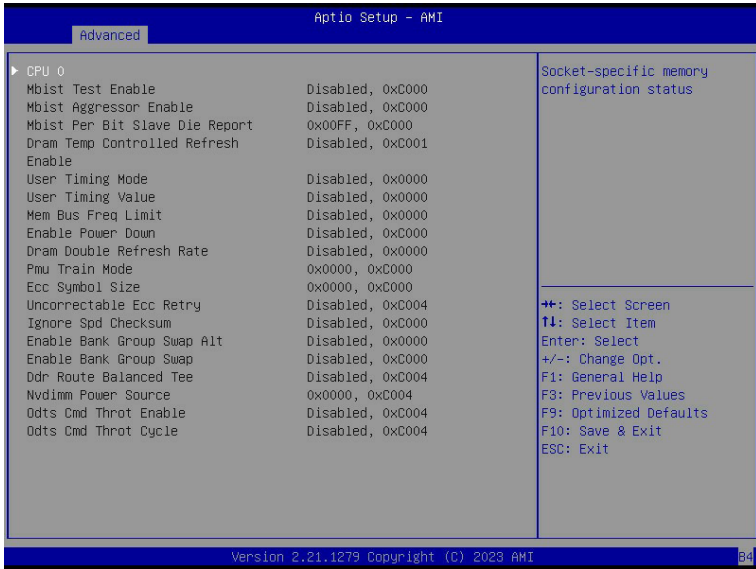
Parameter	Description
SATA Configuration	Displays the installed HDD devices information. System will automatically detect HDD type.

## 2-2-15 Graphic Output Configuration



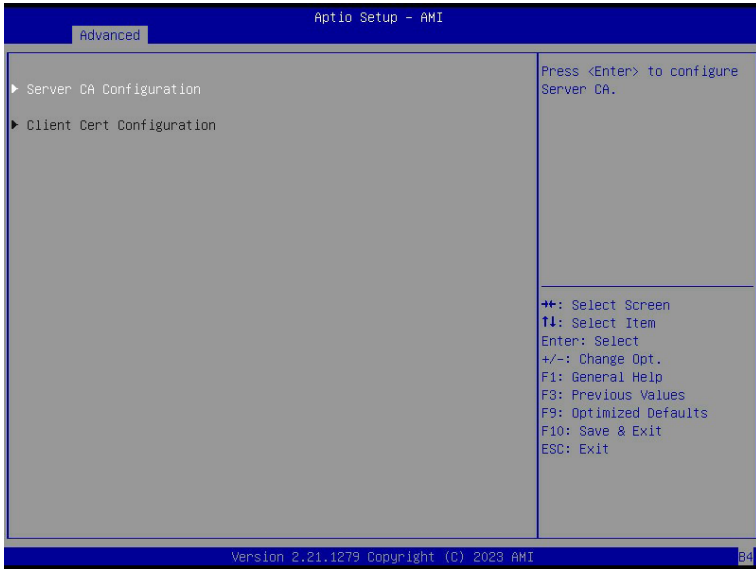
Parameter	Description
Output Device Type	Selects output device type. Options available: First loaded Device, Onboard Device, External Device, Specific Device. Default setting is <b>Onboard Device</b> .

## 2-2-16 AMD Mem Configuration Status



Parameter	Description
CPU 0	Press [Enter] to view the memory configuration status related to CPU 0.

## 2-2-17 Tls Auth Configuration



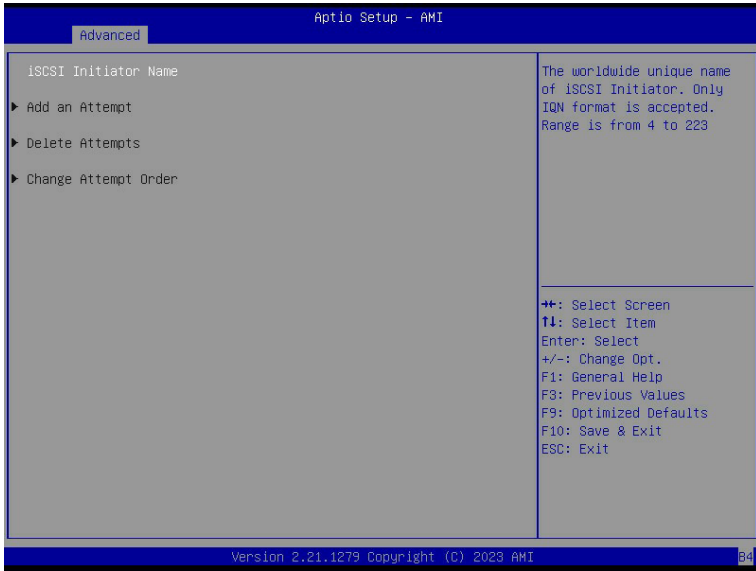
Parameter	Description
Server CA Configuration	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> <li>◆ Enroll Cert                             <ul style="list-style-type: none"> <li>– Press [Enter] to enroll a certificate                                     <ul style="list-style-type: none"> <li>• Enroll Cert Using File</li> <li>• Cert GUID</li> </ul> </li> <li>Input digit character in 1111111-2222-3333-4444-1234567890ab format.</li> <li>– Commit Changes and Exit</li> <li>– Discard Changes and Exit</li> </ul> </li> <li>◆ Delete Cert</li> </ul>
Client Cert Configuration	Press [Enter] for configuration of advanced items.

## 2-2-18 RAM Disk Configuration



Parameter	Description
Disk Memory Type	Specifies the type of memory to use from available memory pool in system to create a disk. Options available: Boot Service Data, Reserved. Default setting is <b>Boot Service Data</b> .
Create Raw	Creates a raw RAM disk. <ul style="list-style-type: none"> <li>◆ Size (Hex) <ul style="list-style-type: none"> <li>– Input a valid RAM disk size that should be multiple of the RAM disk block size.</li> </ul> </li> <li>◆ Create &amp; Exit</li> <li>◆ Discard &amp; Exit</li> </ul>
Create from file	Creates a RAM disk from a given file.
Created RAM disk list	
Remove selected RAM disk(s)	Selects the RAM disk(s) to remove.

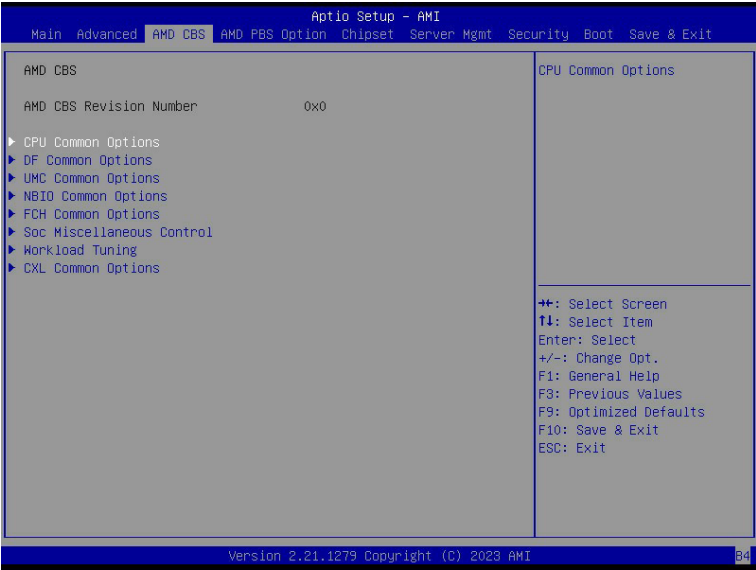
## 2-2-19 iSCSI Configuration



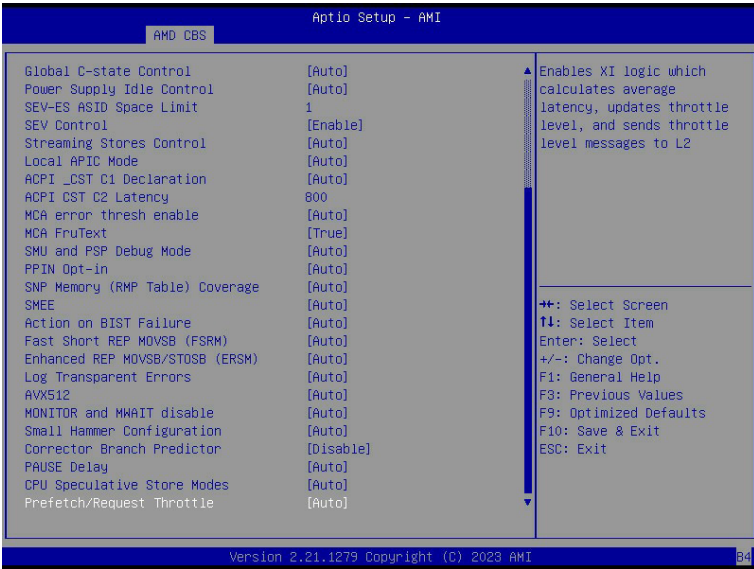
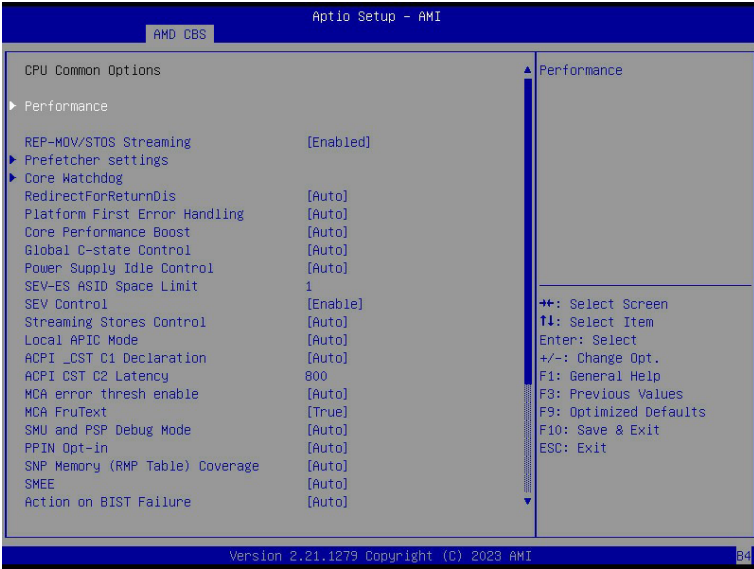
Parameter	Description
iSCSI Initiator Name	Press [Enter] and name iSCSI Initiator. Only IQN format is accepted. Range: from 4 to 223
Add an Attempt	Press [Enter] to configure advanced items.
Delete Attempts	Press [Enter] to configure advanced items.
Change Attempt Order	Press [Enter] to configure advanced items.

## 2-3 AMD CBS Menu

AMD CBS menu displays submenu options for configuring the CPU-related information that the BIOS automatically sets. Select a submenu item, then press [Enter] to access the related submenu screen.



## 2-3-1 CPU Common Options

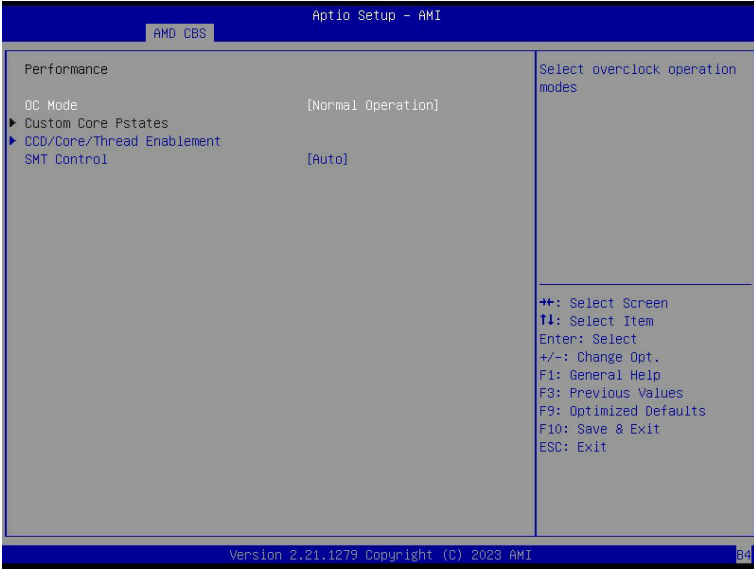




Parameter	Description
CPU Common Options	
Performance	Press [Enter] for configuration of advanced items.
REP-MOV/STOS Streaming	Allow REP-MOV/STOS to use non-caching streaming stores for large sizes. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
Prefetcher settings	Press [Enter] for configuration of advanced items.
Core Watchdog	Press [Enter] for configuration of advanced items.
RedirectForReturnDis	From a workaround for GCC/C000005 issue for XV Core on CZ A0, setting MSRC001_1029 Decode Configuration (DE_CFG) bit 14 [DecfgNoRdrctForReturns] to 1. Options available: Auto, 1, 0. Default setting is <b>Auto</b> .
Platform First Error Handling	Enable/Disable PFEH, cloak individual banks, and mask deferred error interrupts from each bank. Options available: Enabled, Disabled, Auto. Default setting is <b>Auto</b> .
Core Performance Boost	Enable/Disable the Core Performance Boost function. Options available: Disabled, Auto. Default setting is <b>Auto</b> .
Global C-state Control	Controls the IO based C-state generation and DF C-states. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Power Supply Idle Control	Configures the Power Supply Idle Control. Options available: Low Current Idle, Typical Current Idle, Auto. Default setting is <b>Auto</b> .
SEV-ES ASID Space Limit	Configures the Space limit for SEV-ES ASIDs. Default setting is <b>1</b> .
SEV Control	Enable/Disable SEV control. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Streaming Stores Control	Enable/Disable the Streaming Stores functionality. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Local APIC Mode	Sets the Local APIC Mode. Options available: Compatibility, xAPIC, x2APIC, Auto. Default setting is <b>Auto</b> .
ACPI_CST C1 Declaration	Determines whether or not to declare the C1 state to the OS.. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
MCA error thresh enable	Enable MCA error thresholding. Options available: False, True, Auto. Default setting is <b>True</b> .
MCA FruText	Enable MCA FruText. Options available: False, True. Default setting is <b>Auto</b> .
SMU and PSP Debug Mode	When this option is enabled, specific uncorrected errors detected by the PSP FW or SMU FW will hand and not reset the system. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
PPIN Opt-in	Enable/Disable the PPIN feature. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
SNP Memory (RMP Table) Coverage	Enabled: Enter system memory is covered. Options available: Disabled, Enabled, Custom, Auto. Default setting is <b>Auto</b> .

<b>Parameter</b>	<b>Description</b>
SMEE	Controls the Secure Memory Encryption Enable (SMEE) function. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
Action on BIST Failure	Action to take when a CCD BIST failure is detected. Options available: Do nothing, Down-CCD, Auto. Default setting is <b>Auto</b> .
Enhanced REP MOVSB/ STOSB (ERMSB)	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Log Transparent Errors	Enable/Disable the log Transparent errors function. Options available: Auto, Disabled, Enabled. Default setting is <b>Auto</b> .
AVX512	Enable/Disable AVX512. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
MONITOR and MWAIT disable	The MONITOR, MWAIT, MONITORX and MWAITX opcodes become invalid when enabled. Options available: Enabled, Disabled, Auto. Default setting is <b>Auto</b>
Small Hammer Configuration	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Corrector Branch Predictor	Options available: Disable, Enable. Default setting is <b>Disable</b> .
PAUSE Delay	Number a cycles thread will be idle after a PAUSE instruction. Options available: Auto, Disable, 16 cycles, 32 cycles, 64 cycles, 128 cycles. Default setting is <b>Auto</b> .
CPU Speculative Store Modes	Select the CPU speculative store modes. Options available: Balanced, More Speculative, Less Speculative, Auto. Default setting is <b>Auto</b> .

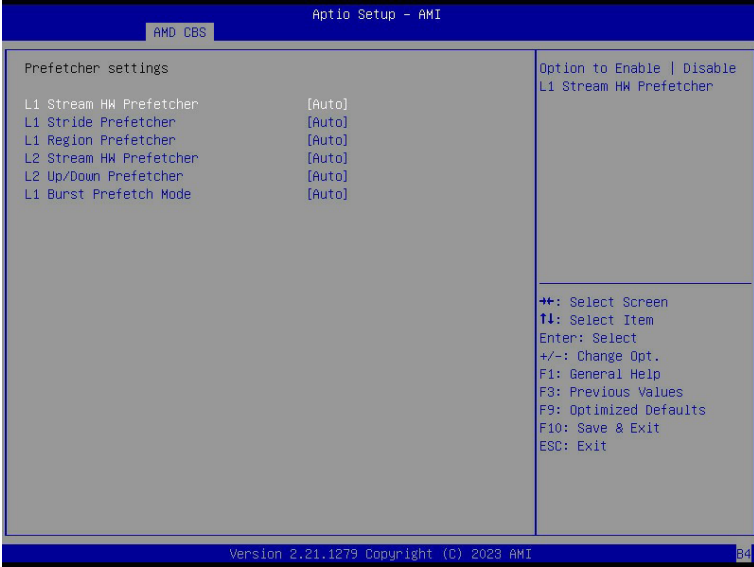
## 2-3-1-1 Performance



Parameter	Description
Performance	
OC Mode <sup>(Notes)</sup>	Options available: Normal Operation, Customized. Default setting is <b>Normal Operation</b> .
Custom Core Pstates	Allows you to accept or decline enabling Custom Core Pstates. When accepted, you can disable or customize core pstates.
CCD/Core/Thread Enablement	<p>Allows you to accept or decline enabling CCDs, processor cores and threads. When accepted, you can control the number of CCDs to be used, and the number of cores to be used.</p> <ul style="list-style-type: none"> <li>◆ CCD Control <ul style="list-style-type: none"> <li>– Options available: Auto, 2 CCDs, 4 CCDs, 6 CCDs, 8 CCDs, 10 CCDs. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ Core Control <ul style="list-style-type: none"> <li>– Options available: Auto, ONE(1+0), TWO(2+0), THREE(3+0), FOUR(4+0), FIVE(5+0), SIX(6+0), SEVEN(7+0).</li> <li>– Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
SMT Control	<p>Can be used to disable symmetric multithreading. To re-enable SMT, a POWER CYCLE is needed after select the 'Enable' option. Select 'Auto' base on BIOS PCD. (PcdAmdSmtMode) default setting.</p> <p>Options available: Disable, Enable, Auto. Default setting is <b>Enable</b>.</p>

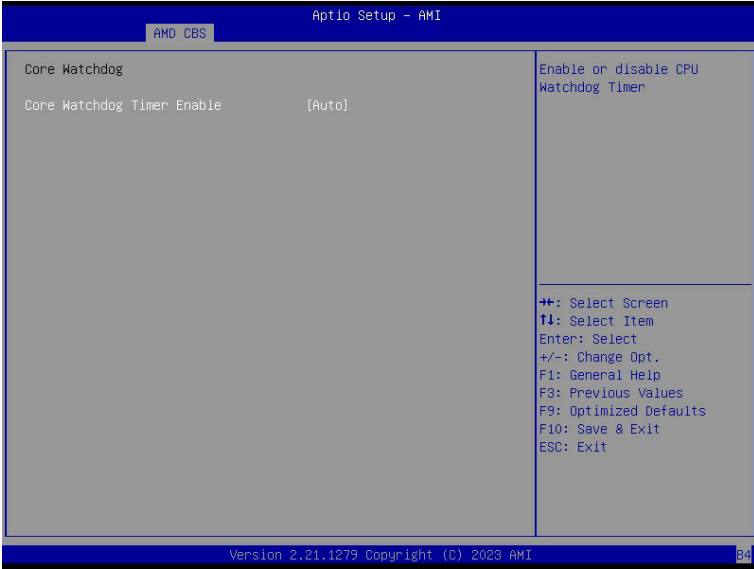
(Note) Advanced items are configurable when this item is defined.

## 2-3-1-2 Prefetcher Settings



Parameter	Description
Prefetcher settings	
L1 Stream HW Prefetcher	Enable/Disable L1 Stream HW Prefetcher. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
L1 Stride Prefetcher	Use memory access history of individual instructions to fetch additional lines when each access is a constant distance from the previous. Enable/Disable L1 Stride Prefetcher. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
L1 Region Prefetcher	Use memory access history to fetch additional lines when the data access for a given instruction tends to be followed by other data accesses. Enable/Disable L1 Region Prefetcher. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
L2 Stream HW Prefetcher	Enable/Disable L2 Stream HW Prefetcher. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
L2 Up/Down Prefetcher	Use memory access history to determine whether to fetch the next or previous line for all memory accesses. Enable/Disable L2 Up/Down Prefetcher. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
L1 Burst Prefetch Mode	Enable/Disable L1 Burst Prefetch Mode. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .

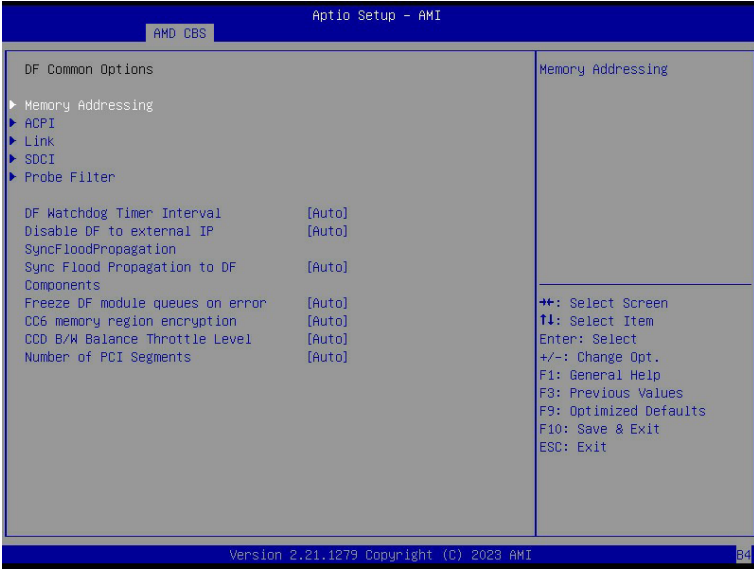
### 2-3-1-3 Core Watchdog



Parameter	Description
Core Watchdog	
Core Watchdog Timer Enable <sup>(Note)</sup>	Enable/Disable CPU Watchdog Timer. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Core Watchdog Timer Interval	Select the CPU Watchdog Timer interval. Options available: 2.681s, 1.340s, 669.41ms, 334.05ms, 166.37ms, 82.53ms, 40.61ms, 20.970ms, 10.484ms, 5.241ms, 2.620ms, 1.309ms, 654.08us, 326.4us, 162.56us, 80.64us, 39.68us, Auto. Default setting is <b>Auto</b> .

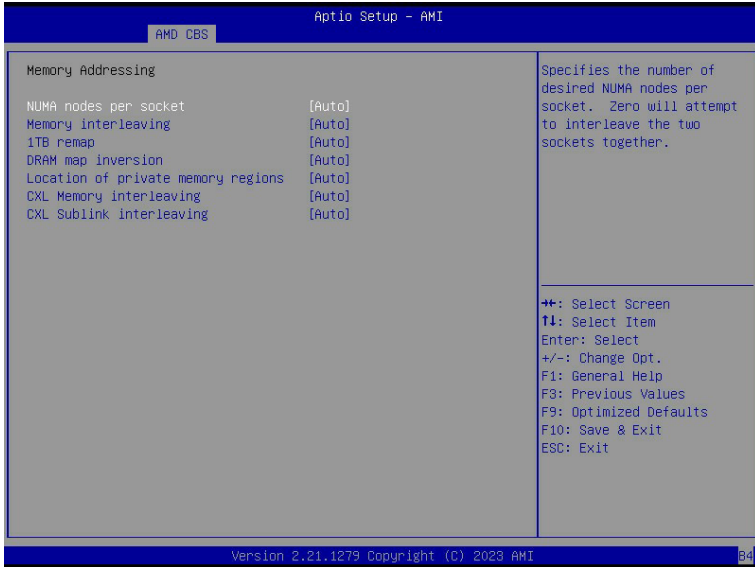
(Note) Advanced items prompt when this item is defined.

## 2-3-2 DF Common Options



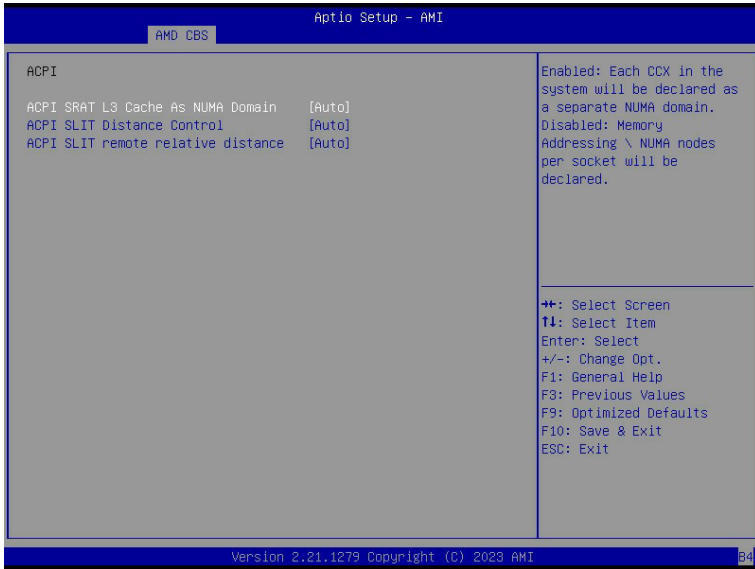
Parameter	Description
DF Common Options	
Memory Addressing	Press [Enter] for configuration of advanced items.
ACPI	Press [Enter] for configuration of advanced items.
Link	Press [Enter] for configuration of advanced items.
SDCI	Press [Enter] for configuration of advanced items.
DF Watchdog Timer Interval	Configures the Data Fabric watchdog timer interval. Options available: Auto, 41ms, 166ms, 334ms, 669ms, 1.34 seconds, 2.68 seconds, 5.36 seconds. Default setting is <b>Auto</b> .
Disable DF to external IP sync flood propagation	Enable/Disable SyncFlood to UMC & downstream slaves. Options available: Sync flood disabled, Sync flood enabled, Auto. Default setting is <b>Auto</b> .
Sync flood propagation to DF Components	Enable/Disable DF Sync Flood propagation. Options available: Sync flood disabled, Sync flood enabled, Auto. Default setting is <b>Auto</b> .
Freeze DF module queues on error	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
CC6 memory region encryption	Controls whether or not the CC6 save/restor memory is encrypted. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
CCD B/W Balance Throttle Level	Options available: Auto, Level 0, Level 1, Level 2, Level 3, Level 4. Default setting is <b>Auto</b> .

## 2-3-2-1 Memory Addressing



Parameter	Description
Memory Addressing	
NUMA nodes per socket	Specifies the number of desired NUMA nodes per socket. Options available: NPS0, NPS1, Auto. Default setting is <b>Auto</b> .
Memory interleaving	Enable/Disable the Memory interleaving feature. Options available: Disabled, Auto, Enabled. Default setting is <b>Auto</b> .
1TB remap	Enable/Disable to remap DRAM out of the space just below the 1TB boundary. The ability to remap depends on DRAM configuration, NPS, and interleaving selection, and may not always be possible. Options available: Do not remap, Attempt to remap, Auto. Default setting is <b>Auto</b> .
DRAM map inversion	Enable/Disable the DRAM map inversion function. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Location of private memory regions	Controls whether or not the private memory regions (PSP, SMU and CC6) are at the top of DRAM or distributed. Options available: Distributed, Consolidated, Auto. Default setting is <b>Auto</b> .
CXL Memory interleaving	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
CXL Sublink interleaving	Options available: Enable, Disable, Auto. Default setting is <b>Auto</b> .

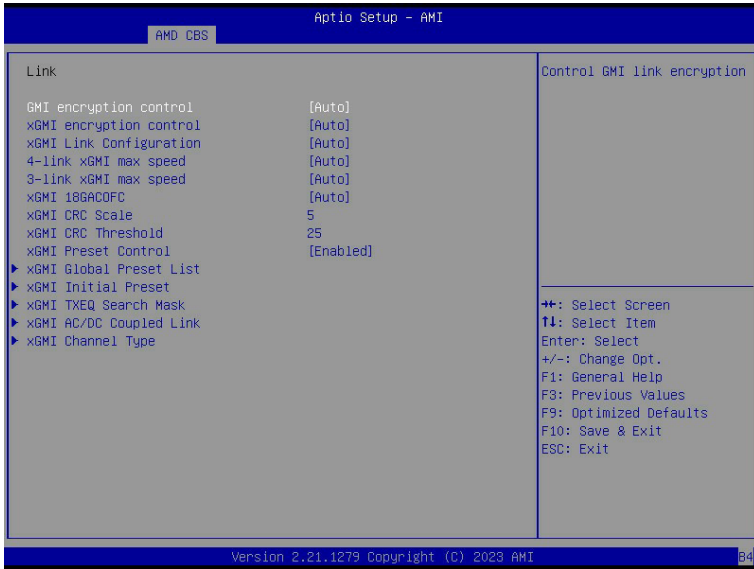
## 2-3-2-2 ACPI



Parameter	Description
ACPI	
ACPI SRAT L3 Cache As NUMA Domain	Enable/Disable report each L3 cache as a NUMA Domain to the OS. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
ACPI SLIT Distance Control	Determines how the SLIT distances are declared. Options available: Manual, Auto. Default setting is <b>Auto</b> .
ACPI SLIT remote relative distance	Sets the remote socket distance for 2P systems as near (2.8) or far (3.2). Options available: Near, Far, Auto. Default setting is <b>Auto</b> .



## 2-3-2-3 Link

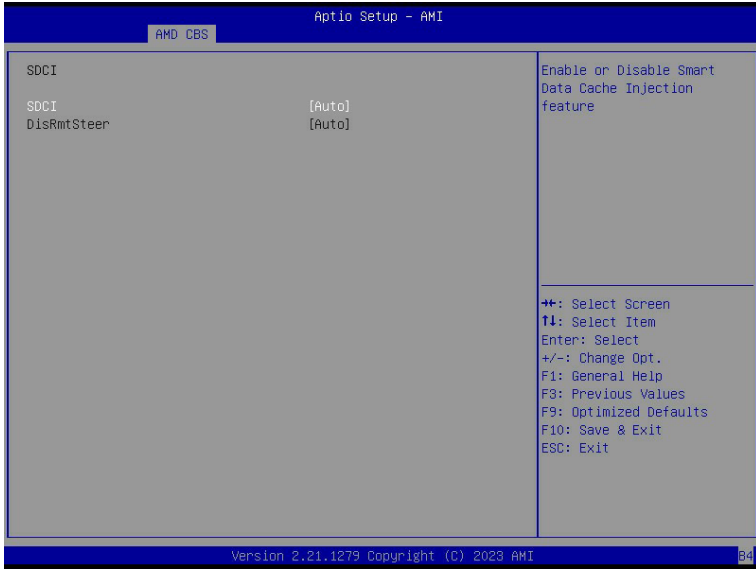


Parameter	Description
GMI encryption control	Enable/Disable GMI link encryption. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
xGMI encryption control	Enable/Disable xGMI link encryption. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
xGMI Link Configuration	Configures the number of xGMI2 links used on a multi-socket system. Options available: Auto, 3 xGMI Links, 4 xGMI Links. Default setting is <b>Auto</b> .
4-link xGMI max speed	Specifies the max speed of 4-link xGMI. Options available: 12Gbps, 16Gbps, 17Gbps, 18Gbps, 20Gbps, 22Gbps, 23Gbps, 24Gbps, 25Gbps, 26Gbps, 27Gbps, 30Gbps, 32Gbps, Auto. Default setting is <b>Auto</b> .
3-link xGMI max speed	Specifies the max speed of 3-link xGMI. Options available: 12Gbps, 16Gbps, 17Gbps, 18Gbps, 20Gbps, 22Gbps, 23Gbps, 24Gbps, 25Gbps, 26Gbps, 27Gbps, 30Gbps, 32Gbps, Auto. Default setting is <b>Auto</b> .
xGMI 18GACOFC	Configures xGMI 18GACOFC. Options available: Auto, Enable, Disable. Default setting is <b>Auto</b> .
xGMI CRC Scale	Configures leaky bucket scale for xGMI and WAFL CRC errors. Every scale milliseconds an error will leak from the CRC counter. Default setting is 7.
xGMI CRC Threshold	Configures leaky bucket threshold for xGMI and WAFL CRC errors. If link CRC counter exceeds this threshold, an error will be logged. Default setting is 25.
xGMI Preset Control	Enable/Disable xGMI Preset control. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .

Parameter	Description
xGMI Global Preset List	Press [Enter] to configure the xGMI Preset list.
xGMI Initial Preset	Press [Enter] to configure the xGMI Initial Preset CPU0/1 link.
xGMI TXEQ Search Mask	Press [Enter] to configure the xGMI TXEQ Search Mask CPU0/1 link.
xGMI AC/DC Coupled Link	Press [Enter] to configure the xGMI AC/DC Coupled link. ♦ xGMI AC/DC Coupled Link Control <sup>(Note)</sup> – Options available: Manual, Auto. Default setting is <b>Auto</b> .
xGMI Channel Type	Press [Enter] to configure the xGMI Channel Type. ♦ xGMI Channel Type Control <sup>(Note)</sup> – Options available: Manual, Auto. Default setting is <b>Auto</b> .

(Note) Advanced items prompt when this item is defined.

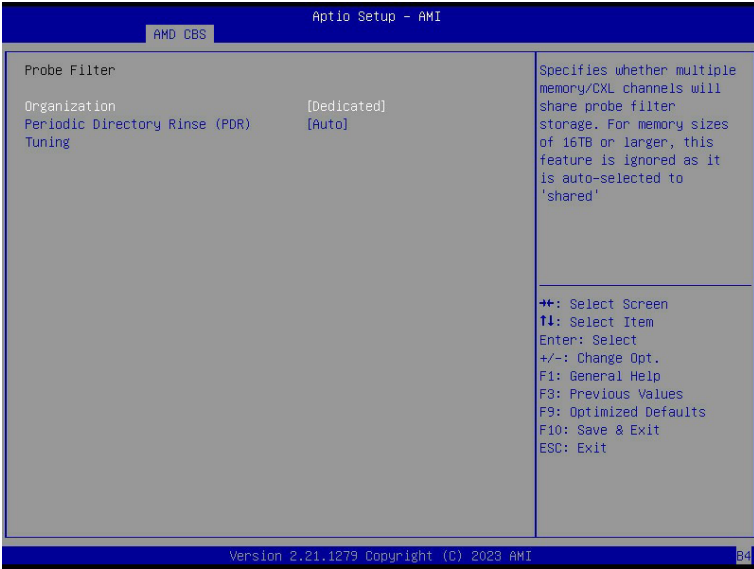
## 2-3-2-4 SDCI



Parameter	Description
SDCI <sup>(Note)</sup>	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
DisRmSteer	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .

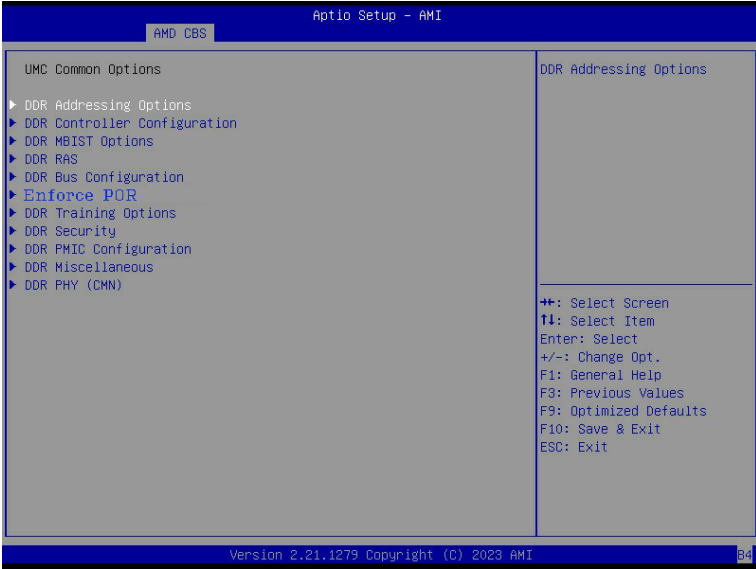
(Note) Advanced items prompt when this item is defined.

### 2-3-2-5 Probe Filter



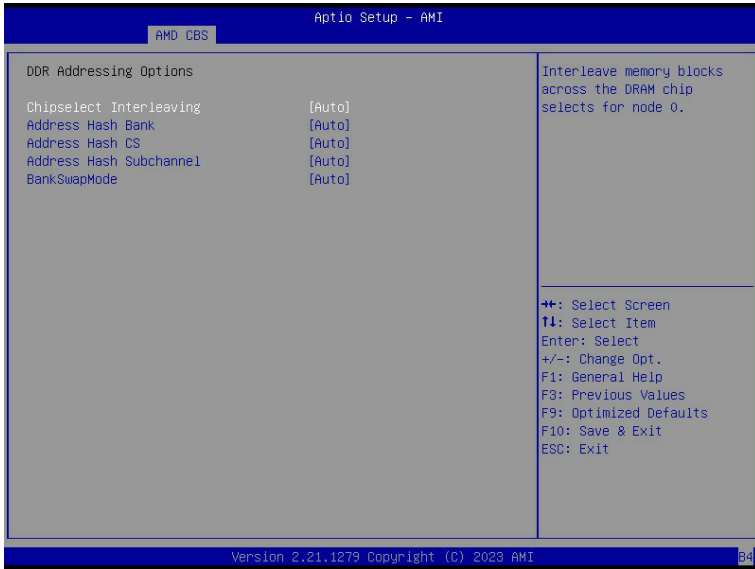
Parameter	Description
Organization	Specifies whether multiple memory/CXL channels will share probe filter storage. Options available: Auto, Dedicated, Shared. Default setting is <b>Dedicated</b> .
Periodic Directory Rinse (PDR)	Controls PDR settings that may impact performance by workload and/or processor.
Tuning	Options available: Memory-Sensitive, Cache-Bound, Neutral, Auto. Default setting is <b>Auto</b> .

### 2-3-3 UMC Common Options



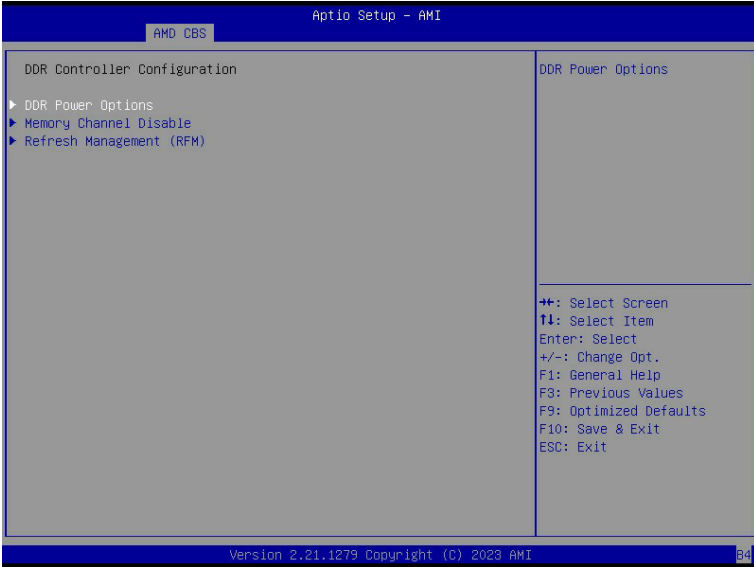
Parameter	Description
UMC Common Options	
DDR Addressing Options	Press [Enter] for configuration of advanced items.
DDR Controller Configuration	Press [Enter] for configuration of advanced items.
DDR MBIST Options	Press [Enter] for configuration of advanced items.
DDR RAS	Press [Enter] for configuration of advanced items.
DDR Bus Configuration	Press [Enter] for configuration of advanced items.
Enforce POR	Press [Enter] for configuration of advanced items.
DDR Training Options	Press [Enter] for configuration of advanced items.
DDR Security	Press [Enter] for configuration of advanced items.
DDR PMIC Configuration	Press [Enter] for configuration of advanced items.
DDR Miscellaneous	Press [Enter] for configuration of advanced items.
DDR PHY (CMN)	Press [Enter] for configuration of advanced items.

## 2-3-3-1 DDR Addressing Options



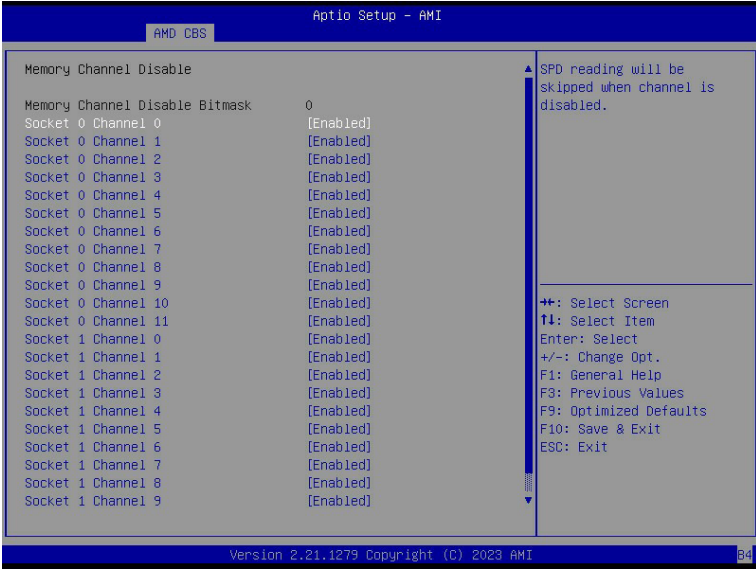
Parameter	Description
DDR Addressing Options	
Chipselect Interleaving	Interleaves memory blocks across the DRAM chip selects for node 0. Options available: Disabled, Auto. Default setting is <b>Auto</b> .
Address Hash Bank	Enable or disable bank addressing hashing. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Address Hash CS	Enable or disable CS addressing hashing. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
Address Hash Rm	Enable or disable RM addressing hashing. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
Address Hash Subchannel	Enable or disable sub-channel addressing hashing. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
Bank SwapMode	Options available: Auto, Disabled, Swap CPU. Default setting is <b>Auto</b> .

### 2-3-3-2 DDR Controller Configuration



Parameter	Description
DDR Cotroller Configuration	
DDR Power Options	Press [Enter] for configuration of advanced items.
Memory Channel Disable	Press [Enter] for configuration of advanced items.
Refresh Management (RFM)	Press [Enter] for configuration of advanced items.

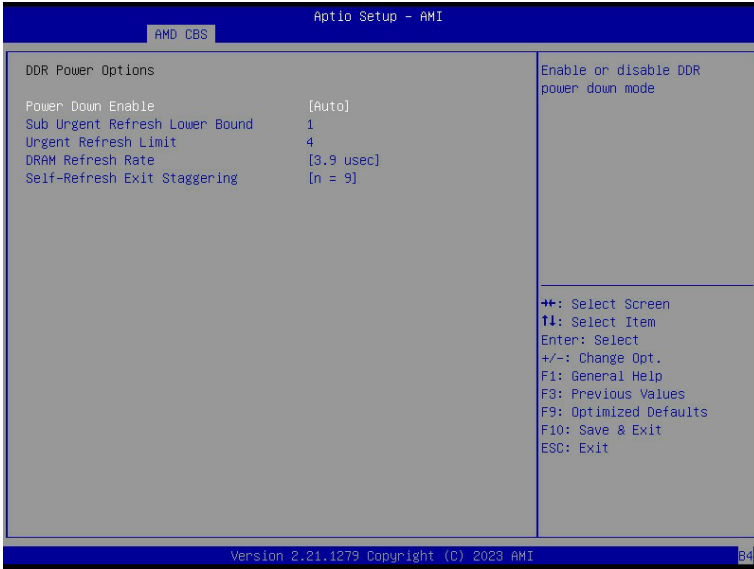
### 2-3-3-2-1 Memory Channel Disable



Parameter	Description
Memory Channel Disable	
Memory Channel Disable Bitmask	
CPU0/CPU1 Channel_#	Press [Enter] to enable/disable specific memory channel.

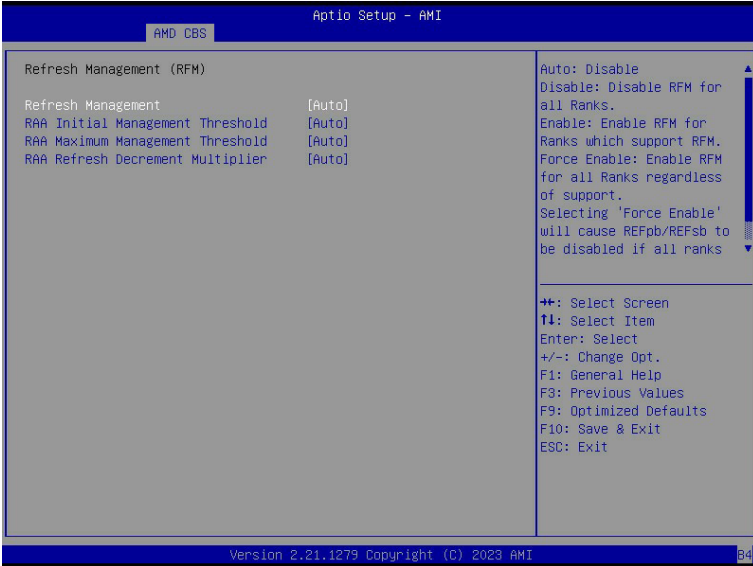


## 2-3-3-2-2 DDR Power Options



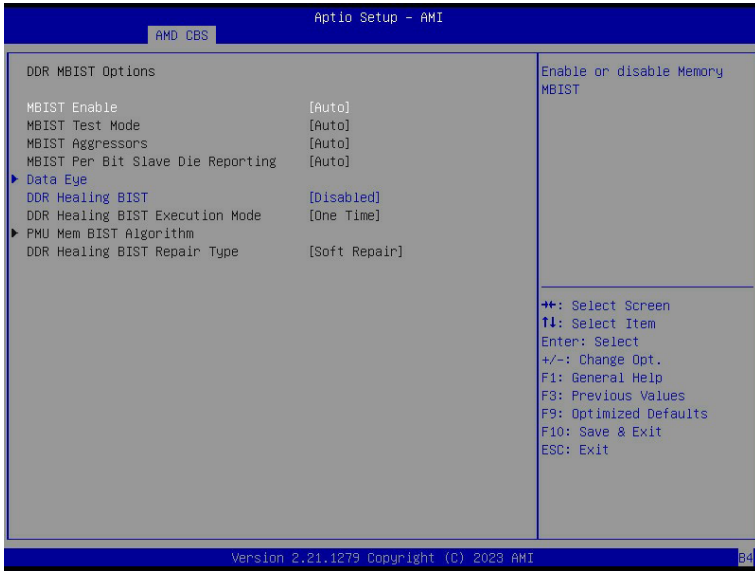
Parameter	Description
DDR Power Options	
Power Down Enable	Enable or disable DDR power down mode. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Sub Urgent Refresh Lower Bound	Specifies the stored refresh limit required to enter sub-urgent refresh mode.
Urgent Refresh Limit	Specifies the stored refresh limit required to enter urgent refresh mode.
DRAM Refresh Rate	DRAM refresh rate: 1.95us or 3.9us. Options available: 3.9 usec, 1.95usec. Default setting is <b>3.9 usec</b> .
Self-Refresh Exit Staggering	Options available: Disabled, n=1~9. Default setting is <b>n=9</b> .

### 2-3-3-2-3 Refresh Management (RFM)



Parameter	Description
Refresh Management (RFM)	
Refresh Management	Configure Refresh Management. Options available: Enable, Disable, Auto, Force Enable. Default setting is <b>Auto</b> .
RAA Initial Management Threshold	Override Rolling Accumulated ACT Initial Management Threshold. Options available: 32, 40, 48, 56, 64, 72, 80, Auto. Default setting is <b>Auto</b> .
RAA Maximum Management Threshold	Override Rolling Accumulated ACT Maximum Management Threshold. Options available: 3X, 4X, 5X, 6X, Auto. Default setting is <b>Auto</b> .
RAA Refresh Decrement Multiplier	Override RAA Refresh Decrement Multiplier. Options available: 0.5, 1, Auto. Default setting is <b>Auto</b> .

## 2-3-3-3 DDR MBIST Options



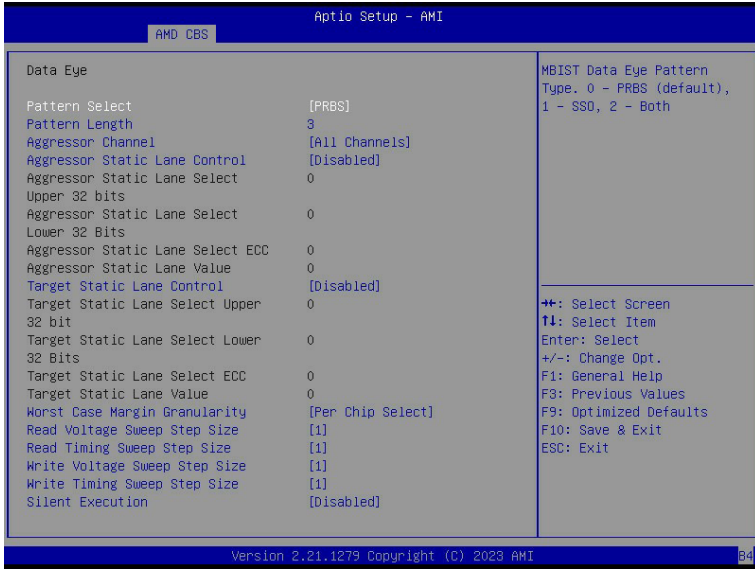
Parameter	Description
DDR MBIST Options	
MBIST Enable	Enable/Disable the Memory MBIST function. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
MBIST Test Mode <sup>(Note1)</sup>	Selects MBIST Test Mode. <b>Interface Mode:</b> Tests Single and Multiple CS transactions and Basic Connectivity. <b>Data Eye Mode:</b> Measures Voltage vs. Timing. Options available: Auto, Both, Interface Mode, Data Eye Mode. Default setting is <b>Auto</b> .
MBIST Aggressors <sup>(Note1)</sup>	Enable/Disable MBIST Aggressor test. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
MBIST Per Bit Slave Die Reporting <sup>(Note1)</sup>	Enable/Disable to report 2D data eye results in ABL log for each DQ, Chipselect, and Channel. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
Data Eye	Press [Enter] to configure advanced items.
Memory Healing BIST	Enable/Disable memory healing BIST. Options available: Disabled, PMU Mem BIST, Self-Healing Mem BIST, PMU and Self-Healing Mem BIST. Default setting is <b>Disabled</b> .

(Note1) This item appears when **MBIST Enable** is set to **Enabled**.

Parameter	Description
DDR Healing BIST Execution Mode <sup>(Note2)</sup>	Options available: One Time, Every boot. Default setting is <b>One Time</b> .
PMU Mem BIST Algorithm <sup>(Note2)</sup>	Press [Enter] to enable/disable PMU Mem BIST Algorithm.
DDR Healing BIST Repair Type <sup>(Note2)</sup>	For DRAM errors found in the BIOS memory BIST select the repair type. Options available: Soft Repair, Hard Repair, No Rrepairs -Test only. Default setting is <b>Soft Repair</b> .

(Note2) This item appears when **DDR Healing BIST** is defined.

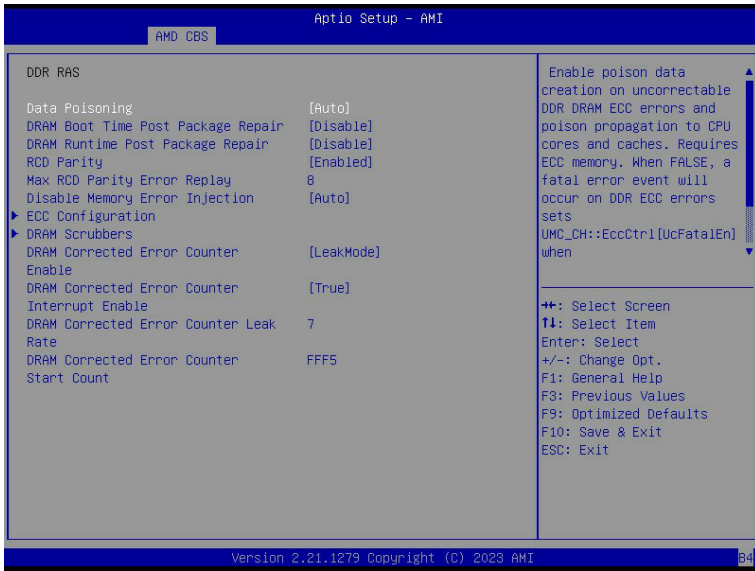
## 2-3-3-3-1 Data Eye



Parameter	Description
Data Eye	
Pattern Select	Options available: PRBS, SSO, Both. Default setting is <b>PRBS</b> .
Pattern Length	Determines the pattern length. The possible options are N=3....12.
Aggressor Channel	This item helps read the aggressors channels. Options available: One Sub-Channel, Half Channels, All Channels. Default setting is <b>All Channels</b> .
Aggressor Static Lane Control	Enable/Disable the Aggressor Static Lane Control function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Aggressor Static Lane Select Upper 32 bits	This item is configurable when <b>Aggressor Static Lane Control</b> is set to <b>Enabled</b> .
Aggressor Static Lane Select Lower 32 bits	This item is configurable when <b>Aggressor Static Lane Control</b> is set to <b>Enabled</b> .
Aggressor Static Lane Select ECC	This item is configurable when <b>Aggressor Static Lane Control</b> is set to <b>Enabled</b> .
Aggressor Static Lane Value	This item is configurable when <b>Aggressor Static Lane Control</b> is set to <b>Enabled</b> .
Target Static Lane Control	Enable/Disable the Target Static Lane Control function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .

Parameter	Description
Target Static Lane Select Upper 32 bits	This item is configurable when <b>Target Static Lane Control</b> is set to <b>Enabled</b> .
Target Static Lane Select Lower 32 bits	This item is configurable when <b>Target Static Lane Control</b> is set to <b>Enabled</b> .
Target Static Lane Select ECC	This item is configurable when <b>Target Static Lane Control</b> is set to <b>Enabled</b> .
Target Static Lane Value	This item is configurable when <b>Target Static Lane Control</b> is set to <b>Enabled</b> .
Worst Case Margin Granularity	Configures Worst Case Margin Granularity. Options available: Per Chip Select, Per Nibble. Default setting is <b>Per Chip Select</b> .
Read Voltage Sweep Step Size	Configures the step size for read Data Eye voltage sweep. Options available: 1, 2, 4. Default setting is 1.
Read Timing Sweep Step Size	Configures the step size for read Data Eye timing sweep. Options available: 1, 2, 4. Default setting is 1.
Write Voltage Sweep Step Size	Configures the step size for write Data Eye voltage sweep. Options available: 1, 2, 4. Default setting is 1.
Write Timing Sweep Step Size	Configures the step size for write Data Eye timing sweep. Options available: 1, 2, 4. Default setting is 1.
Silent Execution	Execute MBIST Data Eye silently without ABL log output. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .

## 2-3-3-4 DDR RAS



Parameter	Description
DDR RAS	
Data Poisoning	Enable/Disable the Data Poisoning function. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
DRAM Boot Time Post Package Repair	Enable/Disable the DRAM Boot Time Post Package Repair function. Options available: Enable, Disable. Default setting is <b>Disable</b> .
DRAM Runtime Post Package Repair	Enable/Disable the DRAM Runtime Post Package Repair function. Options available: Enable, Disable. Default setting is <b>Disable</b> .
RCD Parity	Enable/Disable the RCD Parity function. Options available: Auto, Enabled, Disabled. Default setting is <b>Enabled</b> .
Max RCD Parity Error Replay	Default setting is <b>8</b> .
Write CRC	Enable/Disable the Write CRC function. Options available: Auto, Enabled, Disabled. Default setting is <b>Enabled</b> .
Max Write CRC Error Replay	Default setting is <b>8</b> .
Read CRC	Enable/Disable the Read CRC function. Options available: Auto, Enabled, Disabled. Default setting is <b>Enabled</b> .
Max Read CRC Error Replay	Default setting is <b>8</b> .
Disable Memory Error Injection	Options available: False, True, Auto. Default setting is <b>Auto</b> .
ECC Configuration	Press [Enter] to configure advanced items. <ul style="list-style-type: none"> <li>◆ DRAM ECC Symbol Size                             <ul style="list-style-type: none"> <li>– Configures the DRAM ECC Symbol Size.</li> <li>– Options available: Auto, x4, x16. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>

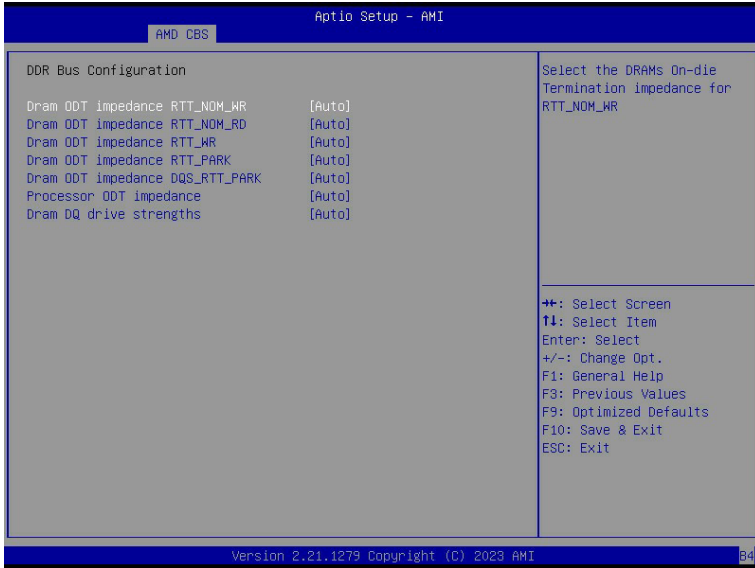
Parameter	Description	
ECC Configuration (continued)	<ul style="list-style-type: none"> <li>◆ DRAM ECC Enable <ul style="list-style-type: none"> <li>– Enable/Disable DRAM ECC. When set to Auto, it will set ECC to enable.</li> <li>– Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ DRAM UECC Retry <ul style="list-style-type: none"> <li>– Enable/Disable DRAM UECC Retry.</li> <li>– Options available: Auto, Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>◆ Max DRAM UECC Error Replay<sup>(Note)</sup> <ul style="list-style-type: none"> <li>– Default setting is <b>8</b>.</li> </ul> </li> <li>◆ Memory Clear <ul style="list-style-type: none"> <li>– Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ Address XDR after ECC <ul style="list-style-type: none"> <li>– Options available: Auto, Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> </ul>	
	Press [Enter] to configure advanced items.	
	DRAM Scrubbers	<ul style="list-style-type: none"> <li>◆ DRAM ECS Mode <ul style="list-style-type: none"> <li>– Options available: Auto, AutoECS, ManualECS, DisableECS. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ DRAM Redirect Scrubber Enable <ul style="list-style-type: none"> <li>– Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ DRAM Scrub Redirection Limit <ul style="list-style-type: none"> <li>– Options available: Auto, 8 Scrubs, 4 Scrubs, 2 Scrubs, 1 Scrub. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ DRAM Scrub Time <ul style="list-style-type: none"> <li>– Options available: Disabled, 1 hour, 4 hours, 6 hours, 8 hours, 12 hours, 16 hours, 24 hours, 48 hours. Default setting is <b>24 Hours</b>.</li> </ul> </li> <li>◆ DRAM Error Threshold Count <ul style="list-style-type: none"> <li>– Options available: Auto, ETC_4, ETC_16, ETC_64, ETC_256, ETC_1024, ETC_4096. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ DRAM ECS Count Mode <ul style="list-style-type: none"> <li>– Options available: Auto, Row Count Mode, Code Word Count Mode. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ DRAM AutoEcs during Self Refresh <ul style="list-style-type: none"> <li>– Options available: Auto, AutoEcs Disabled, AutoEcs Enabled. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ DRAM ECS WriteBack Suppression <ul style="list-style-type: none"> <li>– Options available: Auto, Enable, Disable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>

(Note) This item available when **DRAM UECC Retry** is set to **Enabled**.



Parameter	Description
DRAM Scrubbers (continued)	<ul style="list-style-type: none"> <li>◆ DRAM X4 WriteBack Suppression               <ul style="list-style-type: none"> <li>– Options available: Auto, Enable, Disable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
DRAM Corrected Error Counter Enable	Configure DRAM Corrected Error Counter function. Options available: Disable, NoLeakMode, Leak Mode. Default setting is <b>Leak Mode</b> .
DRAM Corrected Error Counter Interrupt Enable	Enable SMI when DRAM corrected Error Counter count exceeds the threshold value. Options available: False, True. Default setting is <b>True</b> .
DRAM Corrected Counter Leak Rate	Program Rate value for DRAM Corrected Error Counter function. Default setting is <b>7</b> .
DRAM Corrected Error Counter Start Count	Program starting value for DRAM Corrected Error Counter function. Default setting is <b>FFF5</b> .

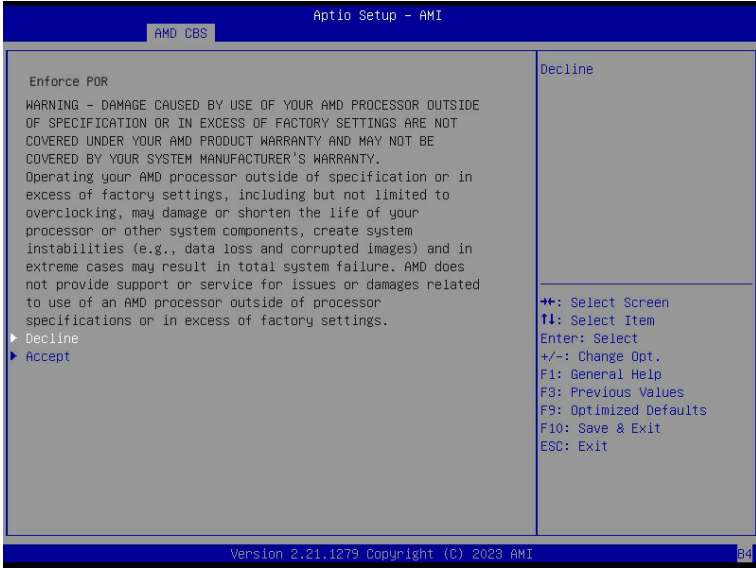
## 2-3-3-5 DDR Bus Configuration



Parameter	Description
DDR Bus Configuration	
Dram ODT impedance RTT_NOM_WR	Select the DRAMs On-die Termination impedance for RTT_NOM_WR. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80) RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is <b>Auto</b> .
Dram ODT impedance RTT_NOM_RD	Select the DRAMs On-die Termination impedance for RTT_NOM_RD. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80) RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is <b>Auto</b> .
Dram ODT impedance RTT_WR	Select the DRAMs On-die Termination impedance for RTT_WR. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80) RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is <b>Auto</b> .
Dram ODT Timpedance RTT_PARK	Select the DRAMs On-die Termination impedance for RTT_PARK. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80) RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is <b>Auto</b> .
Dram ODT Timpedance DQS_RTT_PARK	Select the DRAMs On-die Termination impedance for DQS_RTT_PARK. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80) RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is <b>Auto</b> .

Parameter	Description
Processor ODT impedance	Select the ODT impedance for all DBYTE IOs. Options available: Auto, High Impedance, 480 ohm, 240 ohm, 160 ohm, 120 ohm, 96 ohm, 80 ohm, 68.6 ohm, 60 ohm, 53.3 ohm, 48 ohm, 43.6 ohm, 40 ohm, 36.9 ohm, 34.3 ohm, 32 ohm, 30 ohm, 28.2 ohm, 26.7 ohm, 25.3 ohm. Default setting is <b>Auto</b> .
Dram DQ drive strengths	Select the Dram Pull-up and Pull-Down Output Driver Impedance for all DQ and DMI IOs.. Options available: Auto, 48 ohm, 40 ohm, 34 ohm, Default setting is <b>Auto</b> .

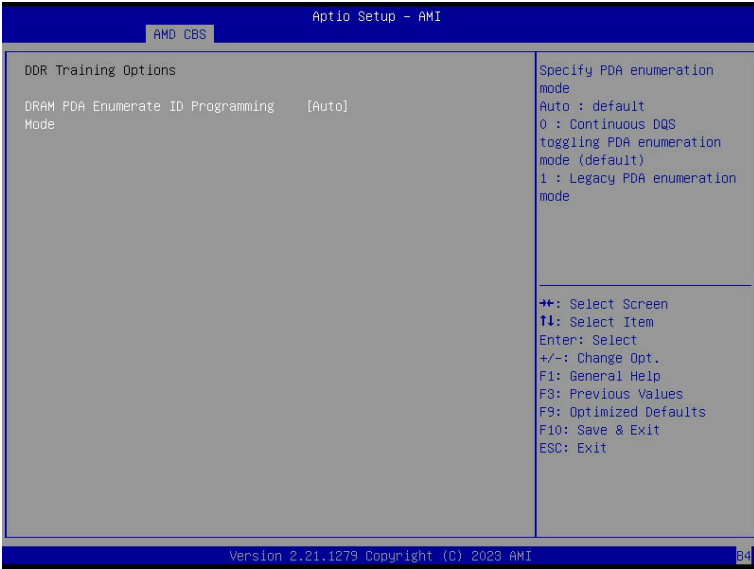
## 2-3-3-6 Enforce POR



Parameter	Description
DDR Timing Configuration	Decline/Accept to configure the advanced items.
Accept	
Active Memory Timing Settings <sup>(Note)</sup>	Active memory Timing Settings. Options available: Auto, Enabled. Default setting is <b>Auto</b> .
Memory Target Speed	Specifies the memory target speed in MT/s. Options available: Auto, DDR3200, DDR3600, DDR4000, DDR4400, DDR4800, DDR5200, DDR5600. Default setting is <b>Auto</b> .
SPD Timing	Press [Enter] to configure advanced items.
Non-SPD Timing	Press [Enter] to configure advanced items.

(Note) Advanced items prompt when this item is defined.

## 2-3-3-7 DDR Training Options



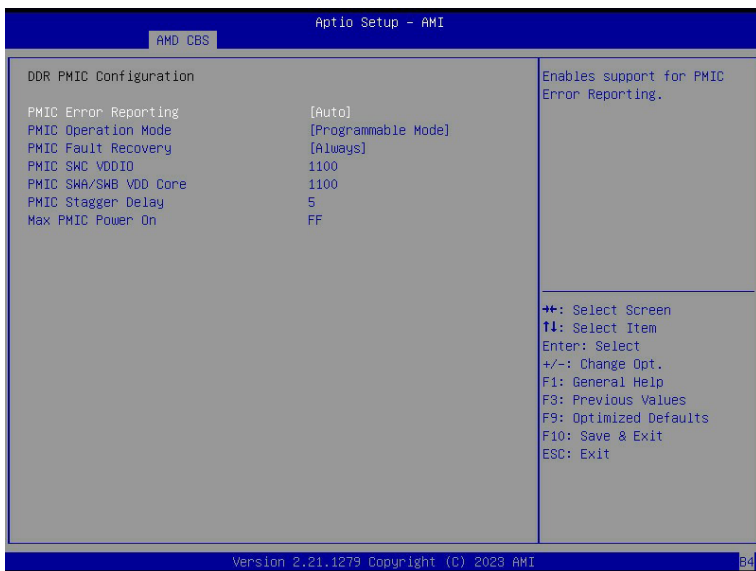
Parameter	Description
DDR Training Options	
DRAM PDA Enumerate ID Programming Mode	Specify PDA enumeration mode. Options available: Auto, Toggling PDA enumeration mode, Legacy PDA enumeration mode. Default setting is <b>Auto</b> .

### 2-3-3-8 DDR Security



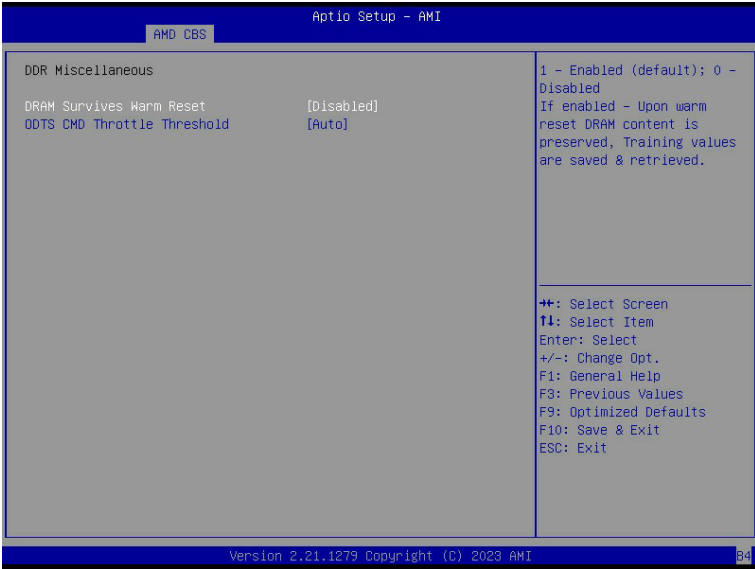
Parameter	Description
Security	
TSME	Enable/Disable Transparent SME. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
AES	Options available: AES-128, AES-256. Default setting is <b>AES-256</b> .
Data Scramble	Enable/Disable Data Scrambling. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
SME-MK	Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .

## 2-3-3-9 DDR PMIC Configuration



Parameter	Description
DDR PMIC Configuration	
PMIC Error Reporting	Enables support for PMIC Error Reporting. Options available: Auto, False, True. Default setting is <b>Auto</b> .
PMIC Operation Mode	Options available: Secure Mode, Programmable Mode. Default setting is <b>Programmable Mode</b> .
PMIC Fault Recovery	Options available: Always, Never, Once. Default setting is <b>Always</b> .
PMIC SWC VDDIO	Default setting is <b>1100</b> .
PMIC SWA/SWB VDD Core	Default setting is <b>1100</b> .
PMIC Stagger Delay	Default setting is <b>5</b> .
Max PMIC Power On	Default setting is <b>FF</b> .

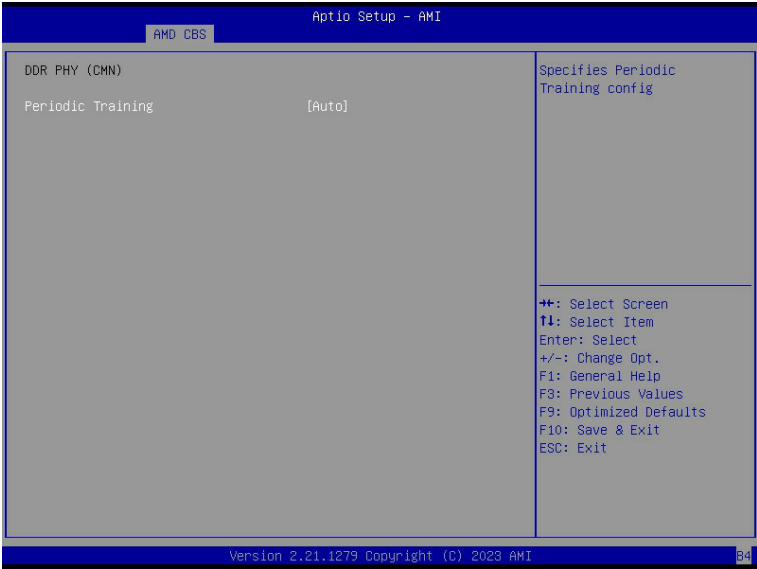
### 2-3-3-10 DDR Miscellaneous



Parameter	Description
DDR Miscellaneous	
DRAM Survives Warm Reset	Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .



### 2-3-3-11 DDR PHY (CMN)



Parameter	Description
DDR PHY (CMN)	
Periodic Training <sup>(Note)</sup>	Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
Periodic Training Interval	Specifies the Periodic Training interval in millisecond.

## 2-3-4 NBIO Common Options

AMD CBS Aptio Setup - AMI

NBIO Common Options		Enable/Disable IOMMU
IOMMU	[Auto]	
DHAR Support	[Auto]	
DMA Protection	[Auto]	
DRTM Virtual Device Support	[Auto]	
DRTM Memory Reservation	[Auto]	
ACS Enable	[Auto]	
PCIe ARI Support	[Auto]	
PCIe ARI Enumeration	[Auto]	
PCIe Ten Bit Tag Support	[Auto]	
▶ SMU Common Options		
▶ NBIO RAS Common Options		
Enable AER Cap	[Auto]	
Early Link Speed	[Auto]	
Hot Plug Handling mode	[Auto]	
Hot Plug Allow FF in Synchronous	[Disabled]	
Presence Detect Select mode	[Auto]	
Data Link Feature Cap	[Auto]	
CV test	[Auto]	
SEV-SNP Support	[Auto]	
Allow Compliance	[Auto]	
SRIS	[Auto]	
Multi Upstream Auto Speed Change	[Auto]	

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AMD CBS Aptio Setup - AMI

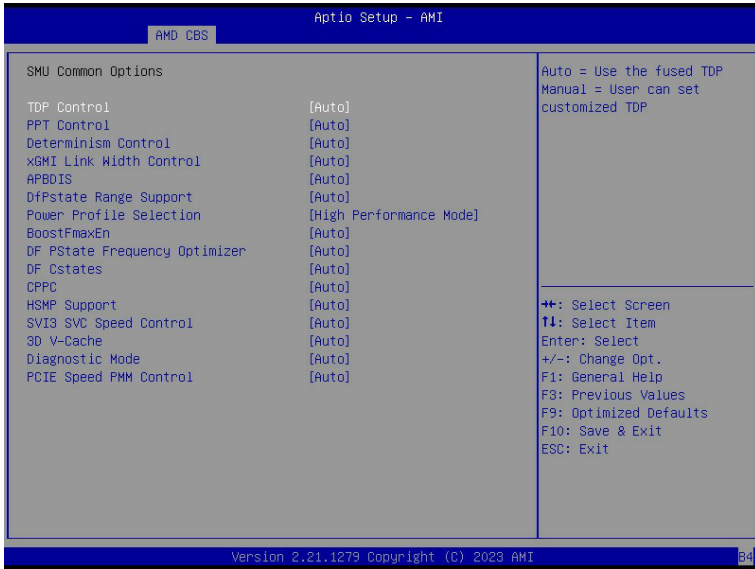
PCIe ARI Enumeration	[Auto]	Enable this setting to use 2 symbols per clock for devices at Gen 4 speed.
PCIe Ten Bit Tag Support	[Auto]	
▶ SMU Common Options		
▶ NBIO RAS Common Options		
Enable AER Cap	[Auto]	
Early Link Speed	[Auto]	
Hot Plug Handling mode	[Auto]	
Hot Plug Allow FF in Synchronous	[Disabled]	
Presence Detect Select mode	[Auto]	
Data Link Feature Cap	[Auto]	
CV test	[Auto]	
SEV-SNP Support	[Auto]	
Allow Compliance	[Auto]	
SRIS	[Auto]	
Multi Upstream Auto Speed Change	[Auto]	
Multi Auto Speed Change On Last Rate	[Auto]	
PCIe Link Speed Capability	[Auto]	
RTM Margining Support	[Auto]	
EQ Bypass To Highest Rate	[Auto]	
Non-PCIe Compliant Support	[Auto]	
PCIe Idle Power Setting	[Optimize for Perf/Power]	
▶ nBif Common Options		
▶ Link EQ Preset Options		
Enable 2 SPC (Gen 4)	[Auto]	

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Parameter	Description
NBIO Common Options	
IOMMU	Enable/Disable the IOMMU function. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
DMAr Support	Enable/Disable DMAr system protection during POST. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
DMA Protection	Enable/Disable DMA remap support in IVRS IVinfo Field. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
DRTM Virtual Device Support	Enable/Disable DRTM ACPI virtual device. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
DRTM Memory Reservation	Enable/Disable DRTM Memory reservation. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
ACS Enable	Enable/Disable ACS. Options available: Enable, Disabled, Auto. Default setting is <b>Auto</b> .
PCIe ARI Support	Enable/Disable Alternative Routing-ID Interpretation. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
PCIe ARI Enumeration	ARI Forwarding Enable for each downstream port. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
PCIe Ten Bit Tag Support	Enable/Disable PCIe ten bit tags for supported devices. (Auto=Disabled) Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
SMU Common Options	Press [Enter] for configuration of advanced items.
NBIO RAS Common Options	Press [Enter] for configuration of advanced items.
Enable AER Cap	Enable/Disable Advanced Error Reporting Capability. Options available: Enable, Disabled, Auto. Default setting is <b>Auto</b> .
Early Link Speed	Configures Early Link Speed. Options available: Auto, Gen1, Gen2. Default setting is <b>Auto</b> .
Hot Plug Handling mode	Controls the Hot Plug Handling mode. Options available: OS First, Firmware First/EDR if OS supports, Firmware First but allow OS First, System Firmware Intermediary, Auto. Default setting is <b>Auto</b> .
Hot Plug Allow FF in Synchronous	Allows firmware first hot plug handling mode to operate in mode A and mode B synchronous mappings. Options available: Disabled, Enabled. Default setting is <b>Disabled</b> .
Presence Detect Select mode	Controls the Presence Detect Select mode. Options available: OR, AND, Auto. Default setting is <b>Auto</b> .

Parameter	Description
Data Link Feature Cap	Enable/Disable the data link feature capability. Options available: Enabled, Disabled, Auto. Default setting is <b>Auto</b> .
CV test	Enable/Disable the running PCIE CV tool support. Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
SEV-SNP Support	Enable/Disable the SEV-SNP support. Options available: Disable, Enable. Default setting is <b>Disable</b> .
Allow Compliance	When enabled, allows the PCIe RP to enter Polling.Compliance state. Options available: Auto, Disable, Enable. Default setting is <b>Auto</b> .
SRIS	Options available: Auto, Disable, Enable. Default setting is <b>Auto</b> .
Multi Upstream Auto Speed Change	Defines the setting of this feature for all PCIe devices. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Multi Auto Speed Change On Last Rate	Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
PCIE Link Speed Capability	Options available: Maximum speed, Gen1, Gen2, Gen3, Gen4, Gen5, Auto. Default setting is <b>Auto</b> .
RTM Margining Support	Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
EQ Bypass To Highest Rate	Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
nBif Common Options	Press [Enter] for configuration of advanced items.

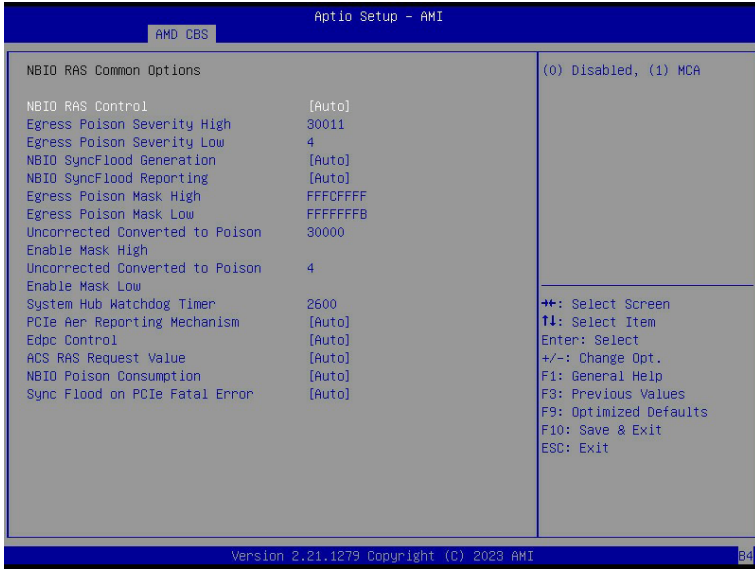
## 2-3-4-1 SMU Common Options



Parameter	Description
SMU Common Options	
TDP Control	Options available: Manual, Auto. Default setting is <b>Auto</b> .
PPT Control	Options available: Manual, Auto. Default setting is <b>Auto</b> .
Determinism Control	Selects use the fused Determinism or set customized Determinism. Options available: Manual, Auto. Default setting is <b>Auto</b> .
xGMI Link Width Control	Options available: Manual, Auto. Default setting is <b>Auto</b> .
APBDIS	Options available: 0, 1, Auto. Default setting is <b>Auto</b> .
Power Profile Selection	Options available: High Performance Mode, Efficiency Mode, Maximum IO Performance Mode. Default setting is <b>High Performance Mode</b> .
BoostFmaxEn	Options available: Manual, Auto. Default setting is <b>Auto</b> .
DF PState Frequency Optimizer	Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
DF PState Latency Optimizer	Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
DF Cstates	Options available: Disabled, Enabled, Auto. Default setting is <b>Disabled</b> .
CPPC	Enable/Disable the CPPC feature. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .

<b>Parameter</b>	<b>Description</b>
HSMP Support	Enable/Disable the HSMP support. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
SVI3 SVC Speed Control	Options available: Auto, Manual. Default setting is <b>Auto</b> .
3D V-Cache	Options available: Auto, Disable, 1 stack, 2 stack, 4 stack. Default setting is <b>Auto</b> .
Diagnostic Mode	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
PCIe Speed PMM Control	Options available: Dynamic link speed determined by Power Management functionality, Static Target Link Speed (GEN4), Static Target Link Speed (GEN5), Auto. Default setting is <b>Auto</b> .
GMI Folding	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .

## 2-3-4-2 NBIO RAS Common Options

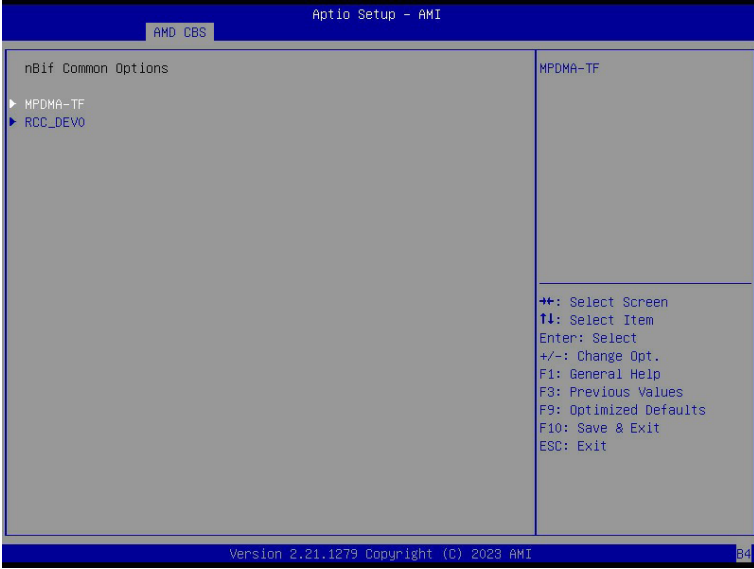


Parameter	Description
NBIO RAS Common Options	
NBIO RAS Control	Options available: Disabled, MCA, Auto. Default setting is <b>Auto</b> .
Egress Poison Severity High	Configures the Egress Poison High Severity. Each bit set to 1 enables High severity on the associated IOHC egress port. A bit of 0 indicates LOW severity.
Egress Poison Severity Low	Configures the Egress Poison Low Severity. Each bit set to 1 enables High severity on the associated IOHC egress port. A bit of 0 indicates LOW severity.
NBIO SyncFlood Generation	The value may be used to mask SyncFlood caused by NBIO RAS options. Options available: Enabled, Disabled, Auto. Default setting is <b>Auto</b> .
NBIO SyncFlood Reporting	The value may be used to enable SyncFlood reporting to APML. Options available: Enabled, Disabled, Auto. Default setting is <b>Auto</b> .
Egress Poison Mask High	Enables mask for masking of errors logged in EGRESS_POISON_STATUS. For each bit set to 1, errors are masked. For each bit set to 0, errors trigger response actions.
Egress Poison Mask Low	Enables mask for masking of errors logged in EGRESS_POISON_STATUS. For each bit set to 1, errors are masked. For each bit set to 0, errors trigger response actions.

Parameter	Description
Uncorrected Converted to Poison Enable Mask High	Enables mask for masking of uncorrectable parity errors on internal arrays.
Uncorrected Converted to Poison Enable Mask Low	Enables mask for masking of uncorrectable parity errors on internal arrays.
System Hub Watchdog Timer	Specifies the timer interval of the SYSHUB Watchdog timer in milliseconds.
PCIe Aer Reporting Mechanism	Selects the method of reporting AER errors from PCI Express. Options available: Firmware First, Firmware First but allow OS First, OS First, Auto. Default setting is <b>Auto</b> .
Edpc Control	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
ACS RAS Request Value	Options available: Direct Request Access Enabled, Request Blocking Enabled, Request Redirect Enabled, Auto. Default setting is <b>Auto</b> .
NBIO Poison Consumption	Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
Sync Flood on PCIe Fatal Error	Options available: Auto, True, False. Default setting is <b>Auto</b> .



### 2-3-4-3 nBif Common Options

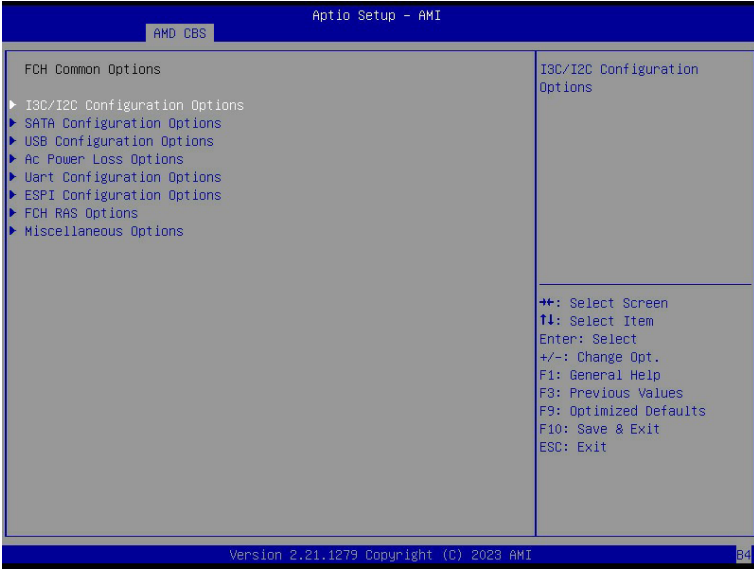


Parameter	Description
MPDMA-TF	<ul style="list-style-type: none"> <li>◆ SRIOV                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ ARI                             <ul style="list-style-type: none"> <li>– Options available: Auto/Default, Disable, Enable. Default setting is <b>Auto/Default</b>.</li> </ul> </li> <li>◆ AER                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ ACS                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ ATS                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ PASID                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ RTR                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ PAGE_REQ                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ PWR                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> <li>◆ ATC_ENABLE                             <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>

Parameter	Description
RCC_DEV0	<ul style="list-style-type: none"> <li>◆ ACS Rcc_Dev0               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ AER Rcc_Dev0               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ DllEnableStrap1               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ Phy16GTStrap1               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ MarginEnStrap1               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ SourceValStrap5               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ TranslationalBlockingStrap5               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ P2pReq ACS Control               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ P2pCompStrap5               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ UpstreamFwdStrap5               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ P2PEgressStrap5               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ DirectTranslatedStrap5               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ SsidEnStrap5               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ PriEnPageReq               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ PriResetPageReq               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ SourceVal ACS cntl               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ TranslationalBlocking ACS Control               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ P2pComp ACS Control               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ UpstreamFwd ACS Control               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ P2PEgress ACS Control               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ P2pReqStrap5               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>
	<ul style="list-style-type: none"> <li>◆ E2E_PREFIX               <ul style="list-style-type: none"> <li>– Options available: Auto, Disable, Enable. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>

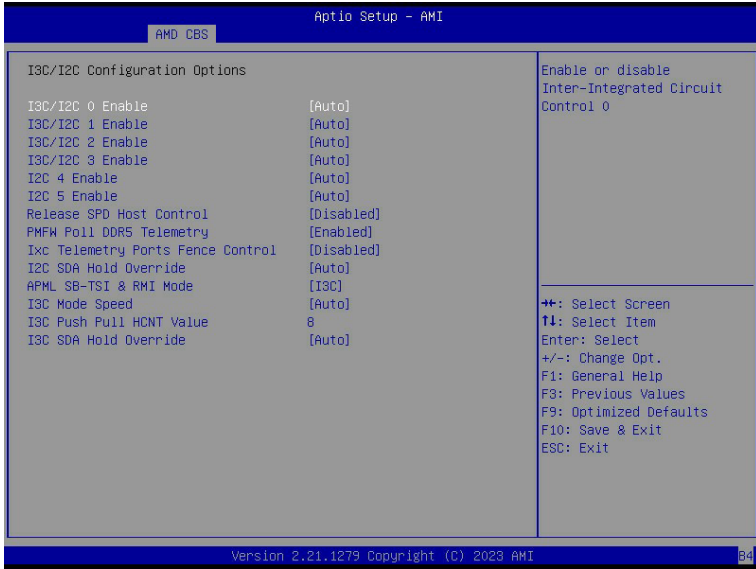
Parameter	Description
RCC_DEV0 (continued)	♦ EXTENDED_FMT – Options available: Auto, Disable, Enable. Default setting is <b>Auto</b> .

## 2-3-5 FCH Common Options



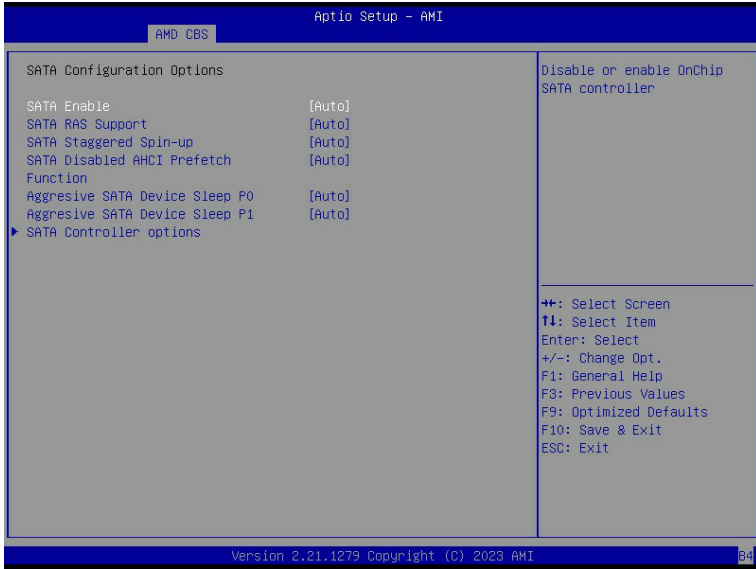
Parameter	Description
FCH Common Options	
I3C/I2C Configuration Options	Press [Enter] for configuration of advanced items.
SATA Configuration Options	Press [Enter] for configuration of advanced items.
USB Configuration Options	Press [Enter] for configuration of advanced items.
AC Power Loss Options	Press [Enter] for configuration of advanced items.
Uart Configuration Options	Press [Enter] for configuration of advanced items.
ESPI Configuration Options	Press [Enter] for configuration of advanced items.
FCH RAS Options	Press [Enter] for configuration of advanced items.
Miscellaneous Options	Press [Enter] for configuration of advanced items.

## 2-3-5-1 I3C/I2C Configuration Options



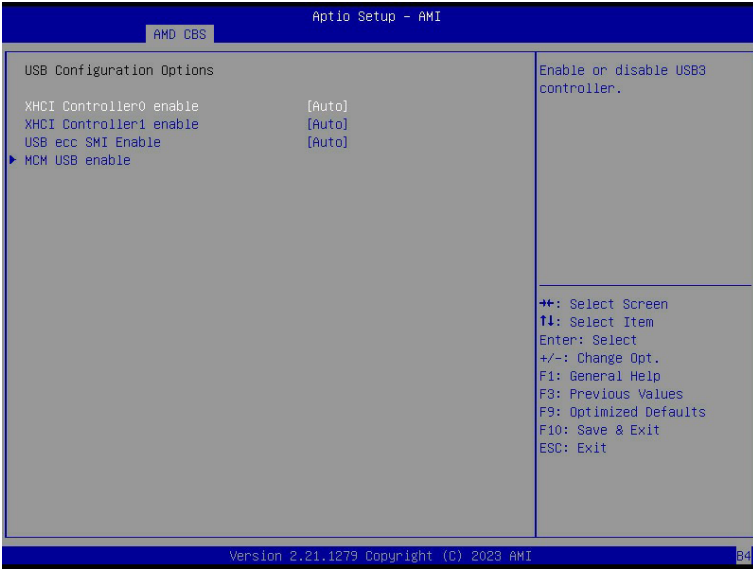
Parameter	Description
I3C/I2C Configuration Options	
I3C/I2C 0/1/2/3 Enable	Options available: Both Disabled, I3C Enabled, I2C Enabled, Auto. Default setting is <b>Auto</b> .
I2C 4/5 Enable	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Release SPD Host Control	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
I2C SDA Hold Override	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
APLM SB-TSI Mode	Options available: I3C, I2C. Default setting is <b>I3C</b> .
I3C Mode Speed	Options available: SDR2(6MHz), SDR0(12.5MHz), Auto. Default setting is <b>Auto</b> .
I3C SDA Hold Value	Configures I3C SDA Hold value.

## 2-3-5-2 SATA Configuration Options



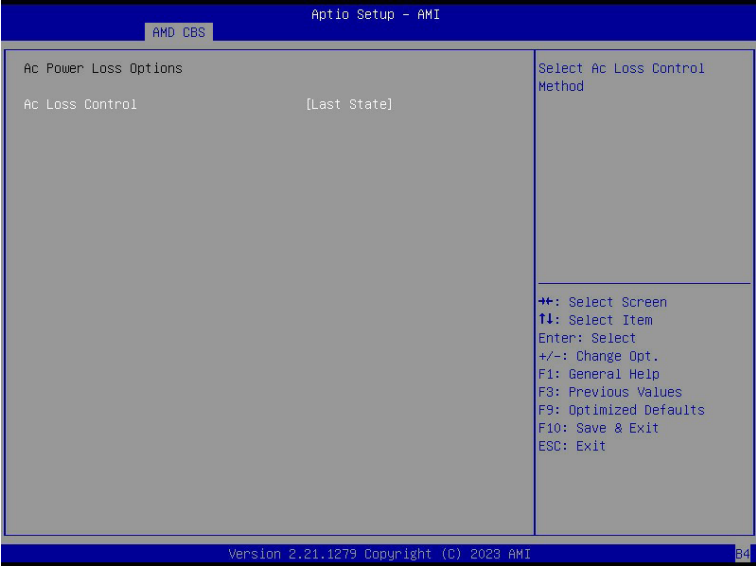
Parameter	Description
SATA Configuration Options	
SATA Enable	Enable/Disable OnChip SATA controller. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
SATA RAS Support	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
SATA Staggered Spin-up	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
SATA Disabled AHCI Prefetch Function	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Aggressive SATA Device Sleep P0/P1	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
SATA Controller options	Press [Enter] for configuration of advanced items. <ul style="list-style-type: none"> <li>◆ SATA Controller Enable</li> <li>◆ SATA Controller eSATA</li> <li>◆ SATA Controller DevSlp</li> <li>◆ SATA Controller SGPIO</li> </ul>

## 2-3-5-3 USB Configuration Options



Parameter	Description
USB Configuration Options	
XHCI Controller0/1 enable	Enable/Disable USB controller. Options available: Enabled, Disabled, Auto. Default setting is <b>Auto</b> .
USB ecc SMI Enable	Options available: Enable, Off, Auto. Default setting is <b>Auto</b> .
MCM USB enable	Press [Enter] for configuration of advanced items. <ul style="list-style-type: none"> <li>◆ XHCI2/ XHCI3 enable (Socket1) <ul style="list-style-type: none"> <li>– Options available: Enabled, Disabled, Auto. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>

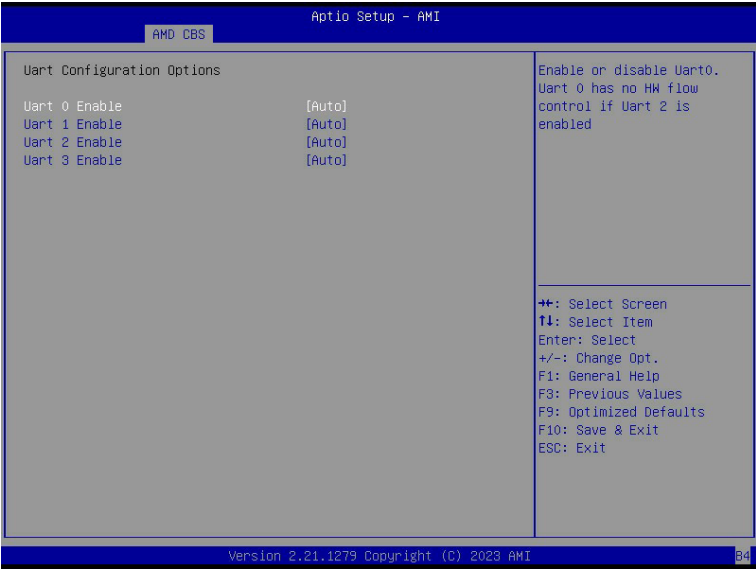
### 2-3-5-4 AC Power Loss Options



Parameter	Description
AC Power Loss Options	
AC Loss Control	Selects the AC Loss Control Method. Options available: Power Off, Power On, Last State. Default setting is <b>Last State</b> .

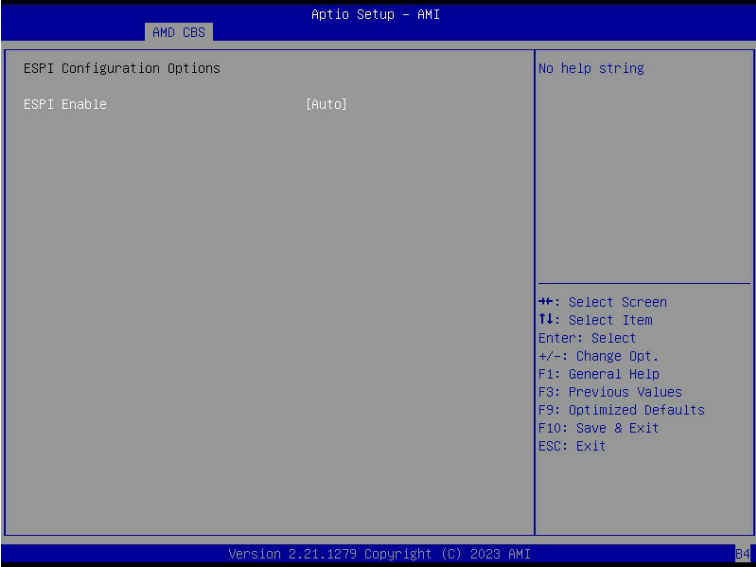


### 2-3-5-5 Uart Configuration Options



Parameter	Description
Uart Configuration Options	
Uart 0/1/2/3 Enable	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .

### 2-3-5-6 ESPI Configuration Options



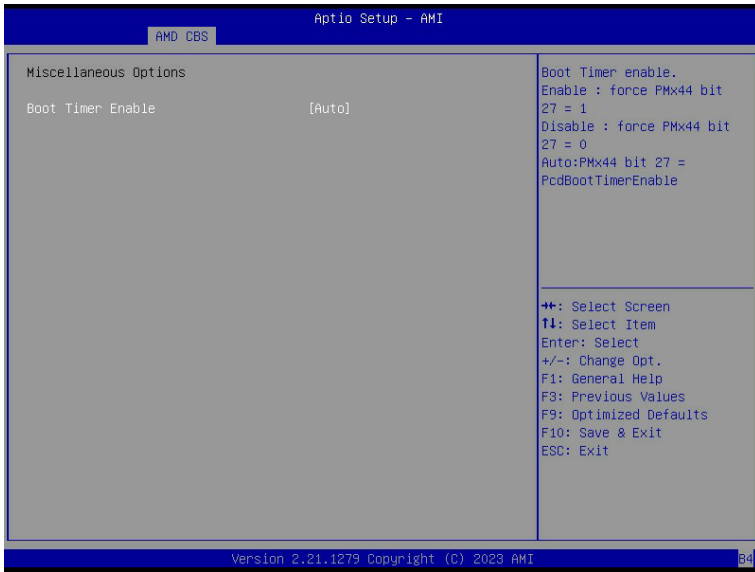
Parameter	Description
ESPI Configuration Options	
ESPI Enable	Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .

### 2-3-5-7 FCH RAS Options



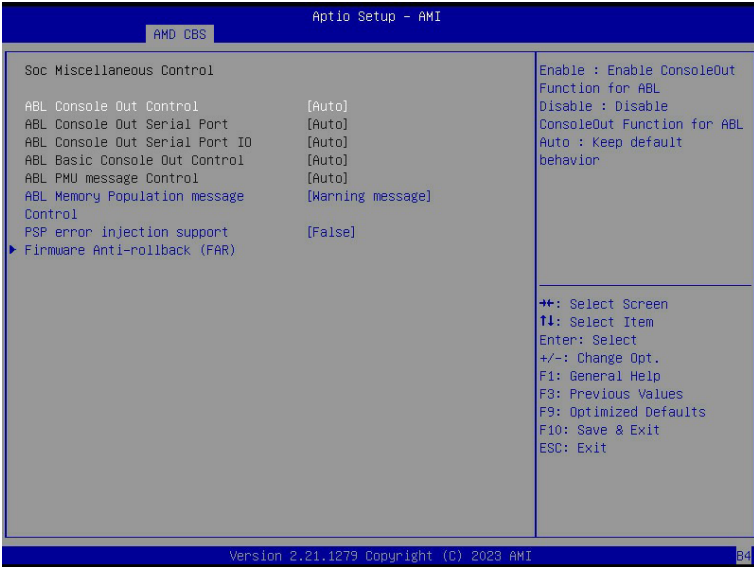
Parameter	Description
FCH RAS Options	
ALink RAS Support	Enable/Disable the ALink RAS Support. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .
Reset after sync flood	Enables AB to forward downstream sync-flood message to system controller. Options available: Enable, Disable, Auto. Default setting is <b>Auto</b> .

## 2-3-5-8 Miscellaneous Options



Parameter	Description
Miscellaneous Options	
Boot Timer Enable	Enable/Disable Boot Timer. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .

## 2-3-6 Soc Miscellaneous Control

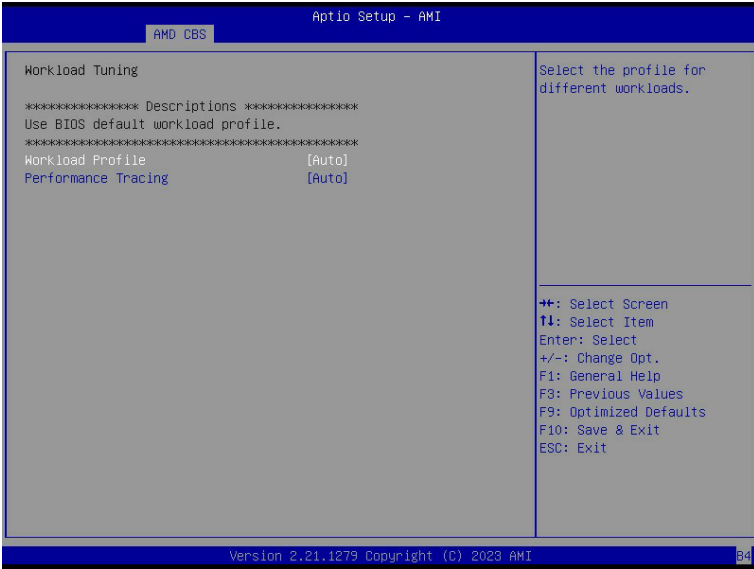


Parameter	Description
SOC Miscellaneous Control	
ABL Console Out Control <sup>(Note)</sup>	Enable/Disable the ConsoleOut function for ABL. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
ABL Console Out Serial Port <sup>(Note)</sup>	Options available: eSPI, SOC UART0, SOC UART1, Auto. Default setting is <b>Auto</b> .
ABL Console Out Serial Port IO	Options available: 0x3F8, 0x2F8, 0x3E8, 0x2E8, Auto. Default setting is <b>Auto</b> .
ABL Basic Console Out Control	Enable/Disable the Basic ConsoleOut function for ABL. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
ABL PMU message Control	To Control the total number of PMU debug messages. Options available: Auto, Detailed debug message, Coarse debug message, Stage completion, Assertion messages, Firmware completion message only. Default setting is <b>Auto</b> .
ABL Memory Population message Control	Options available: Warning message, Fatal error. Default setting is <b>Warning message</b> .
PSP error injection support	Options available: False, True. Default setting is <b>False</b> .

(Note) Advanced items are configurable when this item is defined.

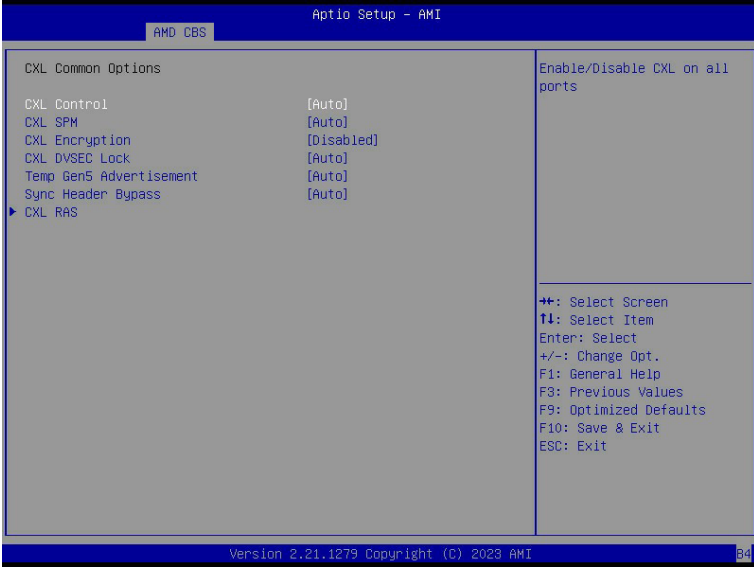
Parameter	Description
SOC Miscellaneous Control	
ABL Console Out Control <sup>(Note)</sup>	Enable/Disable the ConsoleOut function for ABL. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
ABL Console Out Serial Port <sup>(Note)</sup>	Options available: eSPI, SOC UART0, SOC UART1, Auto. Default setting is <b>Auto</b> .
ABL Console Out Serial Port IO	Options available: 0x3F8, 0x2F8, 0x3E8, 0x2E8, Auto. Default setting is <b>Auto</b> .
ABL Basic Console Out Control	Enable/Disable the Basic ConsoleOut function for ABL. Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
ABL PMU message Control	To Control the total number of PMU debug messages. Options available: Auto, Detailed debug message, Coarse debug message, Stage completion, Assertion messages, Firmware completion message only. Default setting is <b>Auto</b> .
ABL Memory Population message Control	Options available: Warning message, Fatal error. Default setting is <b>Warning message</b> .
PSP error injection support	Options available: False, True. Default setting is <b>False</b> .
Firmware Anti-rollback (FAR)	Press [Enter] for configuration of advanced items. <ul style="list-style-type: none"> <li>◆ FAR enforcement state <ul style="list-style-type: none"> <li>– Default setting is <b>Enabled</b>.</li> </ul> </li> <li>◆ SPL value in the CPU Fuse</li> <li>◆ SPL value in the SPL table</li> <li>◆ FAR Switch <ul style="list-style-type: none"> <li>– Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b>.</li> </ul> </li> </ul>

### 2-3-7 Workload Tuning



Parameter	Description
Workload Tuning	Select the profile for different workloads.
Workload Profile	Options available: CPU Intensive, Java Throughput, Java Latency, Power Efficiency, Memory Throughput Intensive, Storgae IO Intensive, NIC Throughput Intensive, NIC Latency Sensitive, Accelerator Throughput, VMware vSphere Optimized, Linux KVM Optimized, Container Optimized, RDBMS Optimized, Big Data Analytics Optimized, IOT Gateway, HPC Optimized, OpenStack NFV, OpenStack for ReakTime Kernel, Auto. Default setting is <b>Auto</b> .
Performance Tracing	Enable to allow capturing performance traces. Options available: Disabled, Enabled, Auto. Default setting is <b>Auto</b> .

## 2-3-8 CXL Common Options

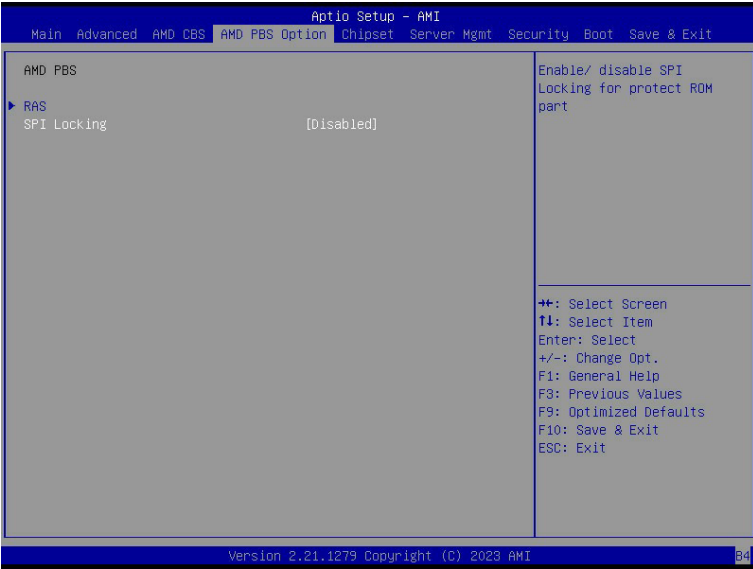


Parameter	Description
CXL Control	Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
CXL SPM	Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
CXL vLSM Power Management	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> <li>◆ CXL.io <ul style="list-style-type: none"> <li>– L1/L2 <ul style="list-style-type: none"> <li>• Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b>.</li> </ul> </li> </ul> </li> <li>◆ CXL.camem <ul style="list-style-type: none"> <li>– L1/L2 <ul style="list-style-type: none"> <li>• Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b>.</li> </ul> </li> </ul> </li> </ul>
CXL Encryption	Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Temp Gen5 Advertisement	Options available: Disable, Enable, Auto. Default setting is <b>Auto</b> .
Sync Header Bypass	Options available: Auto, Enabled, Disabled. Default setting is <b>Auto</b> .
CXL RAS	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> <li>◆ CXL Protocol Error Reporting <ul style="list-style-type: none"> <li>– Options available: Disabled, SameAsPcieAer, ForceAerFwFirstIfCxlPresent. Default setting is <b>SameAsPcieAer</b>.</li> </ul> </li> <li>◆ CXL Component Error Reporting <ul style="list-style-type: none"> <li>– Options available: OS First, FW-First. Default setting is <b>FW-First</b>.</li> </ul> </li> </ul>



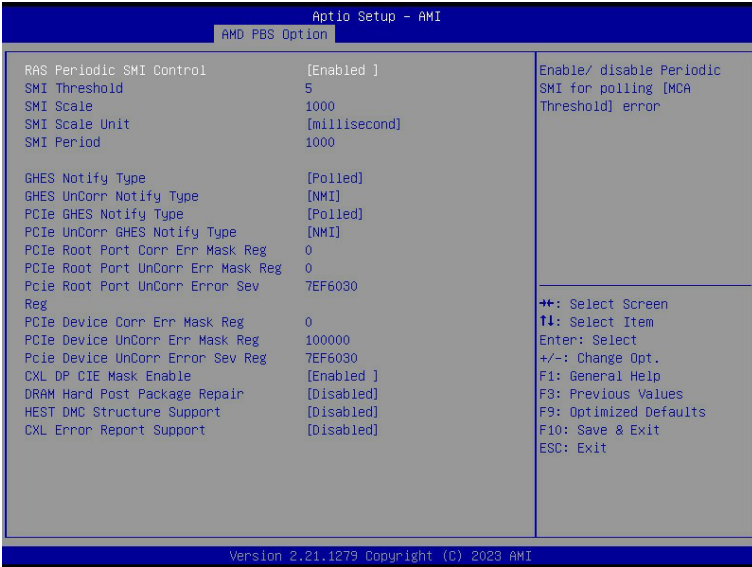
## 2-4 AMD PBS Menu

AMD PBS Option menu displays submenu options for configuring the function of AMD PBS. Select a submenu item, then press [Enter] to access the related submenu screen.



Parameter	Description
RAS	Press [Enter] for configuration of advanced items.
SPI Locking	Enable/Disable SPI Locking for protect ROM part. Options available: Disabled, Enabled. Default setting is <b>Disabled</b> .

## 2-4-1 RAS

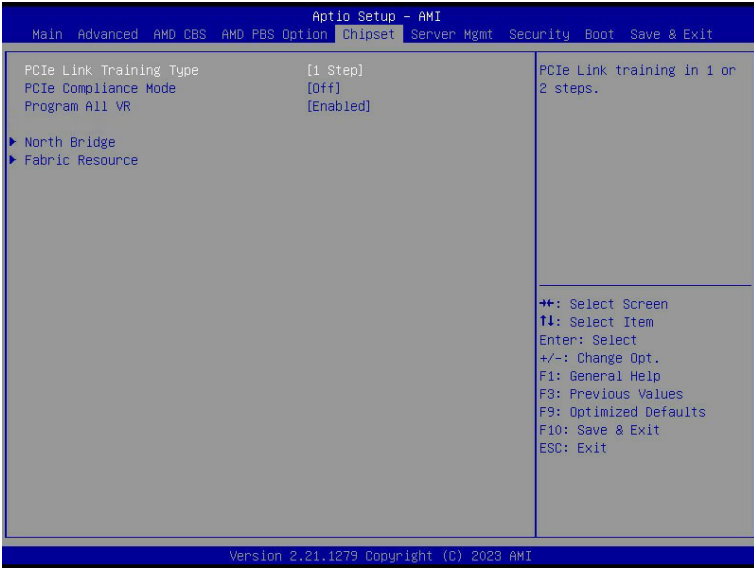


Parameter	Description
RAS Periodic SMI Control	Enable/Disable the Periodic SMI for polling [MCA Threshold] error. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
SMI Threshold	Configures the SMI Threshold value.
SMI Scale	Configures the SMI Scale value.
SMI Scale Unit	Defines the unit of time scale. Options available: millisecond, second, minute. Default setting is <b>millisecond</b> .
SMI Period	Configures the SMI Period.
GHEs Notify Type	Selects the Notification type for deferred/ corrected errors. Options available: Polled, SCI. Default setting is <b>Polled</b> .
GHEs UnCorr Notify Type	Selects the Notification type for uncorrected errors. Options available: Polled, NMI. Default setting is <b>NMI</b> .
PCIe GHEs Notify Type	Selects the Notification type for PCIe corrected errors. Options available: Polled, SCI. Default setting is <b>Polled</b> .
PCIe UnCorr GHEs Notify Type	Selects the Notification type for PCIe uncorrected errors. Options available: Polled, NMI. Default setting is <b>NMI</b> .
PCIe Root Port Corr Err Mask Reg	Initialize the PCIe AER Corrected Error Mask register of Root Port.

Parameter	Description
PCIe Root Port UnCorr Err Mask Reg	Initialize the PCIe AER Uncorrected Error Mask register of Root Port.
PCIe Root Port UnCorr Err Sev Reg	Initialize the PCIe AER Uncorrected Error Severity register of Root Port.
PCIe Device Corr Err Mask Reg	Initialize the PCIe AER Corrected Error Mask register of PCIe device.
PCIe Device UnCorr Err Mask Reg	Initialize the PCIe AER Uncorrected Error Mask register of PCIe device.
PCIe Device UnCorr Err Sev Reg	Initialize the PCIe AER Uncorrected Error Severity register of PCIe device.
DRAM Hard Post Package Repair	This feature allows spare DRAM rows to replace malfunctioning rows via an in-field repair mechanism. Options available: Disabled, Enabled. Default setting is <b>Disabled</b> .
HEST DMC Structure Support	HEST DMC (Deferred Machine Check) Structure Support. Options available: Disabled, Enabled. Default setting is <b>Disabled</b> .
CXL Error Report Support	Enable/Disable CXL Error Reporting. Options available: Disabled, Enabled. Default setting is <b>Disabled</b> .

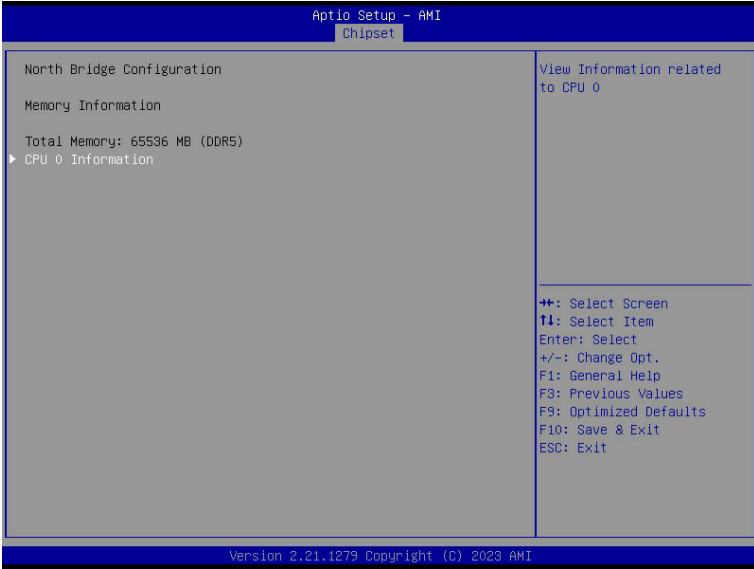
## 2-5 Chipset Setup Menu

Chipset Setup menu displays submenu options for configuring the function of the North Bridge. Select a submenu item, then press <Enter> to access the related submenu screen.



Parameter	Description
PCIe Link Training Type	Options available: 1 Step, 2 Step. Default setting is <b>1 Step</b> .
PCIe Compliance Mode	Options available: Off, On. Default setting is <b>Off</b> .
Program All VR	Enable/Disable program all VR on MB. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
North Bridge	Press [Enter] for configuration of advanced items.
Fabric Resource	Press [Enter] for configuration of advanced items.

## 2-5-1 North Bridge

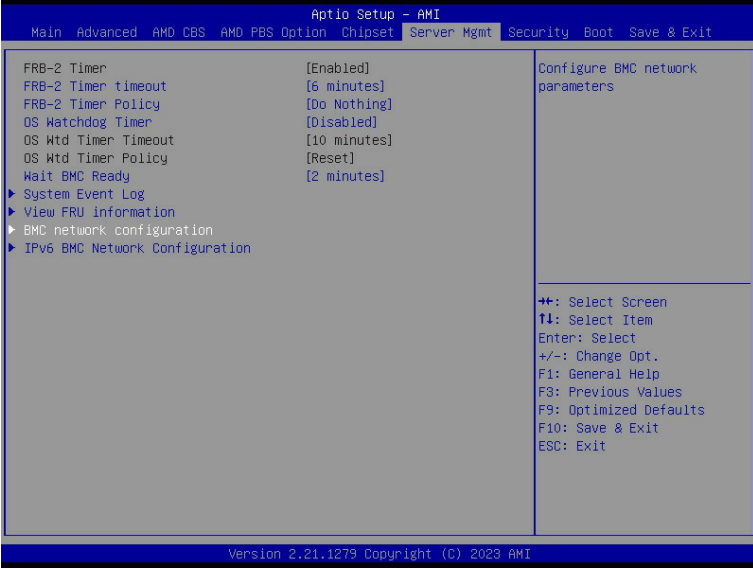


Parameter	Description
North Bridge Configuration	
Memory Information	
Total Memory	Displays the total memory information.
CPU 0 Information	Press [Enter] to view information related to CPU 0.



Parameter	Description
Fabric Resource	
CPU 0 NBIO_# PCIe Bus Number	Change CPU 0/1 NBIO_# PCIe Bus Number.
Prefetchable Mmio Above 4G size	Change CPU 0/1 NBIO_# Prefetchable MMIO Above 4G Size. Options available: System Default, 0, 1G, 2G, 4G, 8G, 16G, 32G, 64G, 128G, 256G, 512G, 1T, 2T, 4T, 8T. Default setting is <b>System Default</b> .
PCIe IO Resource	Change CPU 0/1 NBIO_# PCIe IO Resource.

## 2-6 Server Management Menu



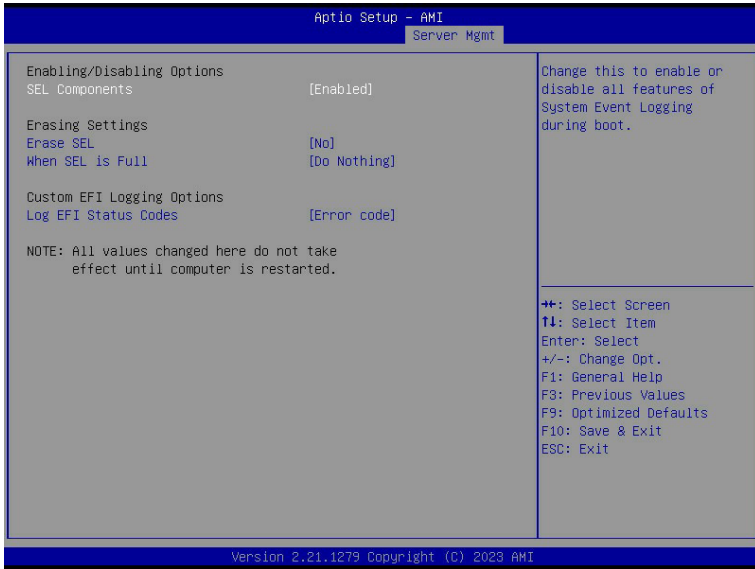
Parameter	Description
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Default setting is <b>Enabled</b> .
FRB-2 Timer timeout	Configures the FRB2 Timer timeout. Options available: 3 minutes, 4 minutes, 5 minutes, 6 minutes. Default setting is <b>6 minutes</b> .
FRB-2 Timer Policy	Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is <b>Do Nothing</b> .
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
OS Wtd Timer Timeout <sup>(Note)</sup>	Configures OS Watchdog Timer. Options available: 5 minutes, 10 minutes, 15 minutes, 20 minutes. Default setting is <b>10 minutes</b> .
OS Wtd Timer Policy <sup>(Note)</sup>	Configure OS Watchdog Timer Policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is <b>Reset</b> .
Wait BMC Ready	Post wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is <b>2 minutes</b> .

(Note) This item is configurable when **OS Watchdog Timer** is set to **Enabled**.



<b>Parameter</b>	<b>Description</b>
System Event Log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the FRU information.
BMC network configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

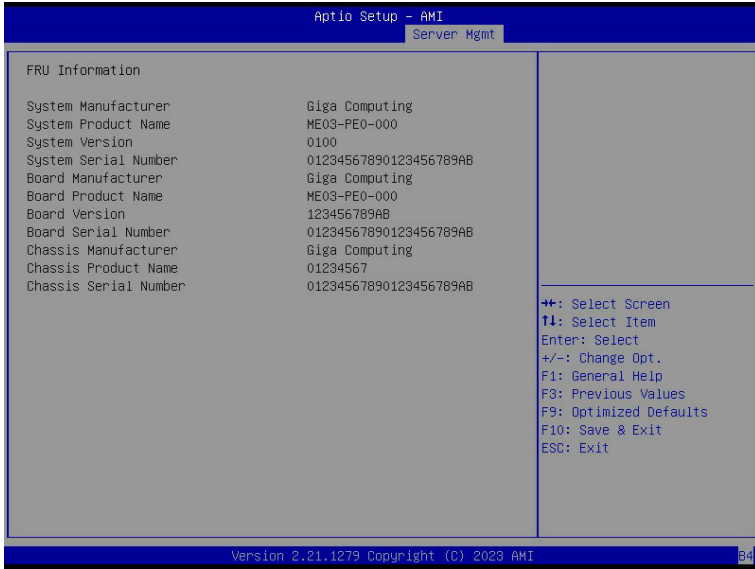
## 2-6-1 System Event Log



Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No/Yes, On next reset/Yes, On every reset. Default setting is <b>No</b> .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately. Default setting is <b>Do Nothing</b> .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is <b>Error code</b> .

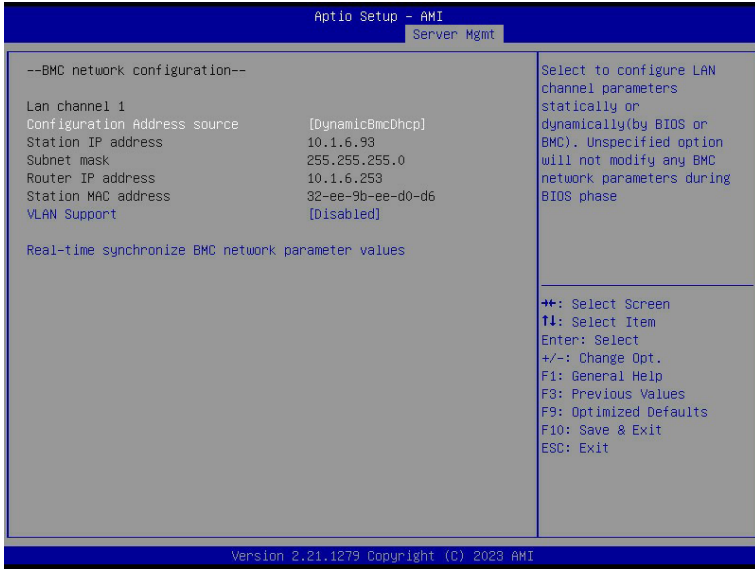
## 2-6-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.



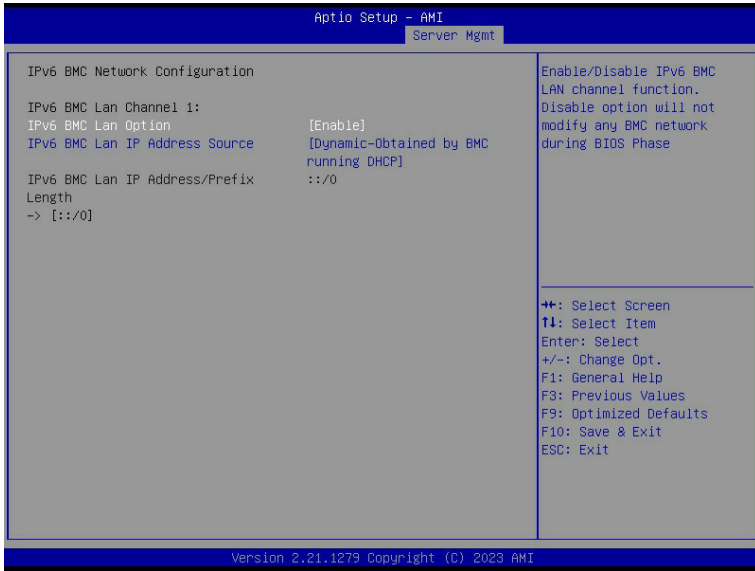
(Note) The model name will vary depends on the product you purchased

## 2-6-3 BMC Network Configuration



Parameter	Description
BMC network configuration	
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase. Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is <b>DynamicBmcDhcp</b> .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
VLAN Support	Set BMC to enable/disable VLAN support. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Real-time synchronize BMC network parameter values	Press [Enter] will set Address source(Static/DHCP) to BMC and then get Station IP address, Subnet mask and Router IP address from BMC.

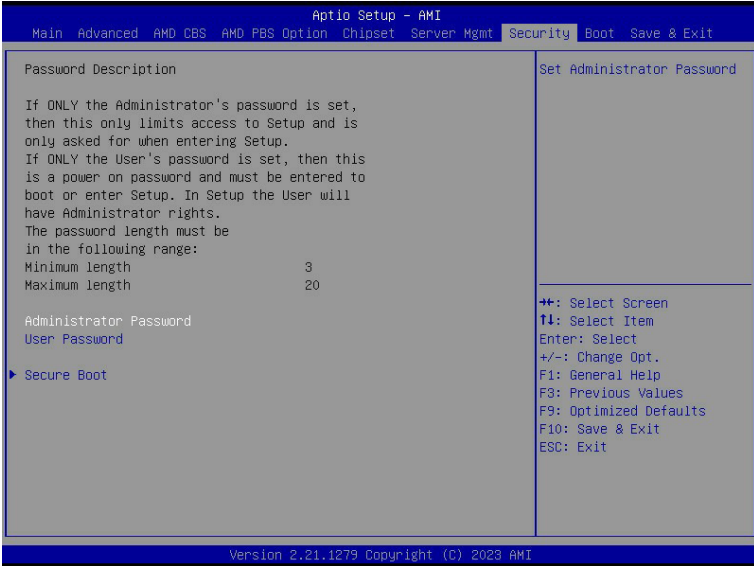
## 2-6-4 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is <b>Enable</b> .
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is <b>Dynamic-Obtained by BMC running DHCP</b> .
IPv6 BMC Lan IP Address/Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

## 2-7 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.



There are two types of passwords that you can set:

- Administrator Password  
Entering this password will allow the user to access and change all settings in the Setup Utility.
- User Password  
Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

## 2-7-1 Secure Boot

The Secure Boot submenu is applicable when your device is installed the Windows® 8 (or above) operating system.



Parameter	Description
System Mode	Displays if the system is in User mode or Setup mode.
Secure Boot	Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Secure Boot Mode <sup>(Note)</sup>	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before Windows loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys from the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is <b>Standard</b> .
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.
Reset To Setup Mode	Press [Enter] to reset the system mode to Setup mode.
Enter Audit Mode	Press [Enter] to set the system mode to audit mode.

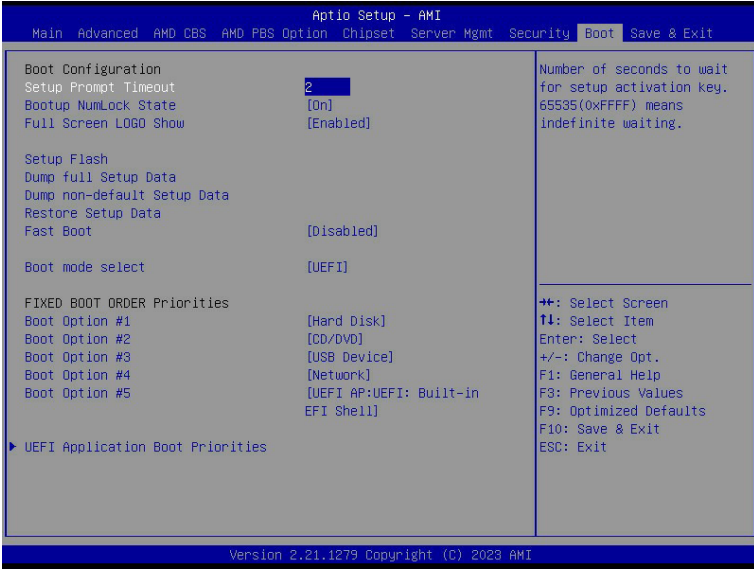
(Note) Advanced items prompt when this item is set to **Custom**.

Parameter	Description
Key Management	<p data-bbox="335 156 665 180">Press [Enter] to configure advanced items.</p> <p data-bbox="335 185 941 235"><b>Please note that this item is configurable when Secure Boot Mode is set to Custom.</b></p> <ul style="list-style-type: none"> <li data-bbox="335 243 946 352">◆ Factory Key Provision <ul style="list-style-type: none"> <li data-bbox="367 266 946 321">– Allows to provision factory default Secure Boot keys when system is in Setup Mode.</li> <li data-bbox="367 326 909 352">– Options available: Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> <li data-bbox="335 357 931 431">◆ Restore Factory Keys <ul style="list-style-type: none"> <li data-bbox="367 381 931 404">– Installs all factory default keys. It will force the system in User Mode.</li> <li data-bbox="367 409 611 431">– Options available: Yes, No.</li> </ul> </li> <li data-bbox="335 435 904 517">◆ Enroll Efi Image <ul style="list-style-type: none"> <li data-bbox="367 459 904 517">– Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db).</li> </ul> </li> <li data-bbox="335 522 899 572">◆ Secure Boot variable <ul style="list-style-type: none"> <li data-bbox="367 545 899 572">– Displays the current status of the variables used for secure boot.</li> </ul> </li> <li data-bbox="335 577 803 682">◆ Platform Key (PK) <ul style="list-style-type: none"> <li data-bbox="367 600 803 627">– Displays the current status of the Platform Key (PK).</li> <li data-bbox="367 631 680 655">– Press [Enter] to configure a new PK.</li> <li data-bbox="367 660 601 682">– Options available: Update.</li> </ul> </li> <li data-bbox="335 686 946 823">◆ Key Exchange Keys (KEK) <ul style="list-style-type: none"> <li data-bbox="367 710 946 736">– Displays the current status of the Key Exchange Key Database (KEK).</li> <li data-bbox="367 741 909 796">– Press [Enter] to configure a new KEK or load additional KEK from storage devices.</li> <li data-bbox="367 801 675 823">– Options available: Update, Append.</li> </ul> </li> <li data-bbox="335 827 952 964">◆ Authorized Signatures (DB) <ul style="list-style-type: none"> <li data-bbox="367 851 909 878">– Displays the current status of the Authorized Signature Database.</li> <li data-bbox="367 882 952 937">– Press [Enter] to configure a new DB or load additional DB from storage devices.</li> <li data-bbox="367 942 675 964">– Options available: Update, Append.</li> </ul> </li> <li data-bbox="335 969 904 1105">◆ Forbidden Signatures (DBX) <ul style="list-style-type: none"> <li data-bbox="367 992 904 1019">– Displays the current status of the Forbidden Signature Database.</li> <li data-bbox="367 1023 893 1078">– Press [Enter] to configure a new dbx or load additional dbx from storage devices.</li> <li data-bbox="367 1083 675 1105">– Options available: Update, Append.</li> </ul> </li> <li data-bbox="335 1110 931 1246">◆ Authorized TimeStamps (DBT) <ul style="list-style-type: none"> <li data-bbox="367 1133 931 1160">– Displays the current status of the Authorized TimeStamps Database.</li> <li data-bbox="367 1165 909 1219">– Press [Enter] to configure a new DBT or load additional DBT from storage devices.</li> <li data-bbox="367 1224 675 1246">– Options available: Update, Append.</li> </ul> </li> <li data-bbox="335 1251 920 1387">◆ OsRecovery Signatures <ul style="list-style-type: none"> <li data-bbox="367 1274 920 1301">– Displays the current status of the OsRecovery Signature Database.</li> <li data-bbox="367 1306 888 1361">– Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices.</li> <li data-bbox="367 1365 675 1387">– Options available: Update, Append.</li> </ul> </li> </ul>



## 2-8 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

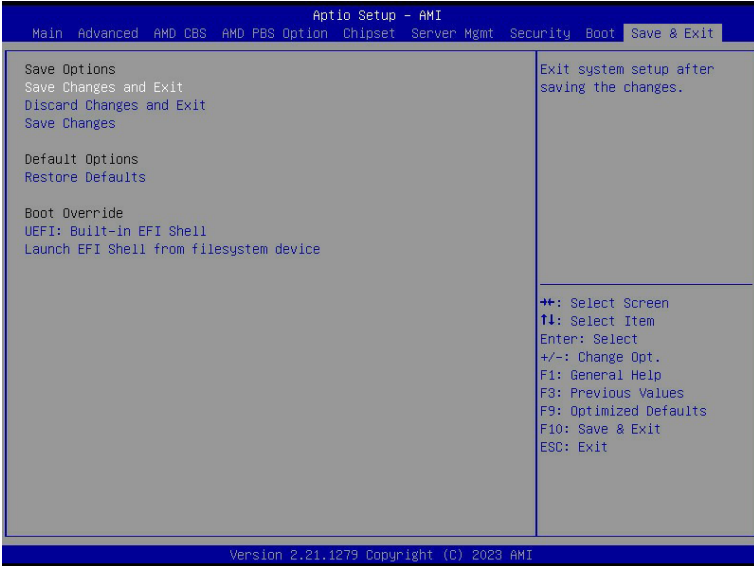


Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is <b>On</b> .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Setup Flash	Press [Enter] to run setup flash.
Dump full Setup Data	Press [Enter] to dump full setup data to file.
Dump non-default Setup Data	Press [Enter] to dump non-default setup data to file.
Restore Setup Data	Press [Enter] to restore setup data from file (cJson format).
Boot mode select	Selects the boot mode. Options available: LEGACY, UEFI. Default setting is <b>UEFI</b> .

Parameter	Description
FIXED BOOT ORDER Priorities	
Boot Option #1 / #2 / #3 / #4 / #5	<p data-bbox="402 200 732 224">Press [Enter] to configure the boot priority.</p> <p data-bbox="402 228 894 283">By default, the server searches for boot devices in the following sequence:</p> <ol data-bbox="439 288 639 426" style="list-style-type: none"> <li data-bbox="439 288 564 312">1. Hard drive.</li> <li data-bbox="439 316 639 340">2. CD-COM/DVD drive.</li> <li data-bbox="439 344 575 368">3. USB device.</li> <li data-bbox="439 373 549 396">4. Network.</li> <li data-bbox="439 401 522 424">5. UEFI.</li> </ol>
UEFI Network Drive BBS Priorities	Press [Enter] to configure the boot priority.
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.

## 2-9 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.



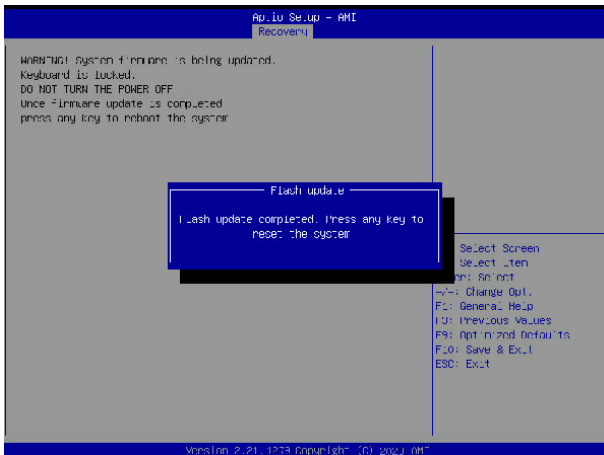
Parameter	Description
Save Options	
Save Changes and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard Changes and Exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Default Options	
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Boot Override	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell from filesystem device	Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices.

## 2-10 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

1. Copy the XXX.rom to USB diskette.
2. Setting BIOS Recovery jump to enabled status.
3. Boot into BIOS recovery.
4. Run Proceed with flash update.
5. BIOS updated.



## 2-11 BIOS POST Beep code (AMI standard)

### 2-11-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXE IPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

### 2-11-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met